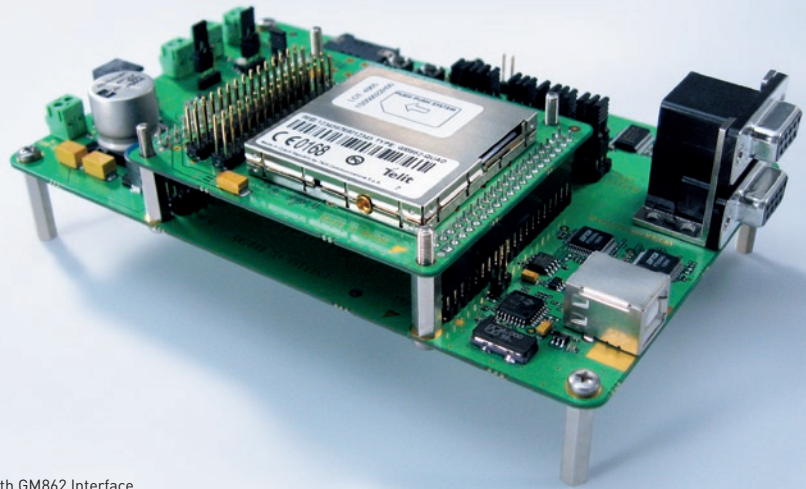


Evaluation Kit

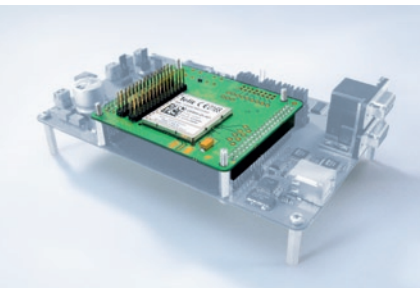
EVK2 for all Telit Modules



EVK2 with GM862 Interface



EVK2 with GE863 Interface



EVK2 with GE864 Interface

The Telit Evaluation Kit (EVK2) provides a robust, future-proof and flexible environment for rapid development of applications for the full range of GSM/GPRS, UMTS/HSDPA and CDMA module families, dramatically reducing time-to-market.

The kit is comprised of a motherboard, and an adapter board in which the relevant module is connected. This concept allows the EVK2 to be used across various form factors and product generations, both present and future.

The motherboard includes the basic interfaces, such as power input, SIM card holder, audio monitor outputs, RS-232, and USB 1.1, as well as a Reset button and power switch. The motherboard represents the recommended reference design for the module's peripheral circuitry. Jumper blocks on the motherboard define the routing of the serial interfaces, audio signals, and power supply. The PC or DTE controlling the module over the AT-command interface can be connected via USB or RS-232.

Adapter boards are available in different models to allow development for the desired module family (board-to-board or BGA solder). The set of basic interfaces found on by the motherboard. The set of interfaces specific to the module (e.g. antenna, general purpose inputs/outputs (GPIO), ADC/DAC, UART etc.) is found on the adapter board for connection to the user's application, extension boards, or other development tools and measuring equipment.

The Telit EVK2 is a tool for:

- Developing applications based on current and future Telit GSM/GPRS, UMTS/HSDPA, and CDMA module families via AT commands through serial ports
- Programming and updating of all Telit modules
- Debugging Telit-module-based applications
- Implementing simple applications (stand alone function) by executing scripts using a Python interpreter equipped module without the need for an external microprocessor



