Trenz Electronic GmbH operates as a provider of development services in the electronic industry since 1992. Our services include design-in support as well as turnkey designs which typically covers all steps from product specification, hardware and software design up to prototyping and production.

We are particularly specialized in the design of high-speed data acquisition, high-accuracy measurement and embedded digital signal processing systems based on FPGA and CPU architectures.

Many of our products are compatible with some widespread form factors. We also provide SoM products for Automotive industry and high-end applications.

In the event that an off-the-shelf FPGA board won’t fit the customers requirements, the design can be easily adapted by our comprehensive engineering design service.

Our in-house EMS and worldwide supply of FPGA and SoC modules complete the portfolio. All modules produced by Trenz Electronic GmbH are developed and manufactured in Germany.

Other assembly options of our modules for cost or performance optimization plus high volume prices are available on request. Also, cooling solutions and several carrier boards are available.

**Hardware Design**
- System architecture and design
- Hardware integration (Design-In)
- Ultrafast digital logic
- Analog and mixed signal
- Digital signal processing
- Schematic capture and PCB layout

**HDL Design**
- FPGA and System-On-Chip design
- System design and synthesis
- HDL design (VHDL, Verilog)
- Integration of soft-cores
- USB, PCI-Express, Gigabit Ethernet
- Ultrafast ADC/DAC interfaces

**Software Development**
- Device driver and application software development
- Software and Firmware development

**Extended device life cycle**
- Rugged for industrial applications
- Automotive grade available
- Small and powerful
- Customizable
- Development and design service
- Rapid Prototyping
- Cooling solutions
- Carrier and testboards
- Free documentation and designs
- Sales worldwide
- In-house EMS
- Developed & produced in Germany
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TE0803 Series
Xilinx Zynq UltraScale+, DDR4, Flash, 8 High Speed Serial Transceivers

Device list | Pin Package | Connectors | SDRAM max | Flash [MByte] | Total I/O | GBit Transceivers | Other Features
--- | --- | --- | --- | --- | --- | --- | ---
ZU2CG - ZU5CG, ZU2EG - ZU5EG, ZU4EV, ZU5EV | C784 | 4 x Samtec ST5 | 8192 MByte DDR4 | 128 | 156 + 65 MIO | 4 x PS GTR | GPU/VCU depending on model, EEPROM MAC address, Programmable Clock Generator, Single Supply

TE0807 Series
Xilinx Zynq UltraScale+, DDR4, Flash, 20 High Speed Serial Transceivers

Device list | Pin Package | Connectors | SDRAM max | Flash [MByte] | Total I/O | GBit Transceivers | Other Features
--- | --- | --- | --- | --- | --- | --- | ---
ZU4CG - ZU7CG, ZU4EG - ZU7EG, ZU4EV - ZU7EV | B900 | 4 x Samtec ST5 | 8192 MByte DDR4 | 128 | 204 + 65 MIO | 4 x GTR, 16 x GTH | GPU and VCU, Programmable Clock Generator, Single Supply
**TE0808 UltraSOM+ Series**

Xilinx Zynq UltraScale+, DDR4, Flash, 20 High Speed Serial Transceivers

http://trenz.org/te0808-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>Pin Package</th>
<th>Connectors</th>
<th>SDRAM max</th>
<th>Flash [MByte]</th>
<th>Total I/O</th>
<th>GBit Transceivers</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZU6CG, ZU9CG, ZU6EG, ZU9EG, ZU1SEG</td>
<td>C900</td>
<td>4 x Samtec ST5</td>
<td>8192 MByte</td>
<td>DDR4</td>
<td>128</td>
<td>204 + 65 MIO</td>
<td>4 x GTR, 16 x GTH</td>
</tr>
</tbody>
</table>

TE0820 Series

Xilinx Zynq UltraScale+, DDR4, Flash, USB, Ethernet, eMMC

http://trenz.org/te0820-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>Pin Packages</th>
<th>Connectors</th>
<th>SDRAM max</th>
<th>Flash [MByte]</th>
<th>USB PHY</th>
<th>Ethernet PHY</th>
<th>Total I/O</th>
<th>GBit Transceivers</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZU2CG - ZU5CG, ZU2EG - ZU5EG, ZU4EV, ZU5EV</td>
<td>784</td>
<td>3 x Samtec LSHM</td>
<td>4096 MByte</td>
<td>DDR4</td>
<td>128</td>
<td>4 - 64</td>
<td>1 GBit</td>
<td>USB2.0 OTG</td>
<td>132 + 14 MIO</td>
</tr>
</tbody>
</table>
TEB0911 UltraRack+ Board
Xilinx Zynq UltraScale+, 6 FMC Slots, Gigabit Ethernet

Key Features

The TEB0911 UltraRack+ board is integrating a Xilinx Zynq UltraScale+ MPSoC with 4 GByte Flash memory for configuration and operation, DDR4-SDRAM SO-DIMM socket with 64-bit wide data bus, 22 MGT lanes and powerful switch-mode power supplies for all on-board voltages. The TEB0911 board exposes the pins of the Zynq MPSoC to accessible connectors and provides a whole range of on-board components to test and evaluate the Zynq UltraScale+ MPSoC and for developing purposes. The board is capable to be fitted to a enclosure, whereby on the enclosure's rear and front panel, I/O's, LVDS-pairs and MGT lanes are accessible through 6 on-board FMC connectors and other standard high-speed interfaces, namely USB3, SFP+, SSD, GbE, etc.

- Zynq UltraScale+ MPSoC
  - 1156 Pin Package
  - Assembly options: ZU6, ZU9, ZU15
  - 64-bit DDR4 SODIMM (PS connected)
  - M2 PCIe SSD (1-Lane)
  - eMMC (bootable)
  - Dual QSPI Flash (bootable)
  - System controller (LCMIO2-7000HC)
    - Power sequencing
    - IO expander
  - Configurable PLLs
    - GTH/GTP reference CLks

Front Panel
- 4 x FMC
  - 4 GTH per FMC
  - 68 ZynqMP PL IO per FMC
- DisplayPort (2-lanes)
- RJ45 ETH + dual USB3 combo
- Dual Stack SFP+
- SD (bootable)
- Status LEDs

Back Panel
- 2 x FMC
  - 4/2 GTH
  - 12 ZynqMP PL IO per FMC
  - 56 SC IO
- USB JTAG/UART ZynqMP
- USB JTAG/GPIO FMC
- CAN FD (DB9 connector)
- SMA (external CLK)
- 5-pin 24V power connector

Additional information
- 406 mm x 234.30 mm board size
- Other assembly options for cost or performance optimization plus high volume prices available on request.

http://trenz.org/teb0911-info
The TEB0911 UltraRack+ board is integrating a Xilinx Zynq UltraScale+ MPSoC with 4 GByte Flash memory for configuration and operation, DDR4-SDRAM SO-DIMM socket with 64-bit wide data bus, 22 MGT lanes and powerful switch-mode power supplies for all on-board voltages. The TEB0911 board exposes the pins of the Zynq MPSoC to accessible connectors and provides a whole range of on-board components to test and evaluate the Zynq UltraScale+ MPSoC and for developing purposes. The board is capable to be fitted to an enclosure, whereby on the enclosure's rear and front panel, I/O's, LVDS-pairs and MGT lanes are accessible through 6 on-board FMC connectors and other standard high-speed interfaces, namely USB3, SFP+, SSD, GbE, etc.

### Zynq UltraScale+ MPSoC
- 1156 Pin Package
- Assembly options: ZU6, ZU9, ZU15

### 64-bit DDR4 SODIMM (PS connected)

### M2 PCIe SSD (1-Lane)

### eMMC (bootable)

### Dual QSPI Flash (bootable)

### System controller (LCMXO2-7000HC)
- Power sequencing
- IO expander
- Conﬁgurable PLLs
- GTH/GTP reference CLKs

### Front Panel
- 4 x FMC
- 4 GTH per FMC
- 68 ZynqMP PL IO per FMC
- DisplayPort (2-lanes)
- RJ45 ETH + dual USB3 combo
- Dual Stack SFP+
- SD (bootable)
- Status LEDs

### Back Panel
- 2 x FMC
- 4/2 GTH
- 12 ZynqMP PL IO per FMC
- 56 SC IO
- USB JTAG/UART ZynqMP
- USB JTAG/GPIO FMC
- CAN FD (DB9 connector)
- SMA (external CLK)
- 5-pin 24V power connector

### Additional information
- 406 mm x 234.30 mm board size
- Other assembly options for cost or performance optimization plus high volume prices available on request.

## TE0802 MPSOC Development Board
**Xilinx Zynq UltraScale+, LPDDR4, Flash, Ethernet, USB, Audio, Display**

http://trenz.org/te0802-info

### Device list
- **SDRAM max**
- **Flash [MByte]**
- **Ethernet RJ45**
- **USB**
- **User I/O**
- **Audio**
- **Other Features**

<table>
<thead>
<tr>
<th>Device</th>
<th>SDRAM max</th>
<th>Flash [MByte]</th>
<th>Ethernet RJ45</th>
<th>USB</th>
<th>User I/O</th>
<th>Audio</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZU2CG</td>
<td>1024 MByte</td>
<td>32</td>
<td>1 GBit</td>
<td>USB3.0 Host (type A connector)</td>
<td>2 Pmod connectors</td>
<td>3.5 mm jack (PWM output)</td>
<td>EEPROM, USB JTAG/IUART MicroUSB, Micro SD card, M2 PCIe SSD support, Display, Power: 5V Plug</td>
</tr>
</tbody>
</table>

## TE0890 S7 Mini
**Xilinx Spartan-7, Fully Open-Source Module with HyperRAM**

http://trenz.org/te890-info

### Device list
- **Footprint compatible**
- **Config PROM**
- **HyperRAM DRAM**
- **Total I/O**
- **Interface(s)**
- **Supply**
- **Other Features**

<table>
<thead>
<tr>
<th>Device</th>
<th>Footprint compatible</th>
<th>Config PROM</th>
<th>HyperRAM DRAM</th>
<th>Total I/O</th>
<th>Interface(s)</th>
<th>Supply</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>7S25</td>
<td>7S6, 7S15, 7S50 FTGB-196 devices</td>
<td>64 MBit</td>
<td>64 MBit</td>
<td>Dual-Pinout DIP-40 or 50mil 80 pin for 32 or 64 FPGA 3.3V I/Os</td>
<td>Standard 1x6 FTDI cable serial</td>
<td>5V input</td>
<td>23K Logic Cells, 29K Flips, 45 36Kb BRAMs, 80 mults., fully Open-Source</td>
</tr>
</tbody>
</table>
TE0745 Series
Xilinx Zynq-7000, DDR3, Flash, USB, Ethernet, 8 x GTX

Device list

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM max</th>
<th>Flash [MByte]</th>
<th>Ethernet PHY</th>
<th>USB PHY</th>
<th>Total I/O</th>
<th>GBit Transceivers</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z-7030, Z-7035, Z-7045</td>
<td>3 x Samtec ST5</td>
<td>1024 MByte DDR3</td>
<td>64</td>
<td>1 GBit</td>
<td>USB 2.0 OTG</td>
<td>250 + 6 MIO</td>
<td>8 x GTX</td>
<td>Real Time Clock, Single Supply</td>
</tr>
</tbody>
</table>

http://trenz.org/te0745-info

TE0729 Series
Xilinx Zynq-7000, DDR3, Flash, 3 x Ethernet, 3 x EEPROM, USB, eMMC

Device list

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM max</th>
<th>Flash [MByte]</th>
<th>e.MMC</th>
<th>Ethernet PHY</th>
<th>USB PHY</th>
<th>EEPROM</th>
<th>Total I/O</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z-7020</td>
<td>3 x Samtec LSHM</td>
<td>512 MByte DDR3</td>
<td>32</td>
<td>4 - 64 GByte</td>
<td>2 x 100 MBit, 1 GBit</td>
<td>USB 2.0 OTG</td>
<td>2 x MAC address</td>
<td>136 + 14 MIO</td>
<td>Real Time Clock, Single Supply</td>
</tr>
</tbody>
</table>

http://trenz.org/te0729-info
**TE0715 Series**

Xilinx Zynq-7000, DDR3, Flash, Ethernet, USB, 4 High Speed Serial Transceivers

http://trenz.org/te0715-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM max.</th>
<th>Flash [MByte]</th>
<th>Ethernet PHY</th>
<th>USB PHY</th>
<th>Total I/O</th>
<th>GBit Transceivers</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z-7015, Z-7030, Z-7012S</td>
<td>3 x Samtec LSHM</td>
<td>1024 MByte DDR3</td>
<td>32</td>
<td>1 GBit</td>
<td>USB 2.0</td>
<td>132 + 14 MIO</td>
<td>Z-7015: 4 x GTP Z-7030: 4 x GTX</td>
<td>Programmable Clock Generator, Real Time Clock, Single Supply</td>
</tr>
</tbody>
</table>

**TE0720 GigaZee Series**

Xilinx Zynq-7000, DDR3, Flash, Ethernet, USB, eMMC, Automotive Grade available

http://trenz.org/te0720-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM max.</th>
<th>Flash [MByte]</th>
<th>eMMC max.</th>
<th>Ethernet PHY</th>
<th>USB PHY</th>
<th>Total I/O</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z-7020, Z-7014S, XA7Z020-1CLG484Q</td>
<td>3 x Samtec LSHM</td>
<td>1024 MByte DDR3</td>
<td>32</td>
<td>32 GByte</td>
<td>1 GBit</td>
<td>USB 2.0</td>
<td>152 + 14 MIO</td>
<td>Real Time Clock, MAC Address, 2k serial EEPROM, 3 User LEDs, Single Supply</td>
</tr>
</tbody>
</table>
**TE0728 Series**  
Xilinx Zynq-7000, DDR3, Flash, 2 x Ethernet, CAN, Automotive

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM max</th>
<th>Flash [MByte]</th>
<th>EEPROM</th>
<th>Ethernet PHY</th>
<th>Total I/O</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>XA7Z020 (automotive)</td>
<td>3 x Samtec QTH</td>
<td>512 MByte DDR3</td>
<td>16</td>
<td>8 KByte</td>
<td>2 x 100 MBit</td>
<td>124 + 30 MIO</td>
<td>Automotive, Real Time Clock, CAN, Single Supply</td>
</tr>
</tbody>
</table>

**TE0724 Series**  
Xilinx Zynq-7000, DDR3L, Flash, Ethernet, EEPROM, CAN

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM max</th>
<th>Flash [MByte]</th>
<th>EEPROM</th>
<th>Ethernet PHY</th>
<th>Total I/O</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z-7010, Z-7020</td>
<td>1 x Samtec ST5</td>
<td>1024 MByte DDR3L</td>
<td>32</td>
<td>MAC Address</td>
<td>1 GBit</td>
<td>PL: 80  PS: 20</td>
<td>CAN, Single Supply</td>
</tr>
</tbody>
</table>

http://trenz.org/te0728-info

http://trenz.org/te0724-info
### TE0728 Series

**Xilinx Zynq-7000, DDR3, Flash, 2 x GBit Ethernet, 2 x USB, eMMC, 16 x Transceivers**

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM max</th>
<th>Flash [MByte]</th>
<th>eMMC</th>
<th>Ethernet PHY</th>
<th>USB PHY</th>
<th>Total I/O</th>
<th>GBit Transceivers</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z-7035, Z-7045, Z-7100</td>
<td>3 x Samtec QTH DDR3</td>
<td>1024 MByte</td>
<td>32</td>
<td>-</td>
<td>2 x 1 GBit</td>
<td>2 x USB 2.0 OTG</td>
<td>230 + 2 MIO</td>
<td>16 x GTX</td>
<td>Programmable Clock Generator, Real Time Clock, Single Supply</td>
</tr>
</tbody>
</table>

### TE0782 Series

**Xilinx Zynq-7000, DDR3, Flash, 2 x GBit Ethernet, 2 x USB, eMMC, 16 x Transceivers**

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM max</th>
<th>Flash [MByte]</th>
<th>eMMC</th>
<th>Ethernet PHY</th>
<th>USB PHY</th>
<th>Total I/O</th>
<th>GBit Transceivers</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z-7035, Z-7045, Z-7100</td>
<td>3 x Samtec QTH DDR3</td>
<td>1024 MByte</td>
<td>32</td>
<td>-</td>
<td>2 x 1 GBit</td>
<td>2 x USB 2.0 OTG</td>
<td>230 + 2 MIO</td>
<td>16 x GTX</td>
<td>Programmable Clock Generator, Real Time Clock, Single Supply</td>
</tr>
</tbody>
</table>

### TE0783 Series

**Xilinx Zynq-7000, Memory on both PS and PL, Flash, Ethernet, USB, eMMC**

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM max</th>
<th>Flash [MByte]</th>
<th>eMMC</th>
<th>Ethernet PHY</th>
<th>Total I/O</th>
<th>GBit Transceivers</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z-7035, Z-7045, Z-7100</td>
<td>3 x Samtec QTH DDR3</td>
<td>1024 MByte</td>
<td>32</td>
<td>-</td>
<td>2 x 1 GBit</td>
<td>2 x USB 2.0 OTG</td>
<td>230 + 2 MIO</td>
<td>16 x GTX</td>
</tr>
</tbody>
</table>
### TE0722 DIPFORTy1 "Soft Propeller" Series

*Xilinx Zynq-7000, Flash, fits on DIP40 Pinout, Parallax Propeller Chip compatibility*

<table>
<thead>
<tr>
<th>Device list</th>
<th>Flash [MByte]</th>
<th>Total I/O</th>
<th>DIP40 form factor</th>
<th>Clock</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z-7010, Z-7007S</td>
<td>16</td>
<td>46</td>
<td>2 x 20 holes for socket pins or pin-header</td>
<td>33.333 MHz (MEMS Oscillator)</td>
<td>3.3V Single Supply, Micro SD card socket, proximity and ambient light sensor, industrial temperature range available</td>
</tr>
</tbody>
</table>

![ZYNQ](http://trenz.org/te0722-info)

### TE0723 "ArduZynq" Series

*Xilinx Zynq-7000, DDR3L, Flash, USB OTG, On-board USB JTAG and UART*

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM max</th>
<th>Flash [MByte]</th>
<th>USB PHY</th>
<th>Total I/O</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Z-7010, Z-7007S</td>
<td>Arduino Pmod headers</td>
<td>512 MByte DDR3L</td>
<td>16</td>
<td>Micro USB OTG, Micro USB, FT2232, JTAG/JTAG/FIFO</td>
<td>30</td>
<td>Micro SD, on-board USB JTAG and UART</td>
</tr>
</tbody>
</table>

![ZYNQ](http://trenz.org/te0723-info)
TE0726 "ZynqBerry" Series
Xilinx Zynq-7000, DDR3L, Flash, 100 MBit Ethernet RJ45, USB, HDMI

Device list | Connectors | SDRAM max | Flash [MByte] | Ethernet PHY | USB PHY | Total I/O | Other Features
---|---|---|---|---|---|---|---
Z-7010, Z-7007S | 40-pin "HAT" headers | 512 MByte DDR3L | 16 | 100 MBit | 4 x USB 2.0 Host | 26 | Form factor like a Raspberry Pi 2 Board, DSI display connector, CSI-2 camera connector, Micro SD card slot, 3.5 mm audio plug, HDMI type A

http://trenz.org/te0726-info

TE0876 IceZero Series
Lattice ICE40HX, Raspberry Pi HAT compatible, SRAM, Flash, Open-Source

Device list | Connectors | SDRAM max | Flash [MByte] | Other Features
---|---|---|---|---
Lattice ICE40 | 4 2x6-pin Pmod connectors (no default) | 4 MBit external SRAM | 8 | 100 MHz User Clock, 3 User LED, supported by fully open source FPGA toolchain, fast FPGA configuration from Raspberry Pi, full FPGA design flow on Raspberry Pi (all Open Source)

http://trenz.org/icezero-info
TE0841 Series
Xilinx Kintex UltraScale, DDR4, Flash, 8 x GTH Transceiver Lanes

http://trenz.org/te0841-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM [MByte] max</th>
<th>Flash [MByte]</th>
<th>Total I/O</th>
<th>GBit Transceivers</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>KU035, KU040</td>
<td>3 x Samtec LSHM</td>
<td>4096 DDR4</td>
<td>64</td>
<td>60 x HR I/Os</td>
<td>84 x HP I/Os</td>
<td>8 x GTH, Programmable Clock Generator, Single Supply</td>
</tr>
</tbody>
</table>

TE0741 Series
Xilinx Kintex-7, Flash, 8 High Speed Serial Transceivers, 25 MHz Oscillator

http://trenz.org/te0741-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>Flash [MByte]</th>
<th>Total I/O</th>
<th>GBit Transceivers</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>70T, 160T, 325T, 410T</td>
<td>3 x Samtec LSHM</td>
<td>32</td>
<td>144 (94 for 70T variant)</td>
<td>8 x MGTs</td>
<td>Programmable Clock Generator, Single Supply</td>
</tr>
</tbody>
</table>
**TE0710 Series**
Xilinx Artix-7, DDR3, Flash, 2 x 100 MBit Ethernet, EEPROM

**Device list**
- 3T, 50T, 75T, 100T

**Connectors**
- 2 x Samtec LSHM

**SDRAM max**
- 512 MByte DDR3

**Flash [MByte]**
- 32

**Ethernet PHY**
- 2 x 100 MBit

**Total I/O**
- 112 (51 differential pairs + 10 single-ended)

**Other Features**
- JTAG, 100 MHz MEMS oscillator, User LED, Single Supply

---

**TE0711 Series**
Xilinx Artix-7, Flash, USB, FTDI USB to UART/FIFO bridge, high pin count

**Device list**
- 3T, 50T, 75T, 100T

**Connectors**
- 3 x Samtec LSHM

**Flash [MByte]**
- 32

**MEMS Oscillator**
- 100 MHz

**USB PHY**
- USB 2.0 UART/FIFO

**Total I/O**
- 178 (84 differential pairs)

**Other Features**
- Four LEDs, Single Supply
### TE0712 Series
**Xilinx Artix-7, DDR3, Flash, 100 MBit Ethernet, 4 x GTP Transceiver**

**Device list**
- 35T, 50T, 75T, 100T, 200T

**Connectors**
- 3 x Samtec LSHM

**SDRAM [MByte] max**
- 1024 DDR3

**Flash [MByte]**
- 32

**EEPROM**
- MAC Address

**Ethernet PHY**
- 100 MBit

**Total I/O**
- 158

**GBit Transceivers**
- 4 x GTP

**Other Features**
- Programmable Clock Generator, Single Supply

### TE0713 Series
**Xilinx Artix-7, DDR3L, Flash, USB 3.0 to FIFO Bridge, 4 x GTP Transceiver**

**Device list**
- 15T - 200T

**Connectors**
- 3 x Samtec LSHM

**SDRAM [MByte] max**
- 1024 DDR3L

**Flash [MByte]**
- 32

**USB PHY**
- USB 3.0

**Total I/O**
- 152

**GBit Transceivers**
- 4 x GTP

**Other Features**
- Programmable Clock Generator, Single Supply
TE0714 Series
Xilinx Artix-7, Flash, 4 x GTP Transceiver, Form Factor 3 x 4 cm only

Device list | Connectors | Flash [MByte] | Total I/O | GBit Transceivers | Other Features
---|---|---|---|---|---
15T, 35T, 50T | 2 x Samtec LSHM | 16 | 138 + 5 (QSPI or user I/Os) | 4 x GTP | Differential MEMS oscillator for MGT clocking, XADC Analog Input, eFUSE bit-stream encryption (AES), Single Supply

http://trenz.org/te0714-info

TE0726 "ZynqBerry" Bundle

Package Content
- 1 x "ZynqBerry" TE0726 with Xilinx Zynq-7010, 512 MByte DDR3L, 16 MByte Flash, 4 x USB, 100 MBit Ethernet RJ45
- 1 x Xilinx SDSoC Development Voucher

Available in our online shop.
TE0725 Series
Xilinx Artix-7, Flash, HyperRAM, 2 x 50-pin Headers, 2.54 mm Pitch

ARTIX

http://trenz.org/te0725-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>RAM [MByte] max</th>
<th>Flash [MByte]</th>
<th>EEPROM</th>
<th>Total I/O</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>15T, 35T, 50T, 75T, 100T</td>
<td>2 x 50-pin headers</td>
<td>8 HyperRAM</td>
<td>32</td>
<td>16 KByte</td>
<td>87</td>
<td>Optional POF Fiber Optical Adapter (125/250 MBit/s), Single Supply</td>
</tr>
</tbody>
</table>

TE0725LP Series
Xilinx Artix-7, Flash, 3.3V or optional 1.8V VIN, 2 x 50-pin Headers, 2.54 mm Pitch

ARTIX

http://trenz.org/te0725LP-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>RAM [MByte] max</th>
<th>Flash [MByte]</th>
<th>EEPROM</th>
<th>Vin</th>
<th>Total I/O</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>100T</td>
<td>2 x 50-pin headers</td>
<td>optional HyperRAM or HyperFlash (8-32 MByte)</td>
<td>32</td>
<td>16 KByte</td>
<td>3.3V or 1.8V depending on variant</td>
<td>95</td>
<td>JTAG/UART Connector, 25 MHz System Clock (up to 100 MHz on request), Single Supply</td>
</tr>
</tbody>
</table>
TEC0850 CompactPCI Serial Card
Xilinx Zynq UltraScale+, 3U Form Factor, DDR4 SODIMM, Flash, Ethernet

http://trenz.org/tec0850-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>Form Factor</th>
<th>DDR4 SODIMM [GByte]</th>
<th>Flash [MByte]</th>
<th>USB</th>
<th>Total I/O</th>
<th>Ethernet</th>
<th>GBit Transceivers</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>ZU15EG, 1156 Pin Packages</td>
<td>3U</td>
<td>8 (32 max.)</td>
<td>512 max.</td>
<td>3.0</td>
<td>32 x differential pairs</td>
<td>1 Gigabit</td>
<td>24 on PL side 4 on PS side</td>
<td>JTAG/JUART via MicroUSB, 2 x EEPROM, Real Time Clock, Zynq MPSoC Cooling FAN Connector</td>
</tr>
</tbody>
</table>

TEC0871 RF SoC
Xilinx Zynq UltraScale+ RF SoC, DDR4, Flash, Ethernet, USB, EEPROM

http://trenz.org/tec0871-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>Pin Packages</th>
<th>Connectors</th>
<th>SDRAM [GByte DDR4]</th>
<th>Flash [MByte]</th>
<th>Ethernet PHY</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>XC2U28DR-2FFVE1156C</td>
<td>1156</td>
<td>2 x Samtec ST5</td>
<td>4</td>
<td>128</td>
<td>1 Gigabit</td>
<td>High Speed USB2 Transceiver OTG, MAC Address Serial EEPROM, RTC</td>
</tr>
</tbody>
</table>
### Device list

<table>
<thead>
<tr>
<th>Device list</th>
<th>Flash [MByte]</th>
<th>SDRAM</th>
<th>Total I/O</th>
<th>GBit Transceivers</th>
<th>GBit Transceivers Transmission Rate</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>XC7VX330T</td>
<td>32</td>
<td>DDR3 SO-DIMM Socket</td>
<td>up to 202 FPGA I/O pins on FMC connector</td>
<td>10 on FMC 8 on PCIe lanes</td>
<td>13.1 GBit/s</td>
<td>FMC High Pin Count (HPC) Connector, Programmable Clock Generator</td>
</tr>
</tbody>
</table>

### TEC0330 PCIe FMC Carrier

**Xilinx Virtex-7, FMC HPC, 8 lane PCIe GEN2 card, DDR3 SODIMM Socket**

http://trenz.org/tec0330-info

### TEF1001 PCIe FMC Carrier

**Xilinx Kintex-7, FMC HPC, 4 lane PCIe GEN2 card, DDR3 SODIMM Socket**

http://trenz.org/tef1001-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>Flash [MByte]</th>
<th>SDRAM</th>
<th>Total I/O</th>
<th>GBit Transceivers</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>XC7K160T</td>
<td>32</td>
<td>DDR3 SO-DIMM Socket</td>
<td>160 on FMC connector</td>
<td>4 on FMC 4 on PCIe lanes</td>
<td>VITA 57.1 FMC HPC Slot, Programmable Clock Generator, 200 MHz Low-Jitter LVDS Oscillator</td>
</tr>
</tbody>
</table>
**TEF0007 Series**
FMC Card with DisplayPort input and output

![Kintex](http://trenz.org/tec0330-info)

**Device list**
- **Connector(s):** HPC FMC
- **Flash [MByte]:** 32
- **Total I/O GBit:** 34 differential (68 single ended)
- **Transceiver(s):** 4 x GTP
- **Other Features:** Data Rates up to 5.4 Gbps, Sink + Source DP Connector, 50 MHz Oscillator, Configurable PLL

---

**TEF0008 Series**
FMC Card with four SFP+ 10 GBit Ports based on VITA 57.1 FMC HPC Standard

![Intel MAX 10](http://trenz.org/tef0008-info)

**Device list**
- **Connector(s):** HPC FMC
- **Dimension:** 69 x 84 mm, SFP + connector excluded (+ 5.5 mm)
- **SFP+:** 4 SFP+ 10 GBit ports for fiber optical SFP modules
- **Other Features:** Low-Jitter Programmable Clock Generator, 3.3V to 1.8V DCDC Converter, 128 KBit EEPROM, Status LED (green)
EDDP Motor Control Kit

The EDDP Kit enables rapid, simplified development and evaluation of three-phase motor control applications by providing software, documentation, binary images, editable source code to run on a Xilinx Zynq®-7000 All Programmable SoC along with associated hardware. For the first time ever, the highly parallel and deterministic benefits of FPGA-based motor control, offering up to 30-40x more responsiveness than traditional embedded approaches, is available in a C/C++ development environment. Furthermore, scalability with minimal CPU burden is increasingly differentiating for developers of such systems given the industry rise in demand for multi-axis motion control.

The three main hardware components included in the EDDP Kit are the development board, TEC0053, from Trenz Electronic as the motor driver board, the Arty Z7-10 from Digilent Inc. as the reference controller board, and a three-phase permanent magnet synchronous motor from Anaheim Automation as the reference motor. The main software components are the field oriented motor control algorithm implemented with the Xilinx Vivado® Design Suite and the Web UI. To edit the included design or replace with proprietary C++ code, users must have access to either a fully licensed seat of Vivado HLx Edition or the no-charge WebPACK Edition. Also required is the SDSoc™ tool, part of the SDx™ Development Environment, available for purchase or no cost evaluation from Xilinx. All other resources are available for free download from http://trenz.org/EDDP/.

**Key Features**

- Development and evaluation of three-phase motor control applications
- Speed and flexibility provided by FPGA-fabric in Xilinx Zynq®-7000 All Programmable SoC
- Implementation of a Field Oriented Control Algorithm with Vivado® SDSoc™, offloading from processor to embedded
- Available motor control modes consist of speed control and stator current control
- Internet connectivity provided by the Linux operating system running on an ARM processor
- Web UI and Network API for the control of the motor over internet
- Runs on 12V DC power
- Optionally, the power stage can be run from a separate 5V…48V DC power supply

Other assembly options for cost or performance optimization plus high volume prices available on request.

**Resources**

trenz.org/EDDP/ - including a Quick Start Guide, an User Manual for the EDDP Kit and the EDPS motor driver board, block diagram, design database and technical specifications

**Support**

A support forum especially for this product is accessible at http://trenz.org/EDDPsupport.
Trenz Electronic Development Boards

DesignWare® ARC® EM Software Development Platform

The DesignWare® ARC® EM Software Development Platform is a flexible platform for rapid software development on ARC EM processors and subsystems. It is intended to accelerate software development and debug of ARC EM processor-based systems for a wide range of ultra-low power embedded applications such as IoT, sensor fusion, and voice applications. It includes an FPGA-based hardware board with commonly used peripherals and interfaces for extensibility. Downloadable platform packages containing different hardware configurations enable the board to be programmed with different ARC EM processors and subsystems. The packages also contain the necessary software configuration information for the toolchain and embARC Open Software Platform.

The development platform is supported by Synopsys’ MetaWare Development Tool Kit, which includes a compiler, debugger and libraries optimized for maximum performance with minimal code size. The embARC Open Software Platform (OSP), available online from embarc.org, gives developers online access to device drivers, FreeRTOS, middleware and examples that enables them to quickly start software development for their ARC-based embedded systems.

Each hardware configuration includes an ARC EM processor and subsystem with access to 16MB of PSRAM, 16MB of SPI Flash and a wide range of peripherals such Audio Line In/Out, UART, SPI, I2C, and ADC. An on-board module providing Wi-Fi/Bluetooth functionality and a 9-D motion sensor enable fast development of IoT applications. Two digital MEMs microphones can also be used for the development of voice applications. The hardware is extensible using the popular Arduino® interface and extension is also possible with Digilent® Pmod™ Interfaces, mikroBUS™ headers and a 50-pin header. Debug and trace are handled with USB/JTAG interfaces and a NEXUS interface for ARC Real-Time Trace (RTT). The board includes a micro-SD card slot for loading application software.

Key Features

- Xilinx Kintex-7 XC7K325T-2FBG676C
- 32 MByte Quad-SPI Flash memory (for configuration and operation)
- USB-JTAG bridge FT2232H
- FPGA configuration through Jtag and SPI Flash memory
- SPI Flash configuration through JTAG and USB
- Connectors
  - Arduino compatible pin headers
  - MicroBUS compatible pin headers
  - 2 x 3.5mm RCA audio jacks (input/output)
  - 2 x PDM Microphones SPK0641HT4H-1
  - 32 MByte User Quad-SPI Flash memory
  - 3-axis gyroscope, 3-axis accelerometer, 3-axis magnetometer ICM-20948
  - Wireless module RS9113-NBZ-D1C (Wi-Fi/BT/BLE)
  - Stereo Audio Codec MAX9880A
  - 100MHz User Clock Oscillator SiT8008
  - Status LEDs, Power LED
  - 12V Power Supply (separately included in the scope of delivery)
  - 12V Fan

http://trenz-electronic.de/DesignWare_ARC_EM
The carrier boards are base-boards for 4 x 5 SoMs, which exposes the modules B2B-connector-pins to accessible connectors and provides a
whole range of on-board components to test and evaluate Trenz Electronic 4 x 5 SoMs.

**TE0701**
- Overvoltage-, undervoltage- and reversed- supply-voltage-protection
- Barrel jack for 12 V power supply
- Carrier Board System-Controller CPLD
- Mini CameraLink connector
- RJ45 Gigabit Ethernet MagJack
- FPGA Mezzanine Card (FMC-LPC) connector
- USB JTAG- and UART interface with Mini-USB connector
- HDMI transmitter with HDMI connector
- 8 x user LEDs, 2 x user push buttons, 2 x DIP switch
- Pmod connectors, Micro SD card socket and Micro-USB interface

**TE0703**
- 2 x VG96 connectors (mounting holes and solder pads)
- SDIO port expander with voltage-level translation
- Micro SD card socket
- 4 x user LEDs, 1 x user-push button, 2 x user configurable DIP switches
- Mini USB connector (USB JTAG and UART interface)
- RJ45 Gigabit Ethernet socket with 4 integrated LED’s.
- USB host connector
- Barrel jack for 5 V power supply input
- DC-DC step-down converter for 3.3 V power supply
- USB JTAG and UART interface

**TE0705**
TE0705 is a “downgraded” version of TE0701. As little as possible has been changed in functionality except the functionality that was removed.

Changes from TE0701
- Pmod connectors changed to IDC headers
- HDMI removed
- CL connector removed
- USB connector position changed
- 5 pin header support added on both USB interfaces
- 12 V DC power input connector changed to different type
- FMC connector removed and replaced by two dual row 100 mil pin headers

**TE0706**
- VG96 connector and 50-pin IDC male connector socket
- SDIO port expander with voltage-level translation
- Micro SD card socket and a USB type A connector
- 1 x user push button, user configurable DIP switch
- 2 x RJ45 Gigabit Ethernet MagJack
- 1 x Ethernet PHY
- Barrel jack for 5 V power supply input
- DC-DC step-down converter for 3.3 V power supply
- JTAG pins on 12-pin header
- 3 x VCCIO selection jumper

**TEBA0841**
Mainly for the use with TE0841 and TE0741 modules.
- XMOD (TE0790) pin header
- SFP connector
- Micro USB
- 1 x pin header 16 pol. (JTAG, MGT-CLK, boot mode, RST, IOs)
- 1 x pin header 10 pol. (SD IOs)
- 2 x pin headers 50 pol. (FPGA bank IOs and power)
- 1 x pin header for FPGA bank power VCCIOA and 1 x for VCCIOD
- LDO voltage regulator 3.3 V to 2.5 V
- 2 x user LEDs (Red/Green)
Trenz Electronic Carrier Boards
for modules with different form factors

Following carrier boards are base boards for specific Trenz Electronic SoMs, which exposes the module's B2B-connector-pins to accessible connectors and provides a whole range of on-board components to test and evaluate Trenz Electronic SoMs.

**TEBF0808**
- Mini-ITX form factor
- ATX power supply connector (12 V only supply required)
- optional 12 V standard power plug
- USB 3.0 with USB 3.0 HUB
- FMC HPC slot (1.8 V max VCCIO)
- MicroSD Card (bootable) and eMMC (bootable)
- PCIe slot - one PCIe lane (16 Lane connector)
- Fan connectors, PC enclosure, FMC fan
- Intel front panel- and HDA audio-connector
- CAN FD transceiver (10 pin IDC connector)
- Displayport Single Lane
- One SATA Connector
- Dual SFP+
- Gigabit Ethernet RJ45
- One Samtec FireFly (4 GT lanes bidir.)
- One Samtec FireFly connector for reverse loopback
- 20 pins ARM JTAG connector (FS JTAG0)

**TEB0728**
- Trenz TE0728 module socket (3 x Samtec SEM connectors 80 pins)
- 2 x RJ45 Ethernet
- SD card slot
- Power supply with DC jack
- 3 x user LEDs (red/yellow/green)
- User push button

**TEB0729**
- Trenz TE0729 module socket (2 x Samtec BTE/BSE connectors 120 pins)
- 5 V board supply via DC jack
- 3 x RJ45 Ethernet
- 1 x MicroUSB and 1 x SD card connector
- 1 x 128K I2C CMOS Serial EEPROM
- 1 x 2K I2C Serial EEPROM
- XMOD (TE0790) pin header
- 2 x pin header FPGA bank power supply
- 1 x VBat pin header and 2 x VG96 pin header
- 1 x user push button, 1 x LED (red), user switch FPGA boot mode

**TEB0745**
- Trenz TE0745 module socket (3 x Samtec STS connectors 160 pins)
- 24 V power supply over ARK2950/2 connecting terminal
- XMOD (TE0790) Pin Header (JTAG / UART)
- 1 x EMI Network Filter
- microSD connector
- RJ45 Ethernet connector
- USB Host connector
- 8 x SFP connector
- 6 x pin header 50 pol. (FPGA bank I/O and power)
- 6 x pin header 12 pol. (FPGA bank I/O and power)

**TEBA0714**
- Trenz TE0714 module socket (2 x Samtec LSHM connectors 100 pins)
- XMOD (TE0790) pin header
- 1 x pin header 16 pol. (JTAG, MGT-CLK, boot mode, XADC, I/O's)
- 1 x pin header 10 pol. (I/O)
- SFP connector
- LDO voltage regulator 3.3 V to 2.5 V
- 2 x user LEDs (red/green) and 1 x LED (red)
- 2 x pin headers 50 pol. (FPGA bank I/O and power)
- 1 x pin header for FPGA bank power VCCIO34 (1.8 VOUT, 2.5 V, 3.3 VOUT)
- 1 x pin header for FPGA bank power V_CFG (1.8 VOUT, 2.5 V, 3.3 VOUT)

http://trenz.org/te-baseboards
MKR Compatible Footprint
Arduino MKR Standard 2.5 x 6.15 cm

Resources http://trenz.org/max1000-info

**MAX1000** IoT/Maker board, TEI0001 series, Intel MAX 10 10M08SAU169C8G or 10M16SAU169C8G) FPGA, 8/16 kLE, 8 - 32 MByte SDRAM (max. 64 MByte), 8 MByte Flash, USB-Programmer on-board, JTAG and UART over Micro USB2 connector, ADC 8 x 12 Bit, 12 MHz oscillator, optional MEMS oscillator, optional Pmod headers, supply USB/pins, 2 switches, 8 configurable and 2 status LEDs, power can be supplied as 5V from the USB port or via a separate pin

Resources http://trenz.org/cyc1000-info

**CYC1000**, TEI0003 series, Intel Cyclone 10CL025YU256 C8G FPGA, 25 kLE, optional 10CL006, 10CL010, 10CL016, 8 MByte SDRAM, 2 MByte Flash, 21 I/O Arduino MKR compatible headers, JTAG and UART over Micro USB2 connector, LIS3DH 3-axis accelero-meter, 2 x 14-pin headers providing 23 GPIOs, 1 x 3-pin header providing 2 GPIOs, Pmod: 2 x 6-pin support, 8 configurable and 2 status LEDs, user push button, 5.0V single power supply with on-board voltage regulators

Resources http://trenz.org/smf2000-info

**SMF2000**, TEM0001 series, Microsemi SmartFusion2 M2S010-VFG400 FPGA, 8 MByte SDRAM, 8 MByte Flash, 25 MHz system clock and 32.768 KHz auxiliary clock, JTAG and UART over Micro USB2 connector, 1 x 3-pin header for Live Probes, 1 x Pmod header providing 8 I/O, 2 x 14-pin header (2.54 mm pitch) providing 23 I/O, 9 User LEDs, 1 user push button

Resources http://trenz.org/tei0009-info

**LXO2000**, TEL0001 series, Lattice X02-4000 FPGA, 22 I/O on MKR header, 2 I/O on additional header, optional Pmod header (+ 8 I/O), on-board USB/JTAG, on-board USB/serial, 100 MHz MEMS clock oscillator, 2 push buttons, 8 LEDs, supply: USB or 5V from pin header; RC-networks
TEI0006 Series
Intel Cyclone 10 GX SoM, DDR3, Flash, Ethernet, Baseboard available

![Image of Intel Cyclone 10 GX FPGA](http://trenz.org/tei0006-info)

<table>
<thead>
<tr>
<th>Device list</th>
<th>Connectors</th>
<th>SDRAM [GByte]</th>
<th>Flash [MByte]</th>
<th>Ethernet</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cyclone 10 GX 10CX220YF78015G</td>
<td>3 x Samtec ST5</td>
<td>2 DDR3</td>
<td>256</td>
<td>1 Gbit</td>
<td>Intel MAX 10 as Power Sequencer, EEPROM, 4 LEDs, 5V Input Voltage</td>
</tr>
</tbody>
</table>

TEI0009 C10LP RefKit Development Board
Intel Cyclone 10 LP, Integrated USB Programmer2, 2 x 10/100 Ethernet, USB2.0

![Image of Intel Cyclone 10 LP FPGA](http://trenz.org/tei0009-info)

<table>
<thead>
<tr>
<th></th>
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<th></th>
<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td>Cyclone 10 LP 10CL055YU484C8G</td>
<td>QSE (for LVDS), Pmod, Arduino, SMA</td>
<td>Up to 512</td>
<td>Up to 512</td>
<td>Up to 128</td>
<td>Up to 32</td>
<td>2 x 10/100</td>
<td>2.0</td>
<td>Integrated USB Programmer2</td>
</tr>
</tbody>
</table>

http://trenz.org/tei0006-info

http://trenz.org/tei0009-info
TEM0002 SmartBerry Series
Microsemi M2S010, DDR3, Ethernet PHY with RJ45 MagJack

http://trenz.org/smartberry-info

<table>
<thead>
<tr>
<th>Device list</th>
<th>SDRAM</th>
<th>Ethernet</th>
<th>Total I/O</th>
<th>Connectors</th>
<th>on-board</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Microsemi M2S010-VF6400</td>
<td>1 Gbit DDR3</td>
<td>1 Gbit Ethernet PHY with RJ45 MagJack</td>
<td>Raspberry Pi compatible header with 26 I/O + 12C</td>
<td>MicroSD Card socket, 4 Pmod headers</td>
<td>USB/JTAG USB/serial</td>
<td>2 Push Buttons</td>
</tr>
</tbody>
</table>

TE0790 XMOD FTDI JTAG Adapter
XMOD Form Factor, FT2232H, Lattice X02-256 CPLD

http://trenz.org/te0790-info

<table>
<thead>
<tr>
<th>Device</th>
<th>Form Factor</th>
<th>FT2232H</th>
<th>Total I/O</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Lattice X02-256 CPLD</td>
<td>XMOD, M3 mounting hole</td>
<td>Mini USB connector, channel B TX/RX LED's, EEPROM</td>
<td>8 universal I/O pins</td>
<td>Step down DC/DC Converter for optional Power Supply via USB-Power, 4 Position DIP Switch</td>
</tr>
</tbody>
</table>

Comes in two versions:
a) compatible with Xilinx tools or
b) not compatible with Xilinx tools, for independent use
TEI0004 ARROW USB Programmer2
For Development with Intel FPGAs, 2.54 mm Header

http://trenz.org/tei0004-info

<table>
<thead>
<tr>
<th>Supported by</th>
<th>JTAG Connector</th>
<th>USB</th>
<th>Voltage levels</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>Intel Quartus</td>
<td>Standard 2 x 5-pin header</td>
<td>MicroUSB connector</td>
<td>1.8V - 3.3V</td>
<td>Additional support for UART, red activity LED, green power-on LED</td>
</tr>
<tr>
<td>programmer</td>
<td></td>
<td>USB2.0 HS support</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

TEI0005 FPGA Programmer2 Module
FT2232H based JTAG Programmer, Surface-Mount module

http://trenz.org/usbprogrammer2

<table>
<thead>
<tr>
<th>Device list</th>
<th>Supported</th>
<th>Powered</th>
<th>Compatible</th>
<th>Other Features</th>
</tr>
</thead>
<tbody>
<tr>
<td>FTDI FT2232H USB2 Interface</td>
<td>by Intel Quartus (JTAG mode only)</td>
<td>via USB</td>
<td>SMT Pick and Place Assembly Process</td>
<td>Additional UART Channel available, Activity LED’s, UART Interface available, two I/O pins reserved for future use</td>
</tr>
</tbody>
</table>
Trenz Electronic Starter Kits
Including Accessories

Trenz Electronic Starter Kits consists in general of a Trenz Electronic micromodule mounted on a Trenz Electronic base board including a pre-assembled heatsink, some of them are built in a black Core V1 Mini-ITX Enclosure. All this provided with a matching power supply including different adapters, a micro SD card, an USB cable plus screws and bolts.

<table>
<thead>
<tr>
<th></th>
<th>Starter Kit 720</th>
<th>Starter Kit 729</th>
<th>Starter Kit 803</th>
<th>Starter Kit 808</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Module</strong></td>
<td>TE0720</td>
<td>TE0729</td>
<td>TE0803</td>
<td>TE0808</td>
</tr>
<tr>
<td><strong>FPGA</strong></td>
<td>Xilinx Zynq-7020</td>
<td>Xilinx Zynq-7020</td>
<td>Xilinx Zynq UltraScale + ZU3EG</td>
<td>Xilinx Zynq UltraScale + ZU9EG</td>
</tr>
<tr>
<td><strong>Baseboard</strong></td>
<td>TE0703</td>
<td>TEB0729</td>
<td>TEBF0808</td>
<td>TEBF0808</td>
</tr>
<tr>
<td><strong>Enclosure</strong></td>
<td>-</td>
<td>-</td>
<td>Core V1 Mini-ITX</td>
<td>Core V1 Mini-ITX</td>
</tr>
<tr>
<td><strong>Power Supply</strong></td>
<td>Universal power supply unit</td>
<td>Universal power supply unit</td>
<td>Be Quiet! 400W ATX Power Supply</td>
<td>Be Quiet! 400W ATX Power Supply</td>
</tr>
<tr>
<td><strong>Heat sink</strong></td>
<td>Heat sink for TE0720, spring-loaded embedded</td>
<td>KK0729-02TE custom built</td>
<td>BGA Heat sink</td>
<td>BGA Heat sink</td>
</tr>
<tr>
<td><strong>USB cable</strong></td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td><strong>MicroSD card</strong></td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
<tr>
<td><strong>Screws &amp; bolts</strong></td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
<td>✔</td>
</tr>
</tbody>
</table>

Photo shows similar product.
We are offering different customized cooling solutions for a selection of modules. Please ask for special solutions at sales@trenz-electronic.de.

**Cooling Solutions for Trenz Electronic Modules**

**Available cooling solutions**

<table>
<thead>
<tr>
<th>Trenz Electronic Article Number</th>
<th>TE600</th>
<th>TE0710</th>
<th>TE0712</th>
<th>TE0714</th>
<th>TE0715</th>
<th>TE0720</th>
</tr>
</thead>
<tbody>
<tr>
<td>26920</td>
<td>26925</td>
<td>26924</td>
<td>KK0714-02</td>
<td>26923</td>
<td>26922</td>
<td></td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Trenz Electronic Article Number</th>
<th>TE0729</th>
<th>TE0741</th>
<th>TE0745</th>
<th>TE0808</th>
<th>TE0820</th>
</tr>
</thead>
<tbody>
<tr>
<td>KK0729-02TE</td>
<td>26921</td>
<td>KK0745-02</td>
<td>KK0808-03</td>
<td>28606</td>
<td></td>
</tr>
</tbody>
</table>
## Module series comparison table

For Trenz Electronic Modules

<table>
<thead>
<tr>
<th></th>
<th>TE0710</th>
<th>TE0711</th>
<th>TE0712</th>
<th>TE0713</th>
<th>TE0714</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Device family</strong></td>
<td>Artix-7</td>
<td>Artix-7</td>
<td>Artix-7</td>
<td>Artix-7</td>
<td>Artix-7</td>
</tr>
<tr>
<td><strong>Device list</strong></td>
<td>35T, 50T, 75T, 100T</td>
<td>35T, 50T, 75T, 100T</td>
<td>35T, 50T, 75T, 100T, 200T</td>
<td>15T - 200T</td>
<td>15T, 35T, 50T</td>
</tr>
<tr>
<td><strong>Form factor/size [cm]</strong></td>
<td>4 x 5</td>
<td>4 x 5</td>
<td>4 x 5</td>
<td>4 x 5</td>
<td>4 x 3</td>
</tr>
<tr>
<td><strong>Connectors</strong></td>
<td>2 x Samtec LSHM</td>
<td>2 x Samtec LSHM</td>
<td>3 x Samtec LSHM</td>
<td>3 x Samtec LSHM</td>
<td>2 x Samtec LSHM</td>
</tr>
<tr>
<td><strong>Programmable logic family</strong></td>
<td>Artix-7</td>
<td>Artix-7</td>
<td>Artix-7</td>
<td>Artix-7</td>
<td>Artix-7</td>
</tr>
<tr>
<td><strong>Processing system</strong></td>
<td>MicroBlaze</td>
<td>MicroBlaze</td>
<td>MicroBlaze</td>
<td>MicroBlaze</td>
<td>MicroBlaze</td>
</tr>
<tr>
<td><strong>SDRAM [MByte] max</strong></td>
<td>512 DDR3</td>
<td>-</td>
<td>1024 DDR3</td>
<td>1024 DDR3L</td>
<td>-</td>
</tr>
<tr>
<td><strong>Flash [MByte]</strong></td>
<td>32</td>
<td>32</td>
<td>32</td>
<td>32</td>
<td>16</td>
</tr>
<tr>
<td><strong>EEPROM</strong></td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td><strong>eMMC</strong></td>
<td>-</td>
<td>-</td>
<td>MAC Address</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td><strong>Ethernet PHY</strong></td>
<td>-</td>
<td>USB2 UART/FIFO</td>
<td>-</td>
<td>USB3.0</td>
<td>-</td>
</tr>
<tr>
<td><strong>USB PHY</strong></td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td><strong>Total I/O</strong></td>
<td>112</td>
<td>178</td>
<td>158</td>
<td>152</td>
<td>144</td>
</tr>
<tr>
<td><strong>GBit transceiver</strong></td>
<td>-</td>
<td>-</td>
<td>4 x GTP</td>
<td>4 x GTP</td>
<td>4 x GTP</td>
</tr>
<tr>
<td><strong>Other features</strong></td>
<td>Single Supply</td>
<td>Single Supply</td>
<td>Programmable Clock Generator, Single Supply</td>
<td>Programmable Clock Generator, Single Supply</td>
<td>Single Supply, Differential MEMS Oscillator for GT clocking, GT Reference Clock input</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th></th>
<th>TE0745</th>
<th>TE0782</th>
<th>TE0783</th>
<th>TE0803</th>
</tr>
</thead>
<tbody>
<tr>
<td><strong>Device family</strong></td>
<td>Zynq-7000</td>
<td>Zynq-7000</td>
<td>Zynq-7000</td>
<td>Zynq UltraScale+</td>
</tr>
<tr>
<td><strong>Connectors</strong></td>
<td>3 x Samtec ST5</td>
<td>3 x Samtec QTH</td>
<td>3 x Samtec QTH</td>
<td>4 x Samtec ST5</td>
</tr>
<tr>
<td><strong>Programmable logic family</strong></td>
<td>Kintex-7</td>
<td>Kintex-7</td>
<td>Kintex-7</td>
<td>UltraScale+</td>
</tr>
<tr>
<td><strong>Processing system</strong></td>
<td>2 x Cortex A9</td>
<td>2 x Cortex A9</td>
<td>2 x Cortex A9</td>
<td>Up to 4 x Cortex A53+</td>
</tr>
<tr>
<td><strong>SDRAM [MByte] max</strong></td>
<td>1024 DDR3/L</td>
<td>1024 DDR3</td>
<td>1024 DDR3 connected to PS 2048 DDR3 connected to PL</td>
<td>8192 DDR4</td>
</tr>
<tr>
<td><strong>Flash [MByte]</strong></td>
<td>64</td>
<td>32</td>
<td>32</td>
<td>128</td>
</tr>
<tr>
<td><strong>EEPROM</strong></td>
<td>MAC Address</td>
<td>2 x MAC + 16 KByte</td>
<td>1 x MAC + 16 KByte</td>
<td>-</td>
</tr>
<tr>
<td><strong>eMMC</strong></td>
<td>-</td>
<td>4 - 64 GByte</td>
<td>4 - 64 GByte</td>
<td>-</td>
</tr>
<tr>
<td><strong>Ethernet PHY</strong></td>
<td>1 GBit</td>
<td>2 x 1 GBit</td>
<td>1 GBit</td>
<td>-</td>
</tr>
<tr>
<td><strong>USB PHY</strong></td>
<td>USB2.0 OTG</td>
<td>2 x USB2.0 OTG</td>
<td>USB2.0 OTG</td>
<td>-</td>
</tr>
<tr>
<td><strong>Total I/O</strong></td>
<td>250 + 6 MIO</td>
<td>250 + 2 MIO</td>
<td>166 + 12 MIO + 40 CPLD muxed IO</td>
<td>156 + 65 MIO</td>
</tr>
<tr>
<td><strong>GBit transceiver</strong></td>
<td>8 x GTX</td>
<td>16 x GTX</td>
<td>16 x GTX</td>
<td>PS GTR 4</td>
</tr>
<tr>
<td><strong>Other features</strong></td>
<td>RTC, Single Supply</td>
<td>Programmable Clock Generator, RTC, Single Supply</td>
<td>* 32-bit and 64-bit wide DDR3, Programmable Clock Generator, RTC, Single Supply</td>
<td>Programmable Clock Generator, Single Supply</td>
</tr>
</tbody>
</table>
Other assembly options for cost or performance optimization available on request.

Cooling solutions and carrier boards are available.

<table>
<thead>
<tr>
<th>TE0715</th>
<th>TE0720</th>
<th>TE0724</th>
<th>TE0728</th>
<th>TE0729</th>
<th>TE0741</th>
</tr>
</thead>
<tbody>
<tr>
<td>Zynq-7000</td>
<td>Zynq-7000</td>
<td>Zynq-7000</td>
<td>Zynq-7000</td>
<td>Zynq-7000</td>
<td>Kintex-7</td>
</tr>
<tr>
<td>Z-7015, Z-7030</td>
<td>Z-7020</td>
<td>Z-7010; Z-7020</td>
<td>Z-7020 (automotive)</td>
<td>Z-7020</td>
<td>7OT, 16OT, 325T, 41OT</td>
</tr>
<tr>
<td>4 x 5</td>
<td>4 x 5</td>
<td>6 x 4</td>
<td>6 x 6</td>
<td>5.2 x 7.6</td>
<td>4 x 5</td>
</tr>
<tr>
<td>3 x Samtec LSHM</td>
<td>3 x Samtec LSHM</td>
<td>1 x Samtech ST5</td>
<td>3 x Samtech SEM</td>
<td>2 x Samtec BTE</td>
<td>3 x Samtec LSHM</td>
</tr>
<tr>
<td>Z-7015: Artix-7, Z-7030: Kintex-7</td>
<td>Artix-7</td>
<td>Artix-7</td>
<td>Artix-7</td>
<td>Artix-7</td>
<td>Kintex-7</td>
</tr>
<tr>
<td>2 x Cortex A9</td>
<td>2 x Cortex A9</td>
<td>2 x Cortex A9</td>
<td>2 x Cortex A9</td>
<td>2 x Cortex A9</td>
<td>MicroBlaze</td>
</tr>
<tr>
<td>1024 DDR3</td>
<td>1024 DDR3</td>
<td>1024 DDR3L</td>
<td>512 DDR3</td>
<td>512 DDR3</td>
<td>-</td>
</tr>
<tr>
<td>32</td>
<td>32</td>
<td>32</td>
<td>16</td>
<td>32</td>
<td>32</td>
</tr>
<tr>
<td>MAC Address</td>
<td>MAC Address</td>
<td>MAC Address</td>
<td>8 KByte</td>
<td>3 x MAC Address</td>
<td>-</td>
</tr>
<tr>
<td>-</td>
<td>4 - 64 GByte</td>
<td>-</td>
<td>-</td>
<td>4 - 64 GByte</td>
<td>-</td>
</tr>
<tr>
<td>1 GBit</td>
<td>1 GBit</td>
<td>1 GBit</td>
<td>2 x 100 MBit</td>
<td>2 x 100 MBit, 1 GBit</td>
<td>-</td>
</tr>
<tr>
<td>USB2.0 OTG</td>
<td>USB2.0 OTG</td>
<td>-</td>
<td>-</td>
<td>USB2.0 OTG</td>
<td>-</td>
</tr>
<tr>
<td>132 + 14 MIO</td>
<td>152 + 14 MIO</td>
<td>PL: 64 PS: 20</td>
<td>124 + 34 MIO</td>
<td>136 + 14 MIO</td>
<td>144</td>
</tr>
<tr>
<td>Z-7015:4 x GTX, Z-7030:4 x GTX</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>8 x GTX</td>
<td>-</td>
</tr>
</tbody>
</table>

Programmable Clock Generator, RTC, Single Supply

<table>
<thead>
<tr>
<th>TE0807</th>
<th>TE0808</th>
<th>TE0820</th>
<th>TE0841</th>
</tr>
</thead>
<tbody>
<tr>
<td>Zynq UltraScale+</td>
<td>Zynq UltraScale+</td>
<td>Zynq UltraScale+</td>
<td>Kintex UltraScale</td>
</tr>
<tr>
<td>ZU4CG, ZU5CG, ZU7CG, ZU4EG, ZU5EG, ZU7EG, ZU4EV, ZU5EV, ZU7EV</td>
<td>ZU6CG, ZU6EG, ZU6EV, ZU7EG, ZU7EV</td>
<td>ZU2CG-ZU5CG, ZU2EG-ZU5EG, ZU2EV, ZU5EV</td>
<td>KU35, KU40</td>
</tr>
<tr>
<td>5.2 x 7.6</td>
<td>5.2 x 7.6</td>
<td>4 x 5</td>
<td>4 x 5</td>
</tr>
<tr>
<td>4 x Samtec ST5</td>
<td>4 x Samtec ST5</td>
<td>3 x Samtec LSHM</td>
<td>3 x Samtec LSHM</td>
</tr>
<tr>
<td>UltraScale+</td>
<td>UltraScale+</td>
<td>UltraScale+</td>
<td>UltraScale+</td>
</tr>
<tr>
<td>Up to 4 x Cortex A53+</td>
<td>Up to 4 x Cortex A53+</td>
<td>Up to 4 x Cortex A53+</td>
<td>MicroBlaze</td>
</tr>
<tr>
<td>8192 DDR4</td>
<td>8192 DDR4</td>
<td>4096 DDR4</td>
<td>4096 DDR4</td>
</tr>
<tr>
<td>128</td>
<td>128</td>
<td>128</td>
<td>64</td>
</tr>
<tr>
<td>16 KByte</td>
<td>16 KByte</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>-</td>
<td>-</td>
<td>4 - 64 GByte</td>
<td>-</td>
</tr>
<tr>
<td>-</td>
<td>-</td>
<td>1 GBit</td>
<td>-</td>
</tr>
<tr>
<td>-</td>
<td>-</td>
<td>USB2.0 OTG</td>
<td>-</td>
</tr>
<tr>
<td>204 + 65 MIO</td>
<td>204 + 65 MIO</td>
<td>132 + 14 MIO</td>
<td>144</td>
</tr>
<tr>
<td>4 x GTR, 16 x GTH</td>
<td>4 x GTR, 16 x GTH</td>
<td>PS GTR 4</td>
<td>8 x GTH</td>
</tr>
</tbody>
</table>

Programmable Clock Generator, Single Supply

Programmable Clock Generator, Single Supply

Programmable Clock Generator, RTC, Single Supply

Programmable Clock Generator, Single Supply
PC/104 OneBank Carrier for SoC Modules

Providing the base for all of our PC/104 integrated systems the EMC²-SoM is a PCIe/104 OneBank® Carrier for a Trenz compatible 40mm x 50mm SoC Module.

The EMC²-SoM also provides expansion for a VITA57.1 FMC™ LPC I/O board.

The PCIe/104 OneBank® design enables the EMC²-SoM to be added to robust and rugged installations for various commercial, medical, industrial and military uses.

Using the EMC² to provide real-time on-board obstacle avoidance for UAVs based on embedded stereo vision.

The increase in the use of UAVs requires an improvement of their usability and safety. UAVs are equipped with multiple sensors to monitor their surroundings for imaging purposes and also for obstacle avoidance. Using stereo cameras to calculate the distance to surrounding objects offers a more cost effective solution than using distance sensors.
The VCS-1 system is an EMC² and FM191 combination which features:

- High performance and low power consumption (24V @ 1.1A)
- Compatible with a wide range of commercially available sensors and actuators
- Raspberry Pi and Arduino compatible
- 4x USB3.0 ports for interfacing with multiple sensors
- MQTT and OpenCV compatible
- ROS compatible, ROS2 ready and HIPPEROS ready
- Will be fully compatible with the Xilinx reVision stack.

Connectivity:

**FM191-R; FMC-LPC to:**

- 15x Digital I/Os [DB9]
- 12x Analogue Inputs [DB9]
- 8x Analogue Outputs [DB9]
- 1x Expansion [SEIC]

**FM191-U; SEIC to:**

- 4x USB3.0 [USB-C]
- 28x GPIO [40-pin GPIO]

**FM191-A1; 40-pin GPIO**

- 28x GPIO [DB9]
cronologic offers a family of high-resolution high-throughput PCIe analog-to-digital converters (ADCs):

- Up to 4 analog input channels
- Additional digital trigger and/or gating inputs
- PCIe x4 or x8 half-size boards
- Gross DMA-bandwidth up to 8 GByte/s
- Arbitrary board combinations can be synchronized
- LEMO 00 series input connectors (adapter cables to SMA connector available)
- The DC-offset can be shifted to make optimal use of the ADC range for either positive or negative pulses

### Ndigo Series

<table>
<thead>
<tr>
<th>Unit</th>
<th>Analog Channels</th>
<th>ADC Resolution</th>
<th>Max. Sampling Rate</th>
<th>Max. Bandwidth</th>
<th>Max. Individual Sample Length</th>
<th>THD (dBc)</th>
<th>SNR (dBc)</th>
<th>SFDR incl. (dBc)</th>
<th>SFDR excl. (dBc)</th>
<th>SINAD (dBc)</th>
<th>ENOB</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ndigo2G-14</td>
<td>2</td>
<td>14</td>
<td>2248 Msps</td>
<td>1000 MHz</td>
<td>58 hours</td>
<td>70*</td>
<td>60*</td>
<td>72*</td>
<td>84*</td>
<td>60*</td>
<td>9.8*</td>
</tr>
<tr>
<td>Ndigo5G-10</td>
<td>4</td>
<td>10</td>
<td>1250 Msps</td>
<td>1000 MHz</td>
<td>26 hours</td>
<td>58</td>
<td>51</td>
<td>61</td>
<td>74</td>
<td>50</td>
<td>8.0</td>
</tr>
<tr>
<td>Ndigo5G-8</td>
<td>2</td>
<td>8</td>
<td>2500 Msps</td>
<td>1000 MHz</td>
<td>26 hours</td>
<td>58</td>
<td>50</td>
<td>60</td>
<td>64</td>
<td>48</td>
<td>7.7</td>
</tr>
<tr>
<td>Ndigo250M-14</td>
<td>1</td>
<td>14</td>
<td>5000 Msps</td>
<td>120 or 700 MHz</td>
<td>32 hours</td>
<td>73</td>
<td>45</td>
<td>58</td>
<td>57</td>
<td>48</td>
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<tr>
<td>Violet125M-14</td>
<td>4</td>
<td>14</td>
<td>2500 Msps</td>
<td>60 or 450 MHz</td>
<td>12 hours</td>
<td>86</td>
<td>64</td>
<td>74</td>
<td>TBD</td>
<td>72</td>
<td>10.3</td>
</tr>
</tbody>
</table>

It has been designed to acquire trains of pulses at high repetition rates. Employing an onboard zero suppression, the pulse data is recorded with pre- and post-cursors, omitting the data in between to reduce the requirements on bandwidth and pulse processing or averaging. There is no deadtime between samples as long as the sustained rate is lower than the available PCIe bandwidth. The first available instances of this series provide 5 Gsps at 10-bit resolution and 250 Msps at 14-bit resolution.

These boards are ideally suited for applications like:
- Mass Spectrometry
- Photon Counting
- Lidar
- NMR

*preliminary data
cronologic offers a family of high-resolution high-throughput PCIe analog-to-digital converters (ADCs)

- Up to 4 analog input channels
- Additional digital trigger and/or gating inputs
- PCIe x4 or x8 half-size boards
- Gross DMA-bandwidth up to 8 GByte/s
- Arbitrary board combinations can be synchronized
- LEMO 00 series input connectors (adapter cables to SMA connector available)
- The DC-offset can be shifted to make optimal use of the ADC range for either positive or negative pulses

<table>
<thead>
<tr>
<th></th>
<th>Ndigo2G-14</th>
<th>Ndigo5G-10</th>
<th>Ndigo5G-8</th>
<th>Ndigo250M-14</th>
<th>Violet125M-14</th>
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<td>Analog channels</td>
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<td>ADC resolution</td>
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<td>Bits</td>
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<td>Max. sampling rate</td>
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<td>1250</td>
<td>2500</td>
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<td>125</td>
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<tr>
<td>Max. bandwidth</td>
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<td>1000</td>
<td>120 or 700</td>
<td>60 or 450</td>
<td>MHz</td>
</tr>
<tr>
<td>Max. individual</td>
<td>58</td>
<td>26</td>
<td>26</td>
<td>32</td>
<td></td>
<td>hours</td>
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<tr>
<td>sample length</td>
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<td></td>
<td></td>
<td></td>
<td></td>
<td>µs</td>
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<tr>
<td>THD</td>
<td>70*</td>
<td>58</td>
<td>58</td>
<td>73</td>
<td>86</td>
<td>dBC</td>
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<td>SNR</td>
<td>60*</td>
<td>51</td>
<td>50</td>
<td>45</td>
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<td>74</td>
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<td>SFDR incl.</td>
<td>72*</td>
<td>61</td>
<td>60</td>
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<td>SFDR excl.</td>
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<td>SINAD</td>
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<tr>
<td>ENOB</td>
<td>9.8*</td>
<td>8.0</td>
<td>7.7</td>
<td>7.7</td>
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<td>11.0</td>
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<tr>
<td>Input type and</td>
<td>AC single ended</td>
<td>AC single ended</td>
<td>AC single ended</td>
<td>DC single ended</td>
<td>DC single ended</td>
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<tr>
<td>coupling</td>
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<td>PCIe lanes</td>
<td>8</td>
<td>4</td>
<td>4</td>
<td>8</td>
<td>8</td>
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<td>PCIe bandwidth</td>
<td>5000</td>
<td>800</td>
<td>800</td>
<td>1400</td>
<td>1400</td>
<td>MB/s</td>
</tr>
</tbody>
</table>

*preliminary data

**Ndigo Series**

It has been designed to acquire trains of pulses at high repetition rates. Employing an onboard zero suppression, the pulse data is recorded with pre- and post-cursors, omitting the data in between to reduce the requirements on bandwidth and pulse processing or averaging. There is no deadtime between samples as long as the sustained rate is lower than the available PCIe bandwidth. The first available instances of this series provide 5 Gsp/s at 10-bit resolution and 250 Msp/s at 14-bit resolution.

These boards are ideally suited for applications like
- Mass Spectrometry
- Photon Counting
- Lidar
- NMR
Ndigo Crate

With the Ndigo Crate it is possible to use up to 8 PCIe boards with a PC. The connection of the external chassis to the PC happens over PCIe 2 x16 for a full duplex bandwidth of 2x 8GByte/s.

The enclosure was specifically designed to operate multiple synchronized cronologic digitizer boards to create a high speed data acquisition system. It can also be used to house other DAQ cards, GPUs for high performance computing, storage adapters or networking equipment.

The extension is fully transparent. The operating system can’t distinguish between boards in the PCIe expansion box and boards inside the PC itself. No drivers are required.

The slot covers are on the front side of the enclosure to easily see status information and plug in cables during operation.

The crate is delivered as a set with cable and PC link board.

---

Violet Series

It has been designed to continuously stream samples data to host computer main memory at full rate. These boards are ideal for any applications that require unusually long samples at rates up to 250 Msp/s at a resolution of 14 bits.

For example

- Software Defined Radio High Precision FFT Spectrum Analyzers

---

Time Tagger

Cronologic presents a new series of low cost, mid resolution time-to-digital converters.

Two new boards are available featuring 500ps to 1ns single shot resolution at highest data bandwidths.

Time Taggers are ideally suitable in applications that do not require highest single shot timing resolution, but high data acquisition rates and lowest multiple hit deadtime. These include certain types of mass spectroscopy, time correlated single photon counting (TCSPC) and frequency counting applications.

---

http://www.trenz-electronic.de/cronologic
Ndigo Crate

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<table>
<thead>
<tr>
<th>Facts</th>
<th>Crate</th>
<th>Crate-3</th>
<th>Crate-5</th>
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</thead>
<tbody>
<tr>
<td><strong>Connection to Host</strong></td>
<td>PCIe 2.0 x 16</td>
<td>PCIe 2.0 x 16</td>
<td>PCIe 2.0 x 16</td>
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<tr>
<td><strong>Bandwidth to Host</strong></td>
<td>8 GByte/s</td>
<td>8 GByte/s</td>
<td>8 GByte/s</td>
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<tr>
<td><strong>Performance relative to 10Gbps Thunderbolt link</strong></td>
<td>8x</td>
<td>8x</td>
<td>8x</td>
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<tr>
<td>PCIe3 16x slots with 8 lanes</td>
<td>-</td>
<td>2</td>
<td>2</td>
</tr>
<tr>
<td>PCIe3 16x slots with 4 lanes</td>
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<td>3</td>
<td>3</td>
</tr>
<tr>
<td>PCIe2 16x slots with 4 lanes</td>
<td>8</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>PCI slots 5V, 32 Bit, 33MHz</td>
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<td>-</td>
<td>2</td>
</tr>
<tr>
<td>PCI slots 3V, 32 Bit, 66MHz</td>
<td>-</td>
<td>2</td>
<td>-</td>
</tr>
<tr>
<td><strong>Availability</strong></td>
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<td>now</td>
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<tr>
<td><strong>Cable and link boards</strong></td>
<td>included</td>
<td>included</td>
<td>included</td>
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<tr>
<td><strong>Cable Length</strong></td>
<td>3 meters (1m, 2m and 5m upon request)</td>
<td></td>
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</tbody>
</table>
Distributor List

**Bulgaria**

Metrisys Ltd.
Web: www.metrisys.com
E-mail: bulgaria@metrisys.com

**China & Taiwan**

Future Linking Solution Tech Co. Ltd
Web: www.fulso.com
E-mail: liu@fulso.com

**Czech Republic + Slovakia**

DFC Design, s.r.o.
Web: www.dfcdesign.cz
E-mail: info@dfcdesign.cz

**France**

Lextronic
Web: www.lextronic.fr
E-mail: lextronic@lextronic.fr

**Germany**

ARIES Embedded GmbH
Web: www.aries-embedded.de
E-mail: info@aries-embedded.de

**Greece**

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Web: www.kernel.gr
E-mail: info@kernel.gr

**Hungary**

ChipCAD Ltd.
Web: www.chipcad.hu
E-mail: info@chipcad.hu

**India**

Innovate Software Solutions Pvt. Ltd.
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E-mail: sales@innovatesolutions.net

**Italy**

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Web: www.mirifica.it
E-mail: store@mirifica.it

**Japan**

Tokushu Denshi Kairo Inc.
Web: www.tokudenkairo.co.jp
E-mail: info@tokudenkairo.co.jp

**Korea**

Inipro Inc.
Web: www.inipro.net
E-mail: webmaster@inipro.net

**Poland**

BTC Korparacja
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E-mail: sprzedaz@kamami.pl

**Romania**

COMET ELECTRONICS S.R.L.
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E-mail: office@comet.srl.ro

**Singapore**

Advinnos Technologies Pte Ltd.
Web: www.advinnos.com
E-mail: info@advinnos.com

**Spain**

SemiPIN PTE Ltd.
Web: www.semipin.com
E-mail: trenz@semipin.com

**Turkey**

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E-mail: info@ekom-ltd.com

**UK**

Sundance Multiprocessor Technology Ltd.
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E-mail: trenz@sundance.com

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E-mail: trenz@concurrenteda.com

**Active Supply**
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E-mail: supply@active-supply.net

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E-mail: sales@digikem.com

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E-mail: sales@digikem.com

Mouser Electronics Inc.
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E-mail: muenchen@mouser.com

RS Components GmbH
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E-mail: rs-gmbh@rs-online.de

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Website http://www.trenz-electronic.de

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