Revision -02
========
1) added pullup on EXT2 signal on second onewire header
2) added input buffers to reduce digital load
3) removed option to mount THR capacitors
4) reduced PCB size
5) changed 3x 3pin THT header (enc. term) to single 2x5 pin SMD
6) changed 5 pin THT enc header to 6 pin SMD
7) changed shrouded 2x5 header to SMD header
8) changed 2 4-pin THT headers to one 2x3 SMD
9) changed high current terminals to correct type
10) added 3 jumpers as mechanical parts
11) added transist on 3.3V and 5V
12) added resettable fuses on 12V and 3.3V
13) changed board size, holes and connectors positions

Revision -03
========
1) New fuse F3
2) Changed board size
3) Added ferrite bead to net 3.3V_IN/3.3V

Revision -04
========
1) Added variant -D: option to swap between signal sources SCLK and EXT1.
2) Resistors 22 Ohm replaced with 33 Ohm
3) New data clock signal SCLK routing. Sources separated by 33 Ohm resistors.
4) GND and ISO_GND nets “stitching” via 150pF X1/Y2 capacitors.
5) ADUM4224WCRWZ replaced with ADUM4224WBRWZ.