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U\_A-Headers  
A-Headers.SchDoc  
[Redacted]

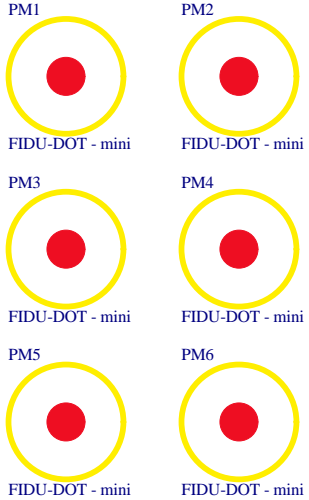
U\_FPGA  
FPGA.SchDoc  
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U\_FPGA\_MISC  
FPGA\_MISC.SchDoc  
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U\_Flash\_RAM  
Flash\_RAM.SchDoc  
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U\_PowerSupply  
PowerSupply.SchDoc  
[Redacted]

Serial  
Serial  
Serialnumber 6,3 x 6.3mm



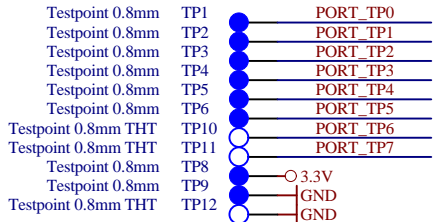
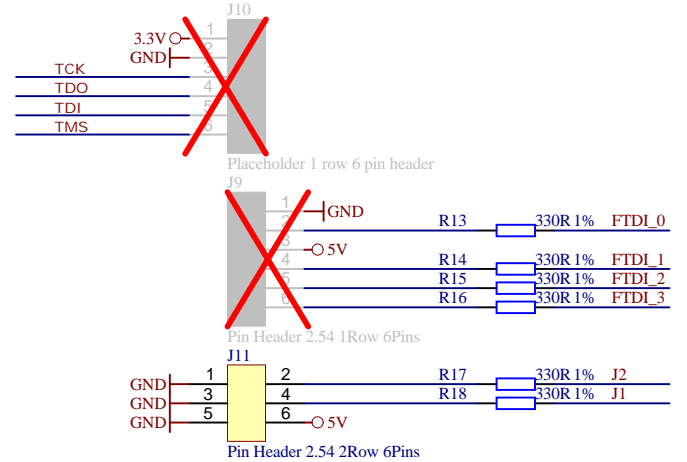
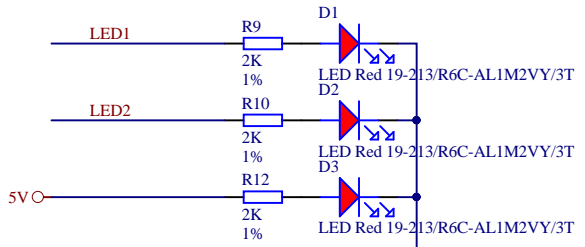
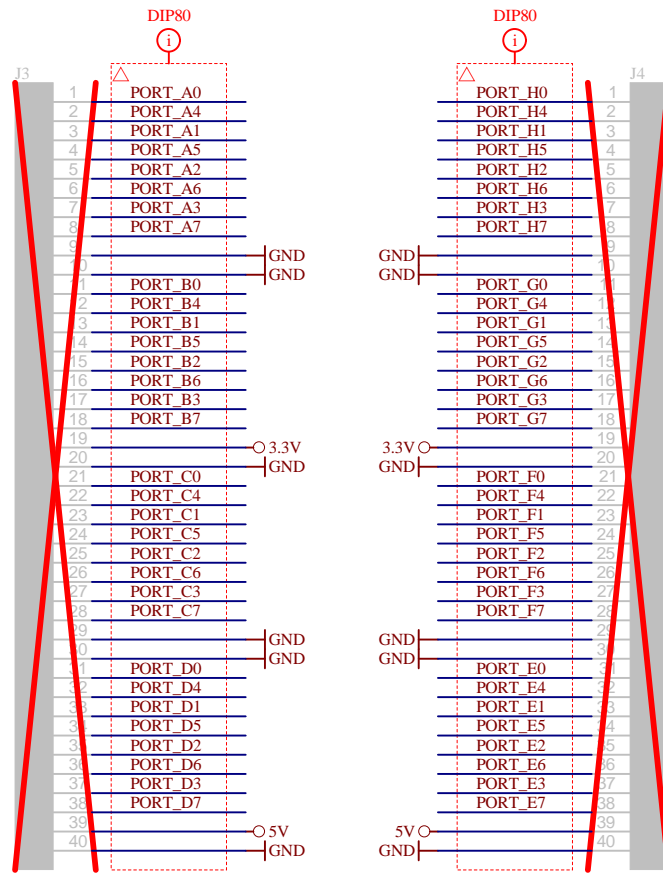
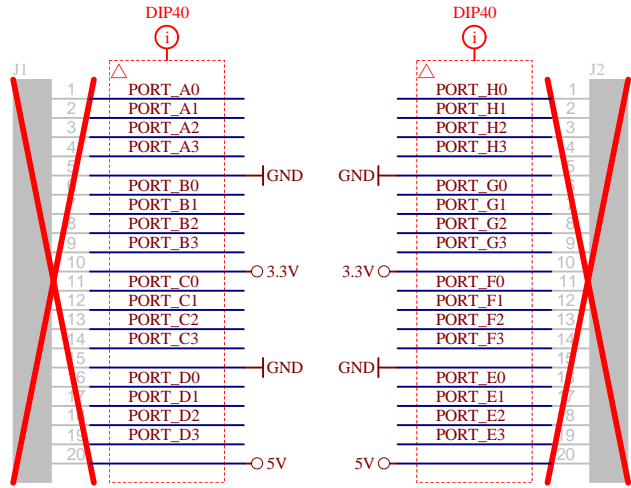
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	A4	Number: TE0890 06-1C	Rev. 01
	Date: 2018-08-06	Copyright: Trenz Electronic GmbH	Page 1 of 6
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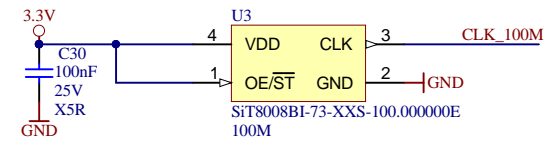
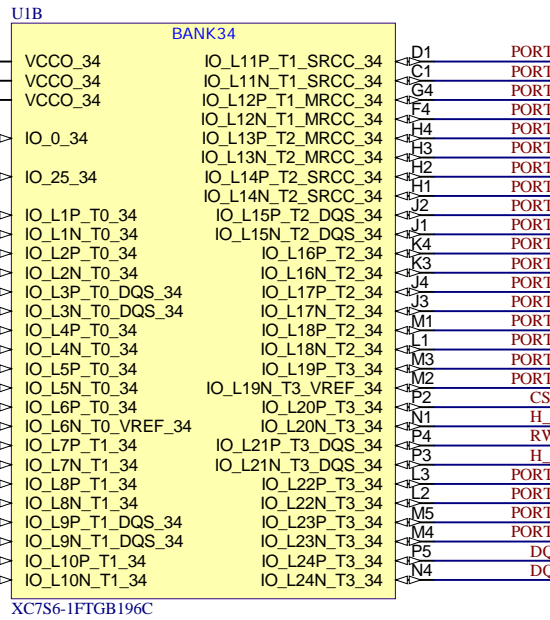
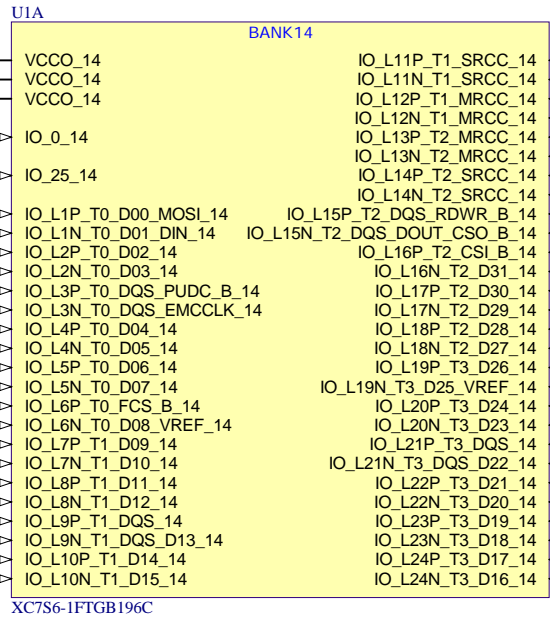
Title: TE0890 - S7Mini		
A4	Number: TE0890 06-1C	Rev. 01
Date: 2018-08-06	Copyright: Trenz Electronic GmbH	Page 2 of 6
Filename: A-Headers.SchDoc		

A Testpoint 0.8mm THT TP13

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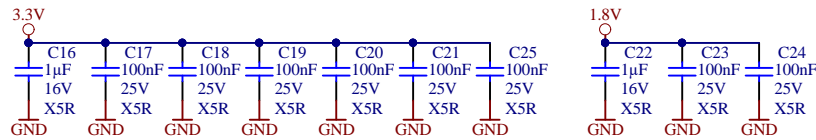
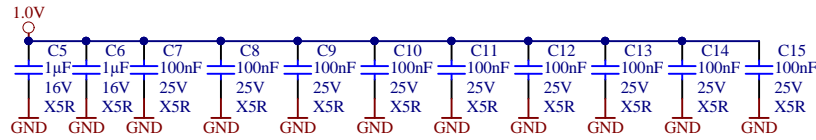
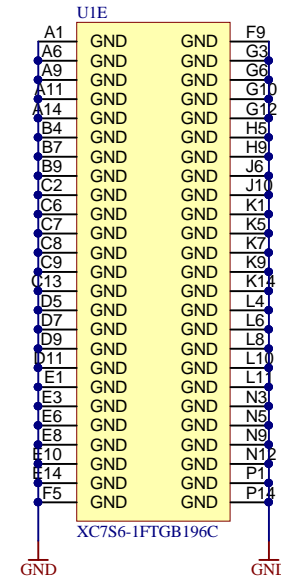
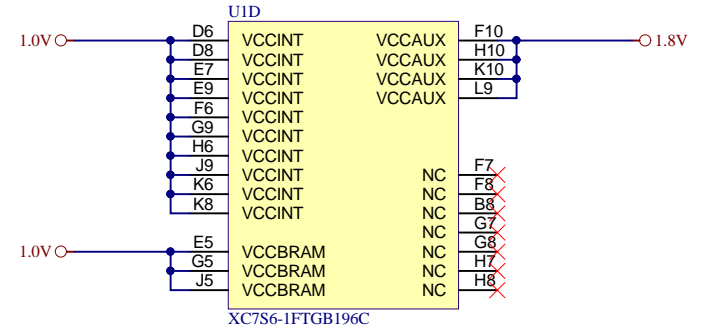
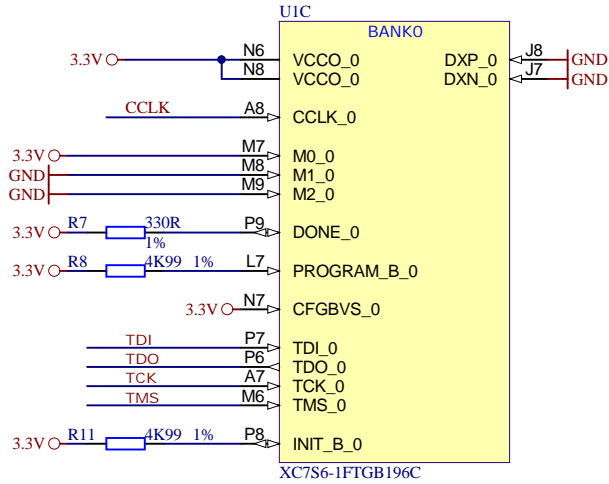
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A4	Number: <b>TE0890 06-1C</b>	Rev. <b>01</b>
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Filename: <b>FPGA.SchDoc</b>		

BOOTMODE = MASTER SPI



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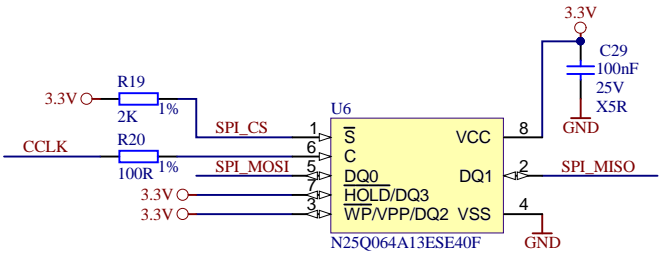
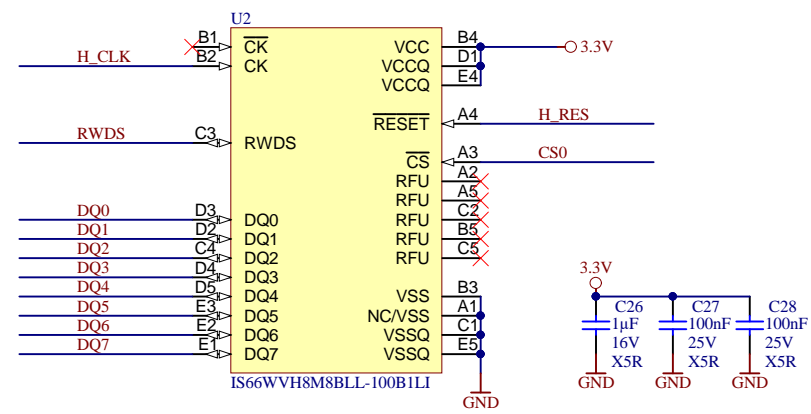
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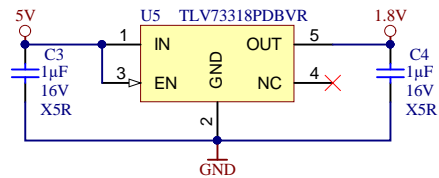
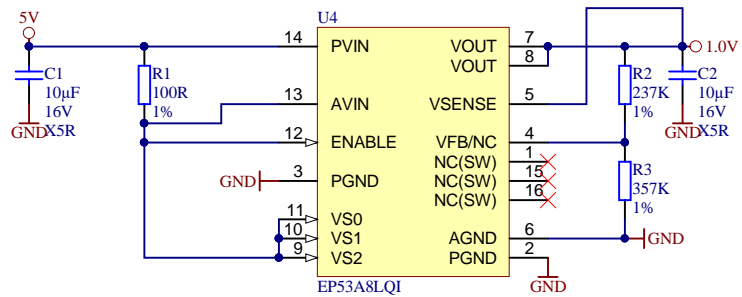
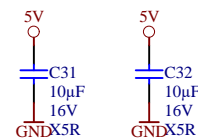
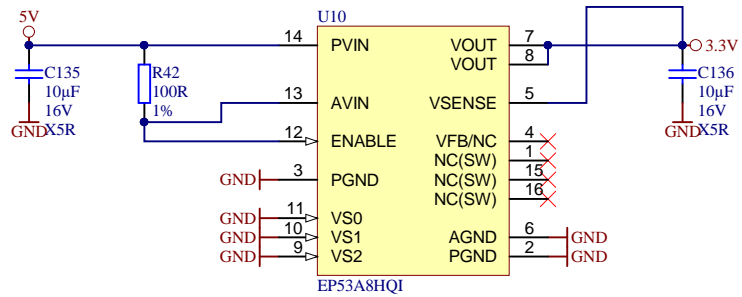
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