OSC FREQ is Assembly OPTION = 125MHZ
Changes REV 02:
1. Added 0 ohm strap option to supply VCCIO0 on B2B connector
2. Added PCB Revision sense support. PCB Revision readout possible from HDL Design
3. Updated FPGA pin PROG_B connection. TPS3805H33 pull-pull output RESET_N not affected
   on baseboard circuit, connected to PROG_B
4. C8, C54, C56 updated to 100uF for variant 50-2I
5. Added testpoints

Changes REV 02A (12.2018):
1. New FLASH memory U7 S25FL127SABMFV10