B13 48 IO, 24 LVDS Pairs
B14 8 / O, 3.3V
MGT 3 Lanes

B16 16 IO, 8 LVDS Pairs
MGT 4 + 1 Lanes
B14 4 I/O, 3.3V

B15 18 IO, 9 LVDS Pairs
B12 48 IO, 24 LVDS Pairs
B12 2 I/O

Table: TE0741

A4: B3E-1-AF

Date: 2016-10-25

B22-Connectors.SchDoc

Title: TE0741

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3.3V

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K 70T version does not have this bank!
Pre-bias feature is not available for parallel operations

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R36  R37  MODE EN3A0QI

| OK | enable pre-bias start-up (available when U14 or U15 as DNP) |
| X  | disable pre-bias start-up |

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Not mount if baseboard EN1 circuit cause problems
Pre-bias feature is not available for parallel operations

R38  R39  MODE EN63A0QI
----------------------------------------
OK   X  |  enable pre-bias start-up (available when U14 or U15 as DNP )
X  OK  |  disable pre-bias start-up
CHANGES REV02 to REV03

1) fixed connection DCDC for parallel operations
2) update Razorbeam Connectors, full update lib
3) added serial number (traceability pad)
4) changed ferrite beads L1..L4 size 0402 on BKP0603HS121-T
5) added thermal vias to mounting holes