Title: TE0820 - MIO Banks

A4 Number: TE0820 03EG-1E

Date: 2017-05-02 Copyright: Trenz Electronic GmbH / TT

Filename: B_MIO.SchDoc
Title: TE0820 - USB_PHY

Number: TE0820

Rev.: 02

Date: 2017-05-02

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Filename: USB-PHY.SchDoc
CHANGES REV01 to REV02

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1) Added MAC EEPROM (slave address)
2) LIB components update
3) Fixed SD Card connection
4) Fixed sense connection from DCDC
5) Made correct power connection for VCU (removed DCDC, added resistors and caps like as Xilinx recommended)
6) Added resistors for variants (ZU+ with/without VCU)
7) Added termination resistors (240R) to VRP pins for all HP-banks