The image contains a schematic diagram of a electronic circuit. The schematic shows various components and connections, including power supplies, signal lines, and other electronic components. The schematic is labeled with component numbers and connections, indicating the flow of signals and power through the circuit. The components include SchDoc files, which are likely to be schematic documents associated with the circuit. The schematic is detailed, showing various parts such as capacitors, resistors, and connectors. The diagram is designed to illustrate the electrical connections and layout of the circuit components.
CHANGES REV01 to REV02

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1) Added MAC EEPROM (slave address)
2) LIB components update
3) Fixed SD Card connection
4) Fixed sense connection from DCDC
5) Made correct power connection for VCU (removed DCDC, added resistors and caps like as Xilinx recommended)
6) Added resistors for variants (ZU+ with/without VCU)
7) Added termination resistors (240R) for VRP pins for all HP-banks

CHANGES REV02 to REV03

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1) Fixed VCU connection: add additional DCDC (0.9V)
2) LIB components update
3) Change package 1K resistors (0402 -> 0201)
4) Added LEDs (1x user LED, 1x LED for ERR_STATUS, 1xLED for ERR_OUT)
5) Change obsolete 2xSPI Flash (256MBit) -> 2xSPI Flash (512MBit)
6) Added additional DCDCs (PL_VCCINT_IO, PS_FP0V85)
7) Changed DCDC (U5) 6A (optional 4A)