Title: TE0841
A4 Number: TE0841 035-21 Rev. 02
Date: 2018-03-14 Copyright: Trenz Electronic GmbH / TT
Page 20 of 21
Filename: PWR2.SchDoc
CHANGES REV01 TO REV01A (08.16.2017):

1) U1: changed schematic symbol. Next pins swapped to mach same polarity order:
   -- AE13 (RO_L6P_TH0U_N10_AD6P_64)/AE12 (RO_L6N_TH0U_N31_AD6N_64)
   -- JS1 (RO_L10P_T2U_N10_AD2P_66)/JS (RO_L18N_T2U_N11_AD2N_66)

2) Net names changed (no electrical changes):
   JM1: swapped signals B64_L6:
   -- B64_L6_N - pin 40 (was pin 42)
   -- B64_L6_P - pin 42 (was pin 40)
   JM3: swapped signals B64_L6:
   -- B66_L18_N - pin 52 (was pin 54)
   -- B66_L18_P - pin 54 (was pin 52)

CHANGES REV01A TO REV02 (03.2018):

2) Fixed sense connection on DCDC
4) Full update LIB
5) Added additional resistors for support 16GBit DDR chips
6) Added strong pull-down to EN_PLL
7) Added additional testpoints for I2C bus
8) Added additional MEMS oscillator (25MHz)
9) Changed pull-up power supply VIN -> 3.3VIN on the PG_DDR net
10) Added pull-down on the EN_DDR