MIO0 - 8 I/O 3.3V
MIO1 - 12 I/O 1.8V
B34 - 32 I/O 3.3V
B35 - 24 LVDS or 48 I/O Variable
XADC used only for temperature and power monitoring

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Testpoint 0.8mm
Testpoint 0.8mm
Testpoint 0.8mm
Testpoint 0.8mm
Title: TE0724 - Zynq_PS_DDR

Filename: PS_DDR.SchDoc

Date: 2017-05-30

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REV. 02:

1) New I2C translator U11: TCA9406DCUR
2) Fixed boot mode connection: Mode0 pulled-up by R27 and Mode 1 pulled-up by R33
3) Green LED D3 replaced to Red LED
4) C91 changed from 47 to 22uF
5) C3 not fitted
6) C105 deleted, C47 replaced from 10 to 22uF (EP53A7HQI output capacitor)
7) PWR_GPIO3 (EP53A7HQI enable) pulled down by R45 (19.1 kOhm)
8) DONE, INIT, PROG_B pulled-up to 3.3V
9) Added testpoints: JTAG, PMIC (I2C, ONKEY, TP)
10) J2 changed to 2 row 6-pin header. To J2 added ONKEY, PWR_TP, VIN
11) Mounting holes changed from 2mm to 2.2mm

REV. 02A:

1) PCB revision: all vias are epoxy filled and copper capped. No mask openings on vias. No electrical changes

REV. 03:

1) Replace DCDC U8 by TPS82140