CHANGES REV01 to REV02

1) MAC EEPROM Address patch fixed on PCB
2) Lib components update
<table>
<thead>
<tr>
<th>Column</th>
<th>Description</th>
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</thead>
<tbody>
<tr>
<td>A1</td>
<td>RESERVED for additional 8 GTX lanes</td>
</tr>
</tbody>
</table>

**TE0745 - B2B connectors J3**

**A4**

**Number:** 5

**Rev:** 02

**Date:** 2016-04-26

**Copyright:** Trenz Electronic GmbH / TT

**Filename:** J3.SchDoc
Slave ADDR:

for RTC registers: 1101111

for User SRAM: 1010111
Pre-bias assembly option

R39 R40 MODE EN63AQQI
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OK X enable pre-bias start-up
X OK disable pre-bias start-up

Soft start 3ms