Title: TE0803 - MIO Banks
A4 Number: TE0803 05EV-1EA
Rev. 02
Date: 2018-05-21 Copyright: Trenz Electronic GmbH / TT
Filename: B_MIO.SchDoc
TE0803 - CLOCK

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Diagram of the circuit schematic with various components labeled and interconnected. The components include resistors, capacitors, and integrated circuits, with specific connections marked for VDD, VSS, PLL_SCL, PLL_SDA, and test points. The diagram also shows the slave address of 1110000 or 1110001.
CHANGES REV01a (20.11.2017):
1) VCU voltage set to 0.9V, R20 changed to 40K, PL_VCU_1V0 renamed to PL_VCU_0.9V.

CHANGES REV02 (20.06.2018):
1) Added LDO to DDR_PLL
2) All differential pairs with length matched with tolerance 0.1mm (excluding package delays)
3) Added MAC EEPROM U28
4) VPS_MGTRAVCC set to 0.85V
5) Added pull-up resistors R68,R69