USB MCU
CY7C68013A-56LFXI

Mini-USB Typ B
USB

24LC128

B2B Hirose DF17 80 Pin

FPGA
XC3S1200E-4FGG320I

JTAG Header
14 pin Xilinx

Configuration
SPI Flash 16 - 64MBIT

SPI Header
14 pin Xilinx
If possible

512MBIT DDR SDRAM

LED

PB

B2B Hirose DF17 80 Pin

TE Logo on Top Layer

LOGO1

LOGO TOP small

PM3

PM1

PM2

PM4

PM5

PM6

M Hole 2.7mm

FGND

512MBit
Place resistors as close to the USB connector as possible.
Route FEEDBACK the same length as bank3 to DDR and back