Title: TE0723 - FPGA RAM

A4 Number: TE0723
TE0723-M Rev. 03

Date: 2016-07-15 Copyright: Trenz Electronic GmbH
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Filename: FPGA_RAM.SchDoc


**Title:** TE0723 - Analog Input

**Date:** 2016-07-15

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**Filename:** Analog.SchDoc

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**Default:**

* AMUX disabled
* Digital buffers disabled

**AIN_BUF = 3.3V then VR_P = 0.9971V**

**From AD Header**

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**Always enabled**

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CHANGES REV02 to REV03

1) changed FTDI-chip on 56pins package
2) changed micro-USB connectors
3) optimized ROM
4) update footprints, full update lib
5) added serial number (traceability pad)