Serialnumber 6.3 x 6.3mm
Date: 18.09.2018

Title: TEB0911 - I2C System

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Filename: I2C_system.schdoc
REV02:
1. 2 CPLDs replaced with LCMXO2-7000HC-4FG484C (U27).
2. Single SFP connector removed. MGT lanes B129, TX/RX, F connected to FMC F.
3. DDCI U45 (power supply of 2xSFP and 1xSFP connectors) removed. 2xSFP powered by U11.
4. HDMI lane B305, TX2 connected to DIP1, TX and lane B305, TX3 to DIP0, TX.
5. Supervisor U69 TPS3106 (33 replaced with TPS3106E09.
6. U17 (85345), IN_SEL0',1 disconnected from CPLD and connected to GND.
8. 12 pull-ups R154/R155 changed from 7.8k to 1.5kOhm.
10. Added fuse F2 (4A). Fuses F1 (1.85A) "not fitted.
11. C534 (PWDR_IN_24V/GND) deleted.
12. Removed clock loop-back OUT9P-N - IN29N (U17, 85345).
13. OUT7 (P) of U17 (clock source 85345) connected to CPLD U27 Bank 5.
14. Signal SD Card detect SD_C0 connected to CPLD. MIO45 connected to CPLD.
15. Signals FMC*_PG_C2M(X2) pulled-up to 3.3VBB (was FMC*_3V3).
16. CLK_OUT(U15 (USB PHY)), serial resistor value R13 changed from 33 to 0 Ohm.
17. ENS31(1Q), Cout value changed from 22µF to 10µF.
18. ROM optimization.
19. Added connections from CPLD to FMC A and F (x_LA06_SC - x_LA33_SC).
20. Not EN_VCCINT connected also to DDCI U31 (5V_SYS_PL) and U46 (5V_SYS_PS).
21. Added 2 2.4F EEPROMU1 (U55/U45).
22. Added power-up sequencing.
23. MIO connected to CPLD.
24. Signals EN_SFP was renamed to EN_SFP, SSD. 12V_FMC was renamed to 12V_FMC, AF. FMC+12V_PG to FMC+12V_PG.
25. Nets names F_LA02/03/04 swapped.

REV03:

REV03a (17.11.2017):
1) Flash N25Q256A11E1240E changed to N25Q512A11G1240E.

REV04:
1) Added SDIO level translator TXS02612RTWR (U95) and 33Ω series resistors on SDIO lines from FPGA side.
2) Added user button S5 and jumper J5 (connected to U27 pin W17).
3) New FAN switches U76-78/U97-99: BTS4141N.
4) New FANs: AFB04512MA. Was added an option to use 5V or 12V (default) power rail.
5) Replaced EOL components:
   - TPS2702LDOVR - TPS22918DBVT (U96).
   - ESDV1J4ULCE/6327TXS1A -> AOZ8808DI-03 (U28).