

1

2

3

4

A

A

B

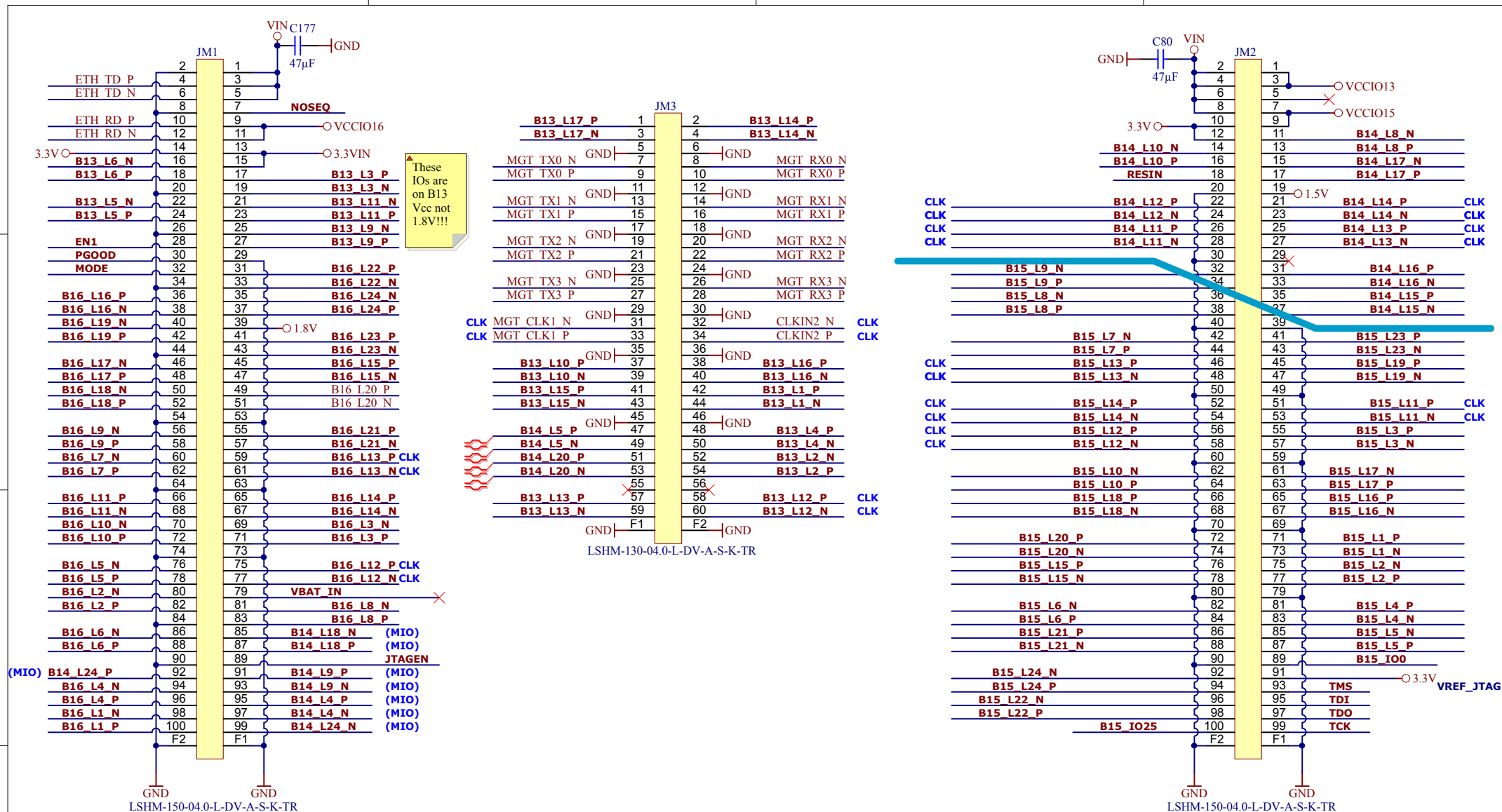
B

C

C

D

D



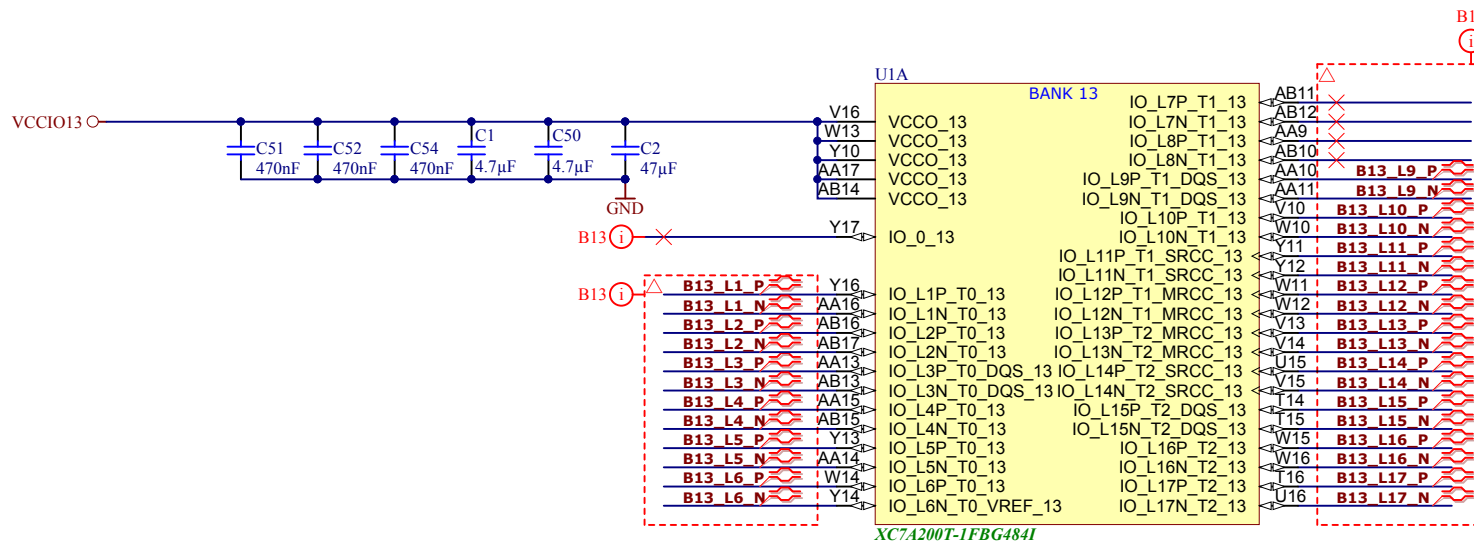
Title: B2B		
A4	Number: TE0712 200_1IC1	Rev. 01
Date: 2014-06-23	Copyright: 2014 Trenz Electronic GmbH	
Page1 of 16		
Filename: B2B-Connectors.SchDoc		

1

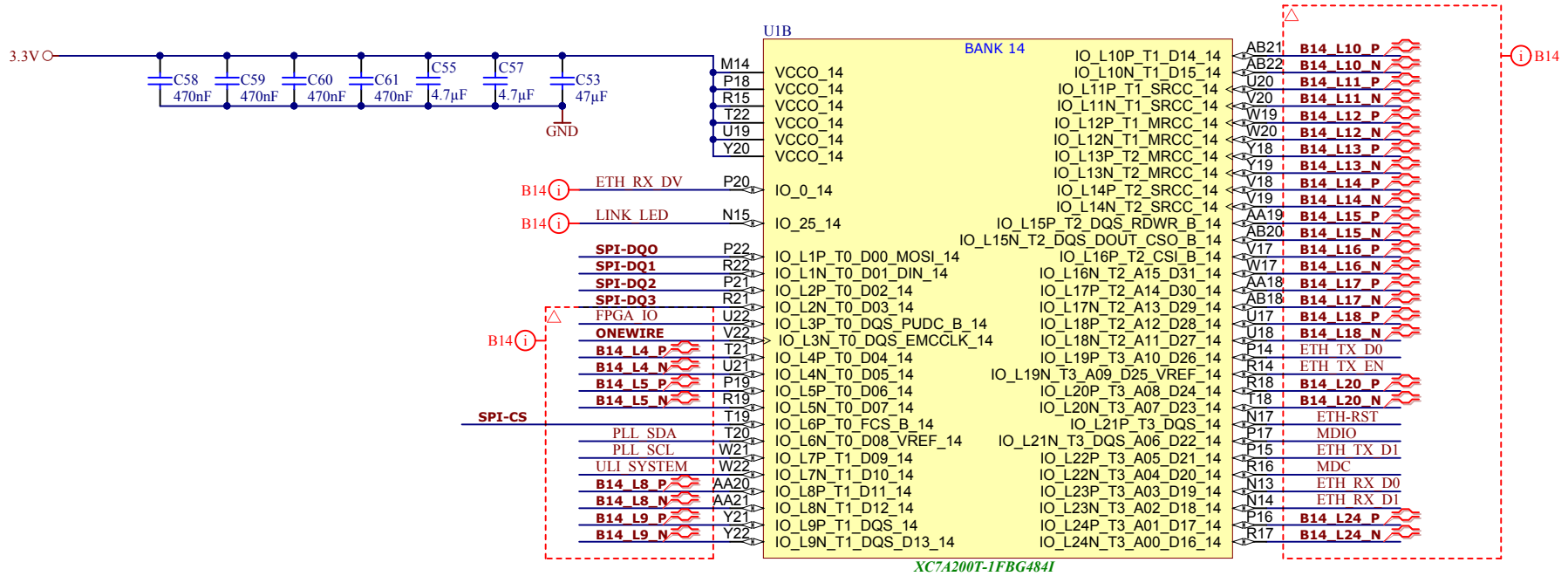
2

3

4



	Title: B13		
	A4	Number: TE0712 200_1IC1	Rev. 01
	Date: 2014-06-23	Copyright: 2014 Trenz Electronic GmbH	Page2 of 16
	Filename: B13.SchDoc		



		Title: B14	
		A4	Number: TE0712 200_1IC1
Date: 2014-06-23		Copyright: 2014 Trenz Electronic GmbH	
Filename: B14.SchDoc		Page3	of 16

1

2

3

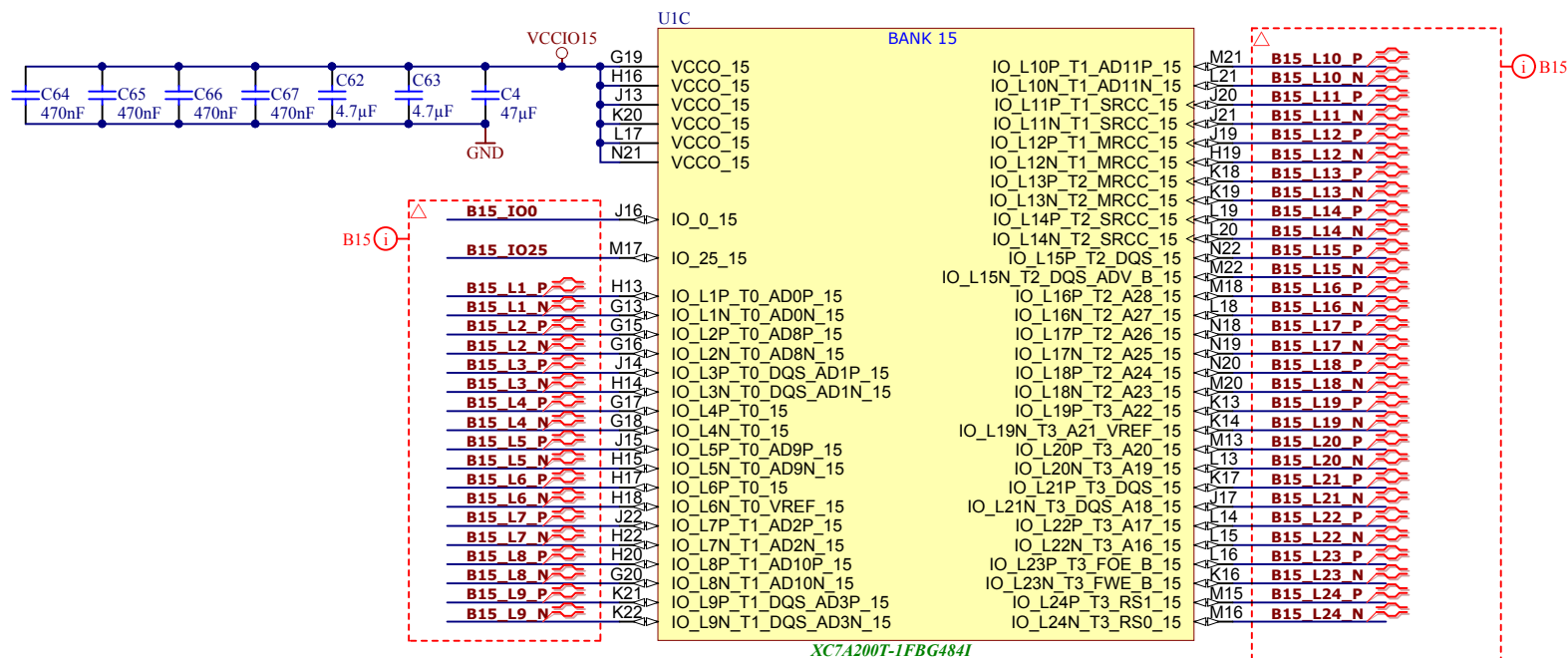
4

A

B

C

D



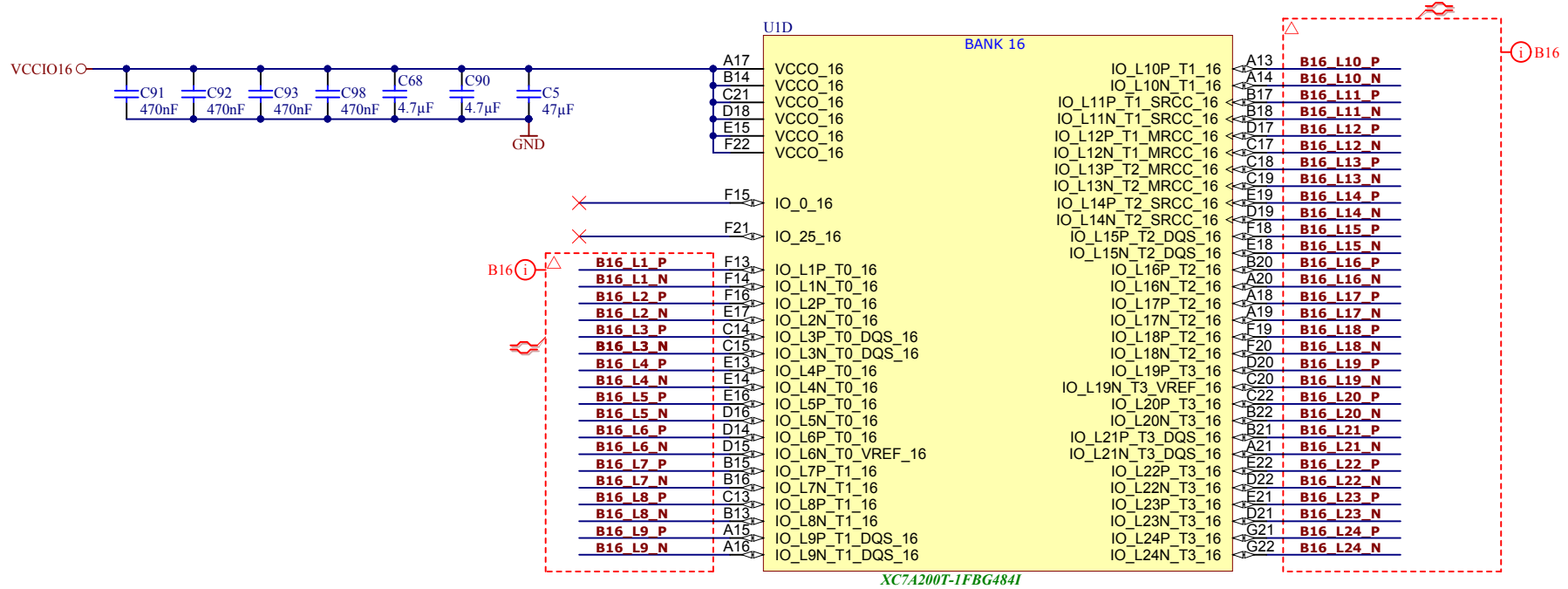
		Title: B15	
		A4	Number: TE0712 200_1IC1
Date: 2014-06-23		Copyright: 2014 Trenz Electronic GmbH	
Filename: B15.SchDoc		Page 4	of 16

1

2

3

4



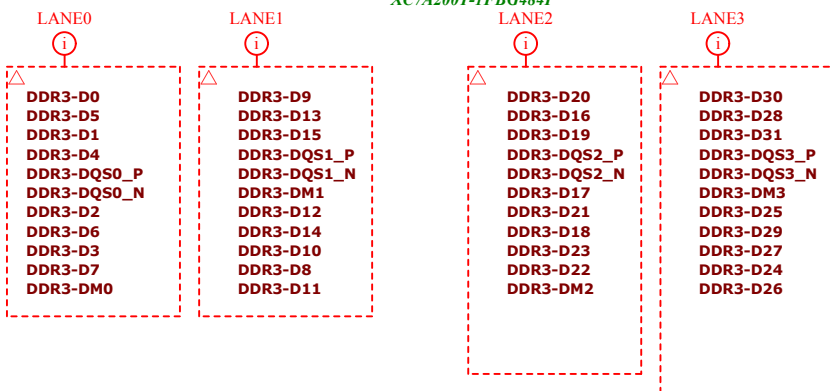
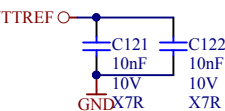
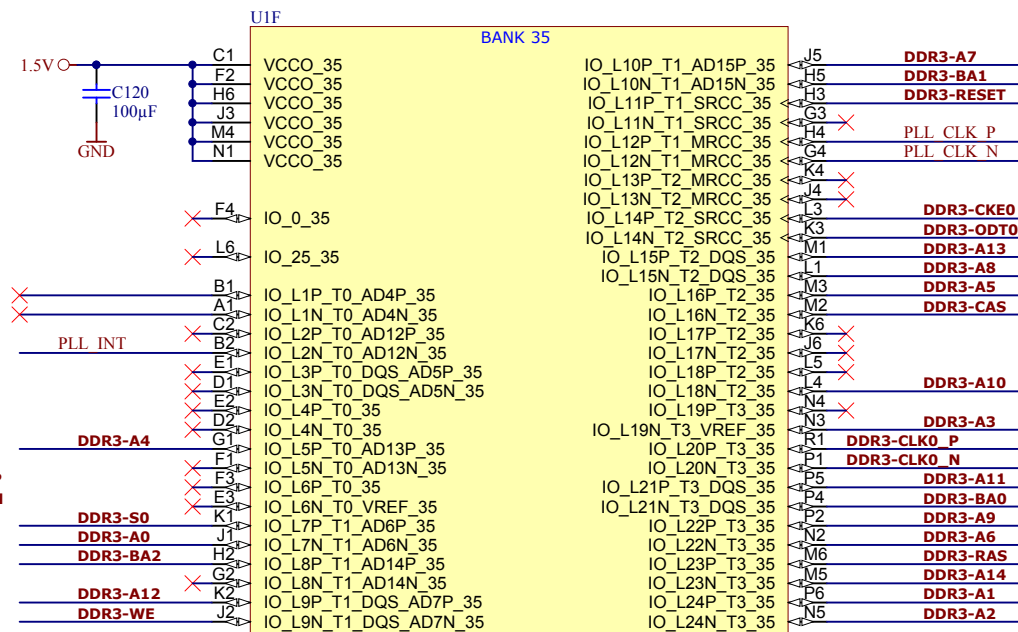
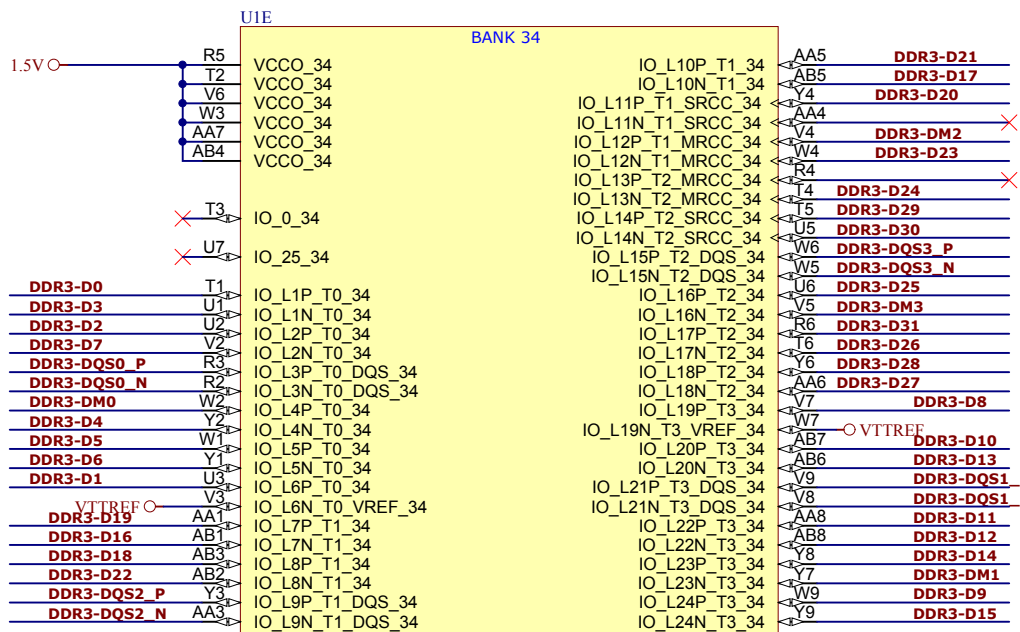
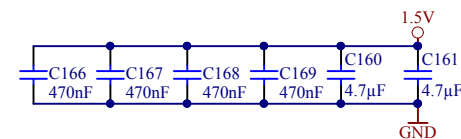
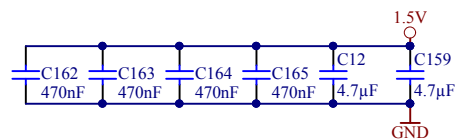
	Title: B16	
	A4	Number: TE0712 200_1IC1
Date: 2014-06-23	Copyright: 2014 Trenz Electronic GmbH	
Filename: B16.SchDoc	Page5	of 16

1

2

3

4



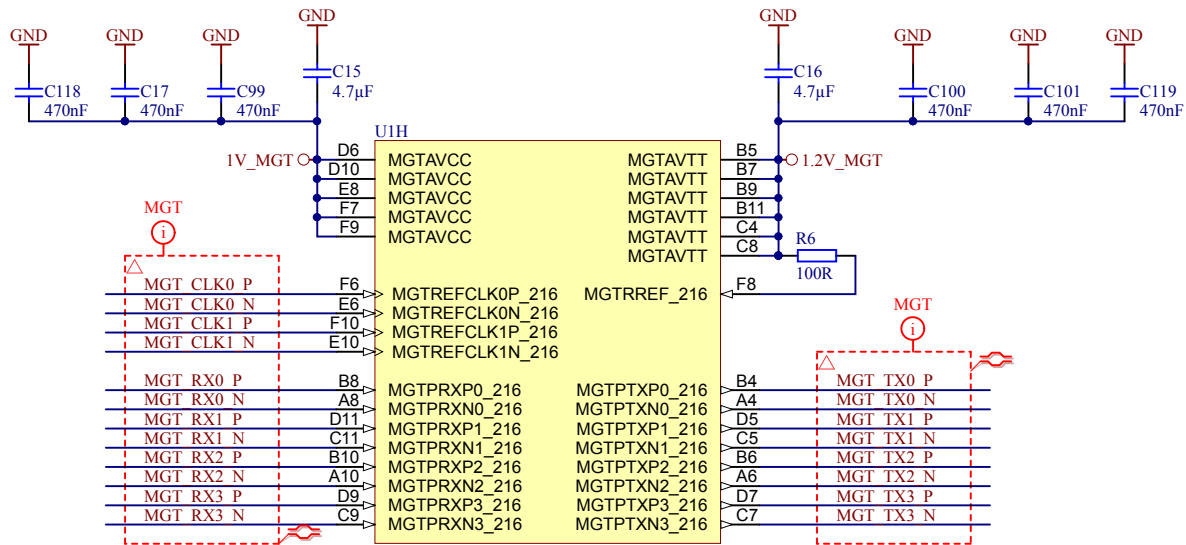
Title: B34		
A4	Number: TE0712 200_1IC1	Rev. 01
Date: 2014-06-23	Copyright: 2014 Trenz Electronic GmbH	
Filename: B34.SchDoc		Page6 of 16

1

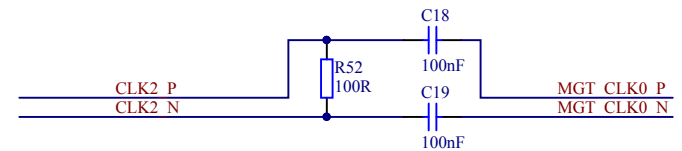
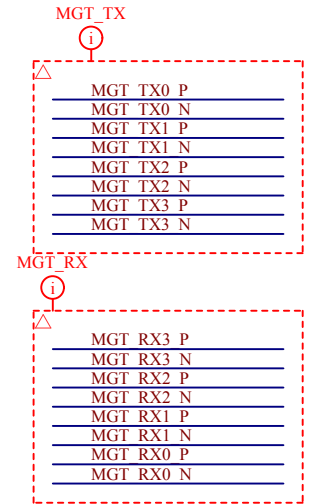
2

3

4



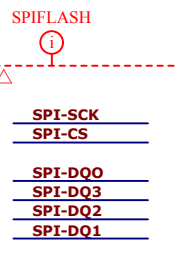
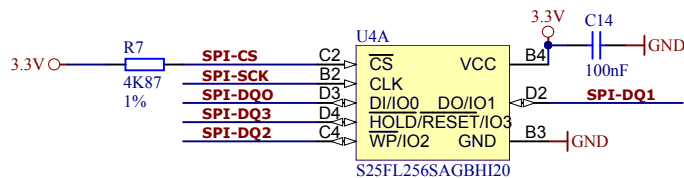
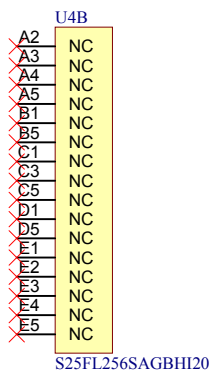
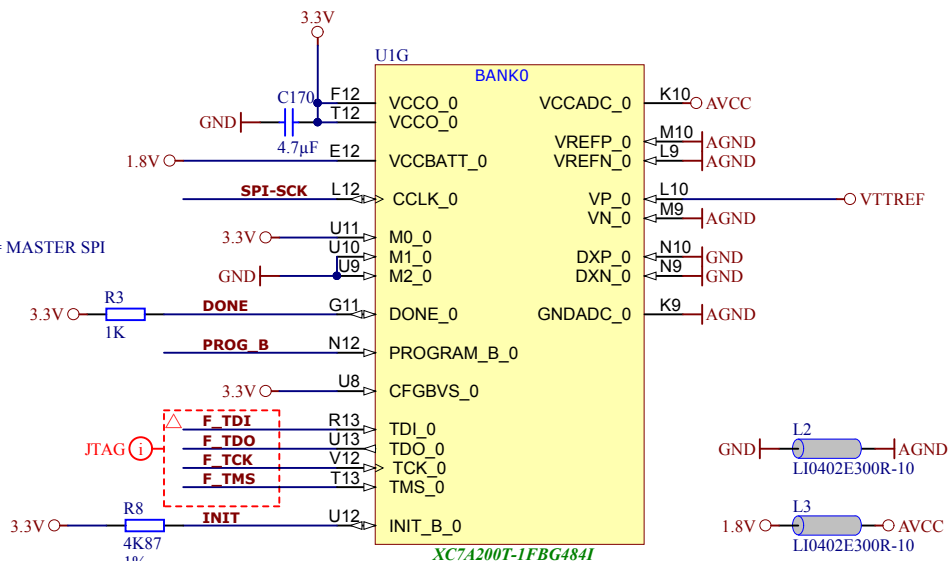
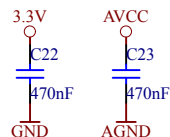
XC7A200T-1FBG484I



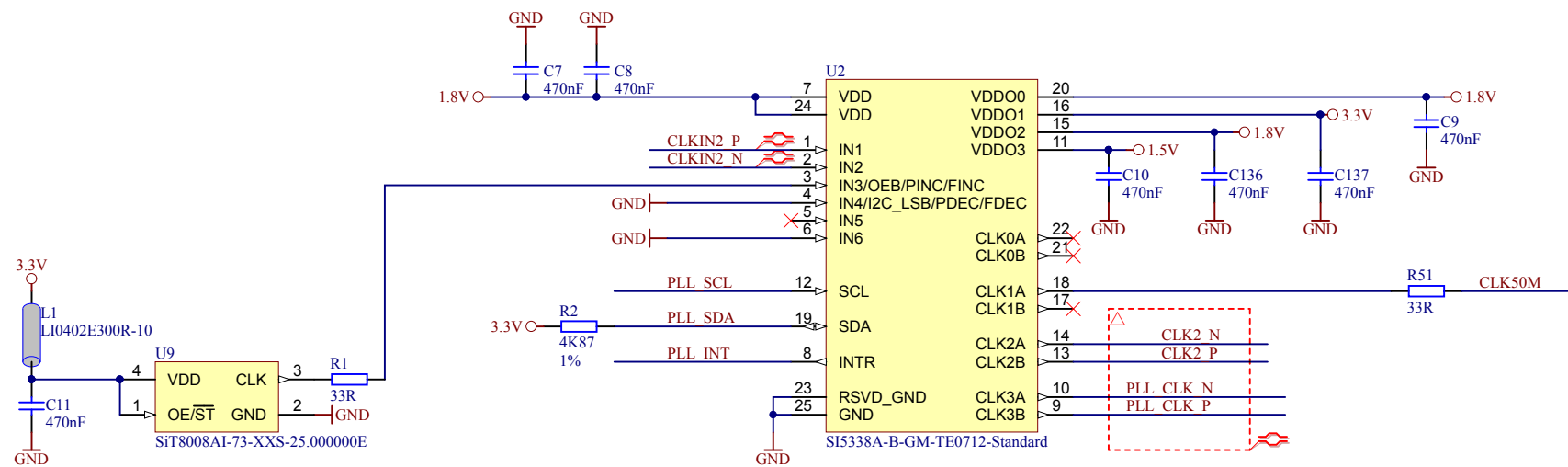
		Title: MGT	
A4	Number: TE0712 200_1IC1	Rev. 01	
Date: 2014-06-23	Copyright: 2014 Trenz Electronic GmbH		Page7 of 16
Filename: FPGA-MGT.SchDoc			



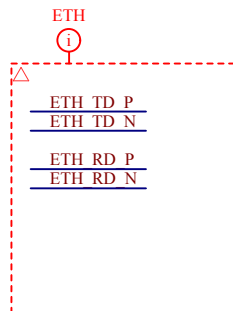
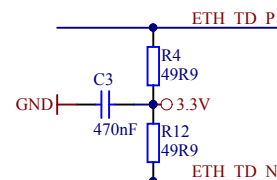
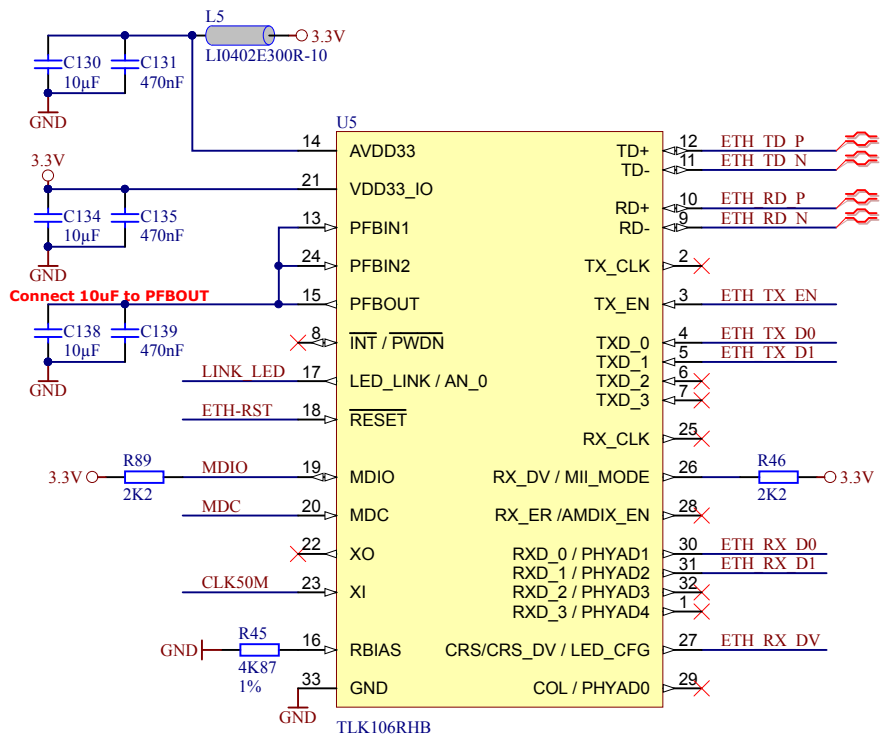
BOOTMODE = MASTER SPI



Title: CFG		
A4	Number: TE0712 200_1IC1	Rev. 01
Date: 2014-06-23	Copyright: 2014 Trenz Electronic GmbH	Page 8 of 16
Filename: FPGA-CFG.SchDoc		



		Title: Clock	
		A4	Rev. 01
Date: 2014-06-23	Copyright: 2014 Trenz Electronic GmbH		Page 10 of 16
Filename: Clock.SchDoc			



Title: ETH		
A4	Number: TE0712 200_11C1	Rev. 01
Date: 2014-06-23	Copyright: 2014 Trenz Electronic GmbH	
Filename: ETHERNET.SchDoc		Page12 of 16

1

2

3

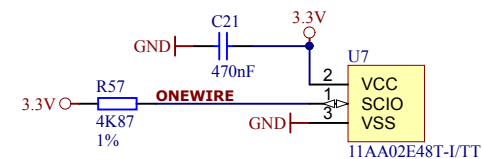
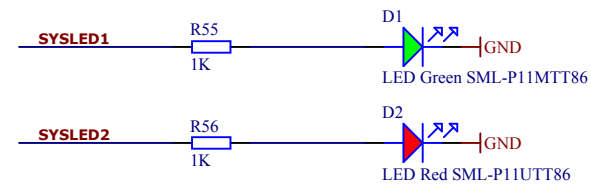
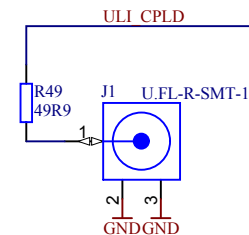
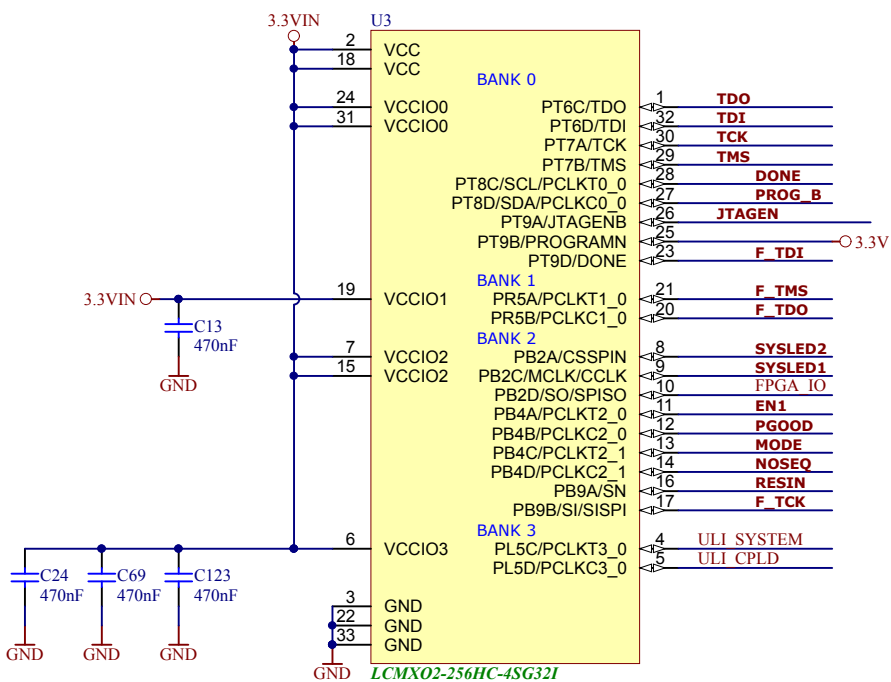
4

A

B

C

D



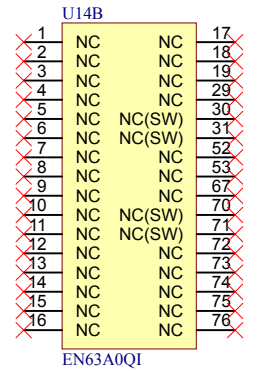
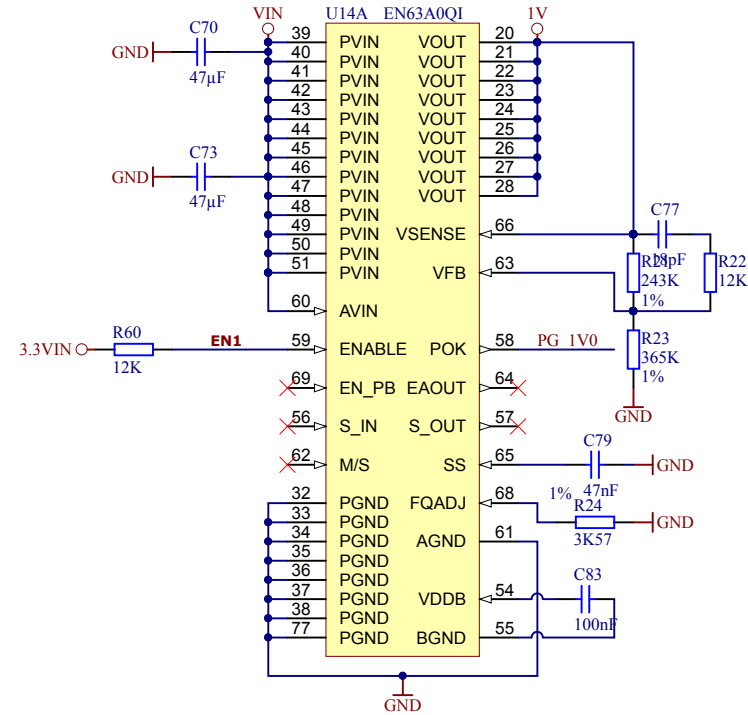
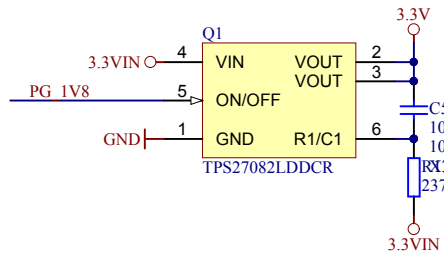
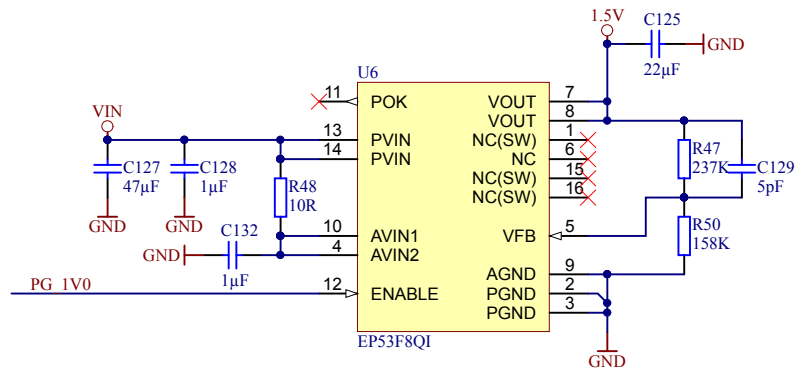
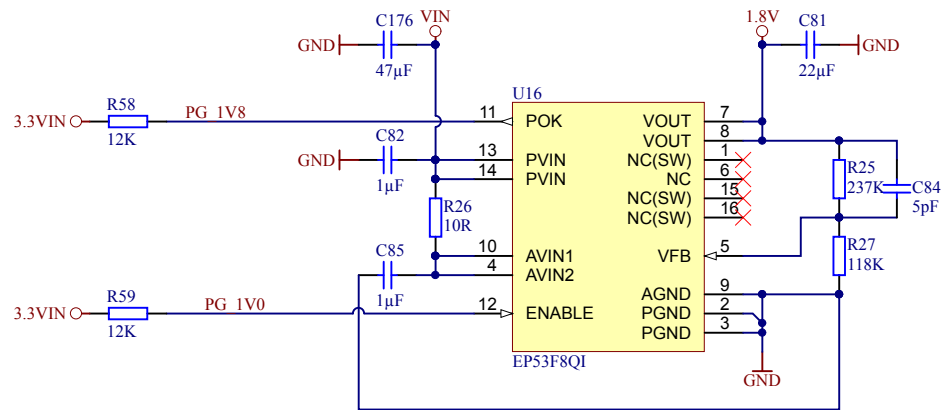
Title: CPLD		
A4	Number: TE0712 200_1IC1	Rev. 01
Date: 2014-06-23	Copyright: 2014 Trenz Electronic GmbH	Page 13 of 16
Filename: CPLD.SchDoc		

1

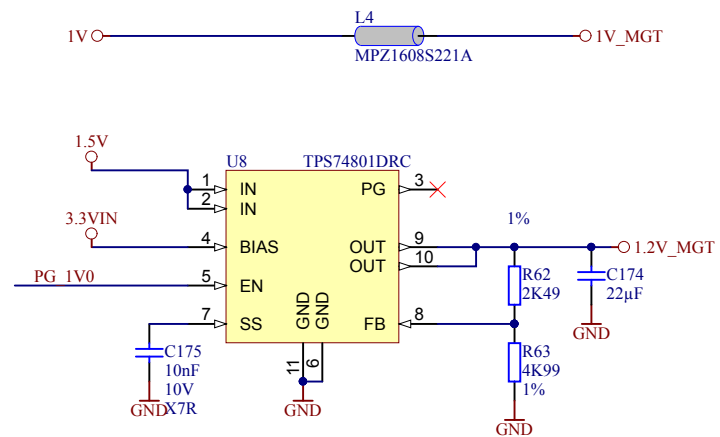
2

3

4



Title: PWR		
A4	Number: TE0712 200_11C1	Rev. 01
Date: 2014-06-23	Copyright: 2014 Trenz Electronic GmbH	
Filename: PWR1.SchDoc		Page 14 of 16



			Title: PWR	
			A4	Number: TE0712 200_1IC1
Date: 2014-06-23		Copyright: 2014 Trenz Electronic GmbH		Rev. 01
Filename: PWR2.SchDoc		Page 15 of 16		

U_B2B-Connectors
B2B-Connectors.SchDoc

U_B13
B13.SchDoc

U_B14
B14.SchDoc

U_B15
B15.SchDoc

U_B16
B16.SchDoc

U_B34
B34.SchDoc

U_FPGA-MGT
FPGA-MGT.SchDoc

U_FPGA-CFG
FPGA-CFG.SchDoc

U_FPGA-PWR
FPGA-PWR.SchDoc

U_Clock
Clock.SchDoc

U_DDR3-RAM
DDR3-RAM.SchDoc

U_ETHERNET
ETHERNET.SchDoc

U_CPLD
CPLD.SchDoc

U_PWR1
PWR1.SchDoc

U_PWR2
PWR2.SchDoc

Top of Board



Screw M3x4



Screw M3x4



Screw M3x4



Screw M3x4



Distance Holder M3x8



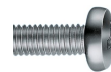
Distance Holder M3x8



Distance Holder M3x8



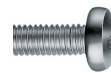
Distance Holder M3x8



Screw M3x6



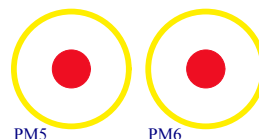
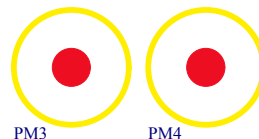
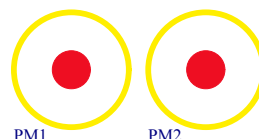
Screw M3x6



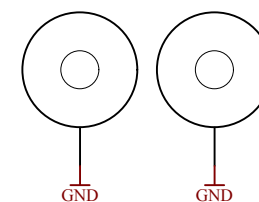
Screw M3x6



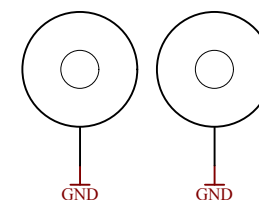
Screw M3x6



Mount.Hole 3.2mm Mount.Hole 3.2mm



Mount.Hole 3.2mmMount.Hole 3.2mm



Title: TE0712		
A4	Number: TE0712 200_11C1	Rev. 01
Date: 2014-06-23	Copyright: 2014 Trenz Electronic GmbH	
Filename: TE0712.SchDoc		Page16 of 16