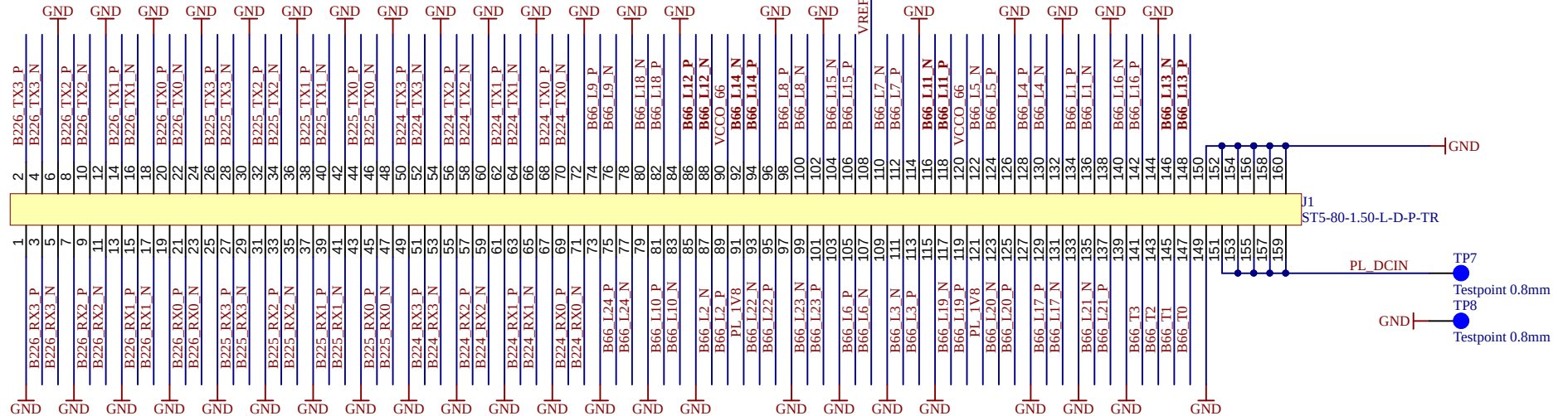
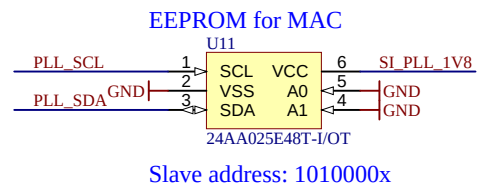
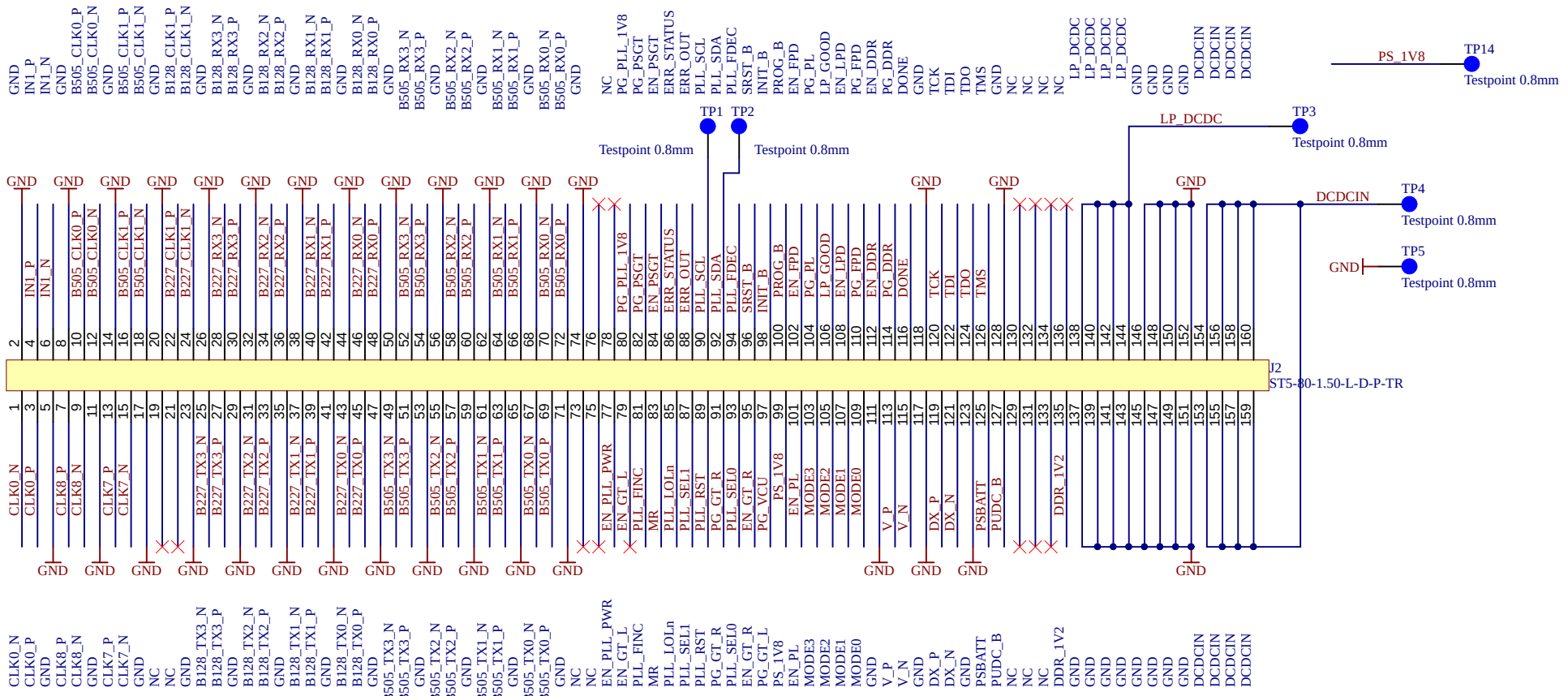




Title: TE0807 - Connector J2		
A4	Number: TE0807 4AI21-A	Rev. 02
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT	Page3 of 28
Filename: J2.SchDoc		

A

B

C

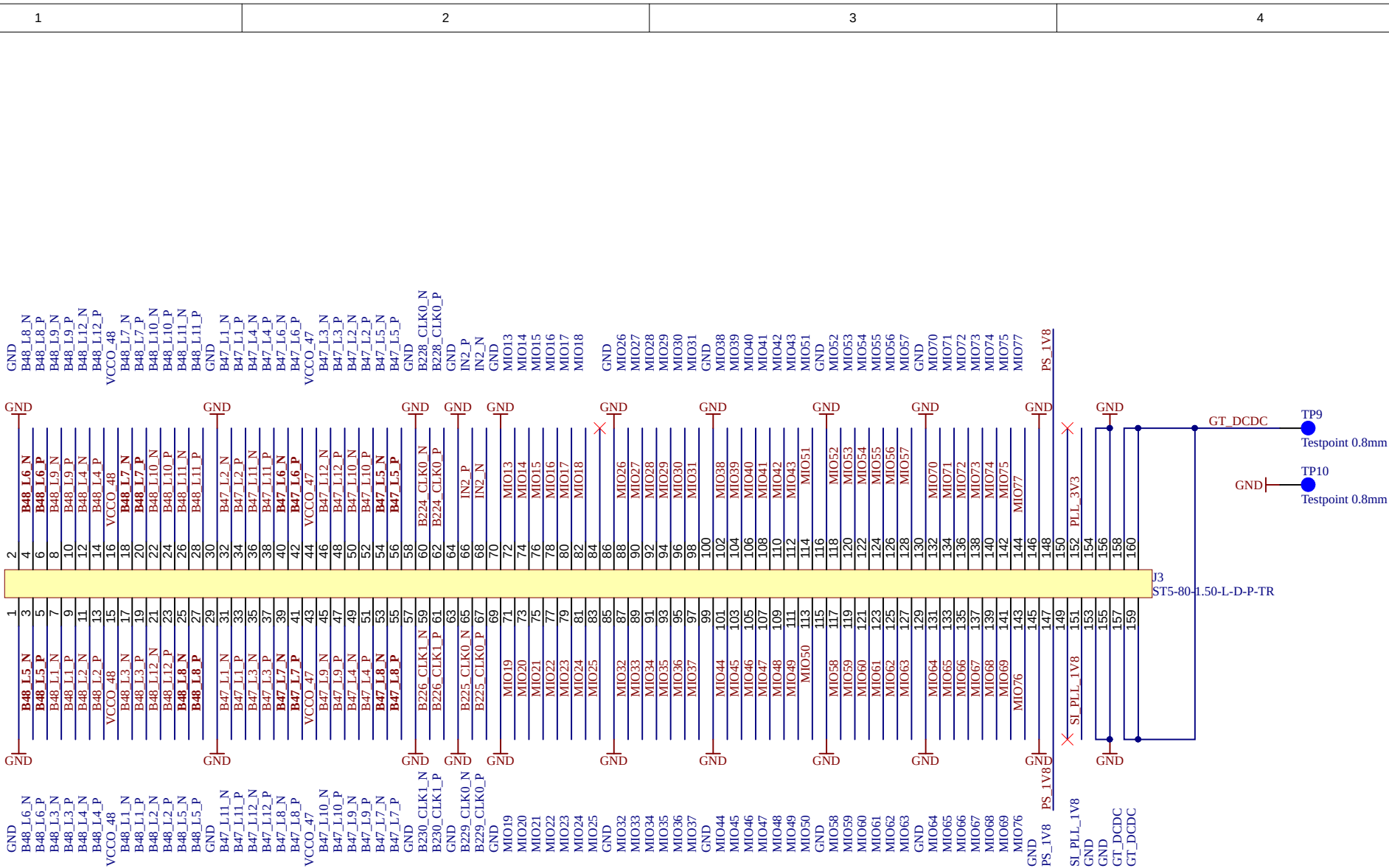
D

A

B

C

D



Title: TE0807 - Connector J3			
A4	Number:	TE0807 4AI21-A	Rev. 02
	Date:	2018-07-09	Page4 of 28
Filename:		J3.SchDoc	

A

B

C

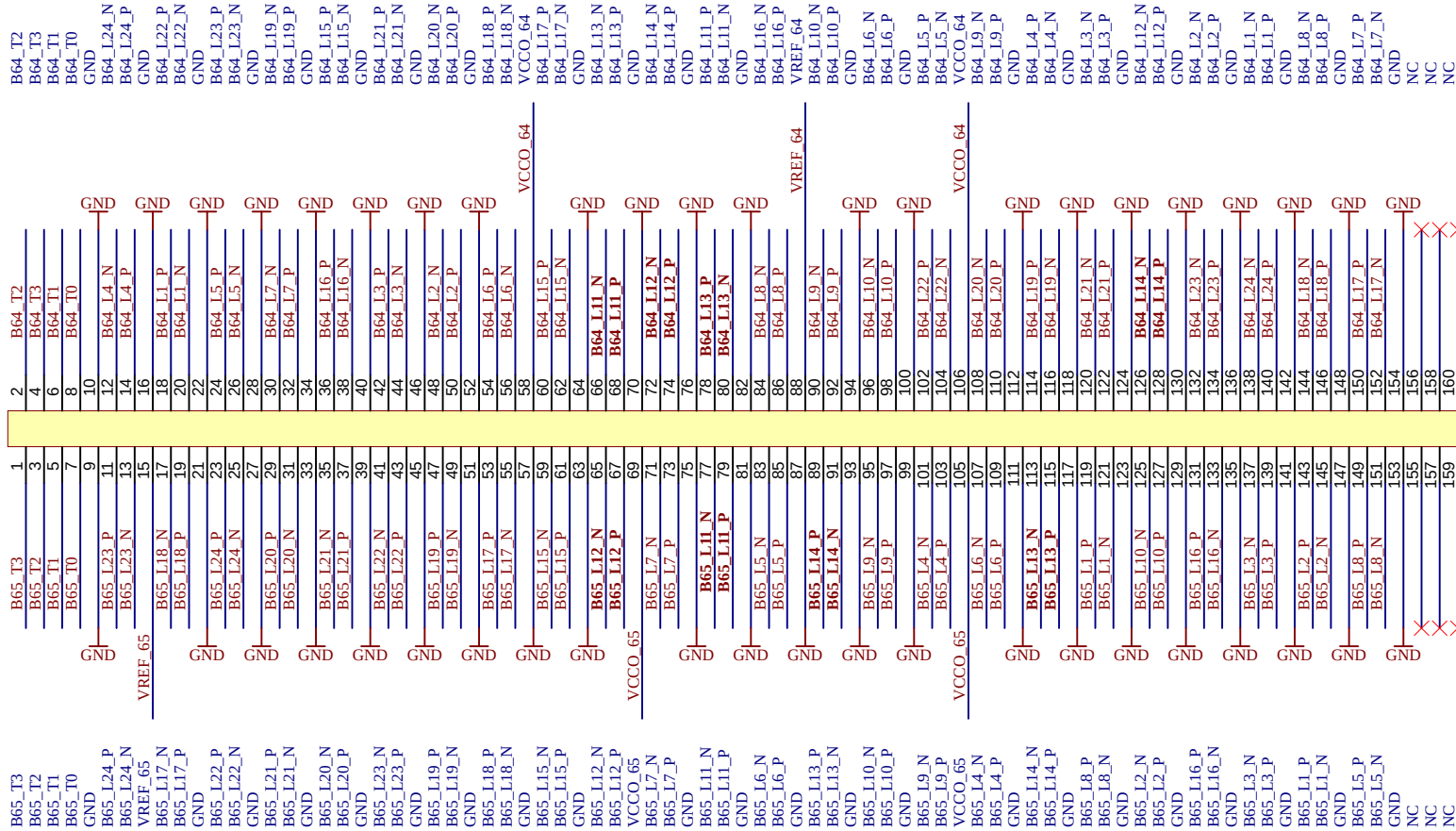
D

1

2

3

4



J4
ST5-80-1.50-L-D-P-TR



Title: TE0807 - Connector J4		
A4	Number: TE0807 4AI21-A	Rev. 02
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT	
Filename: J4.SchDoc		Page5 of 28

1

2

3

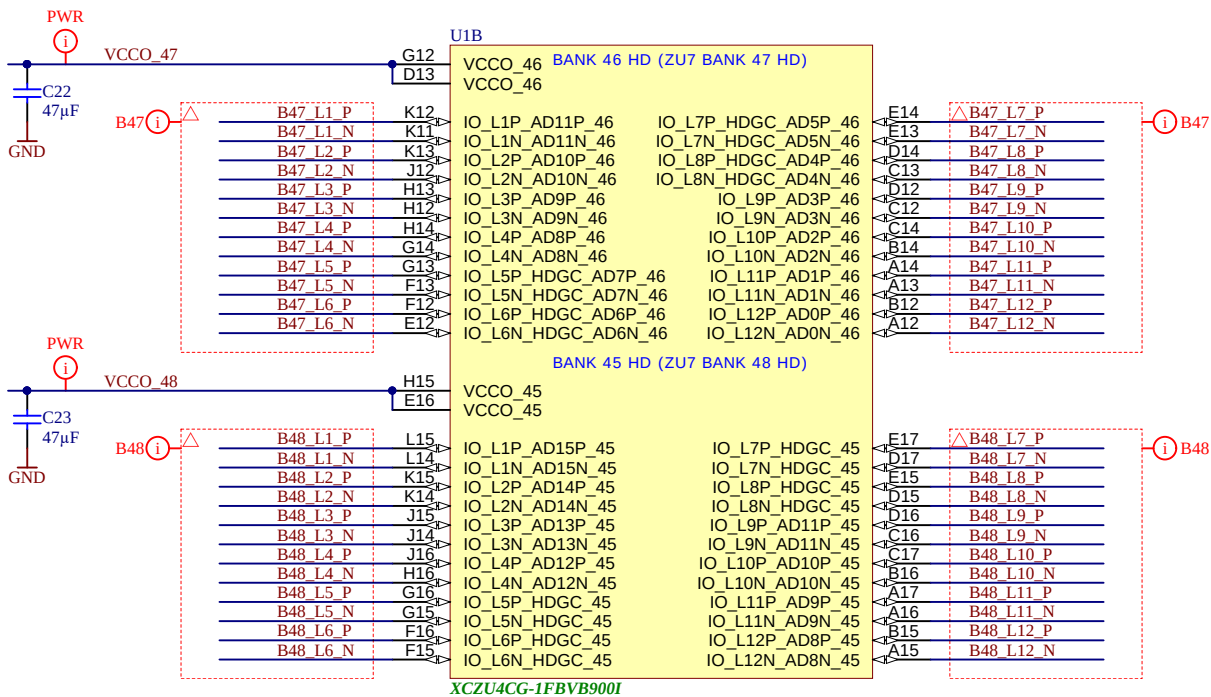
4

A

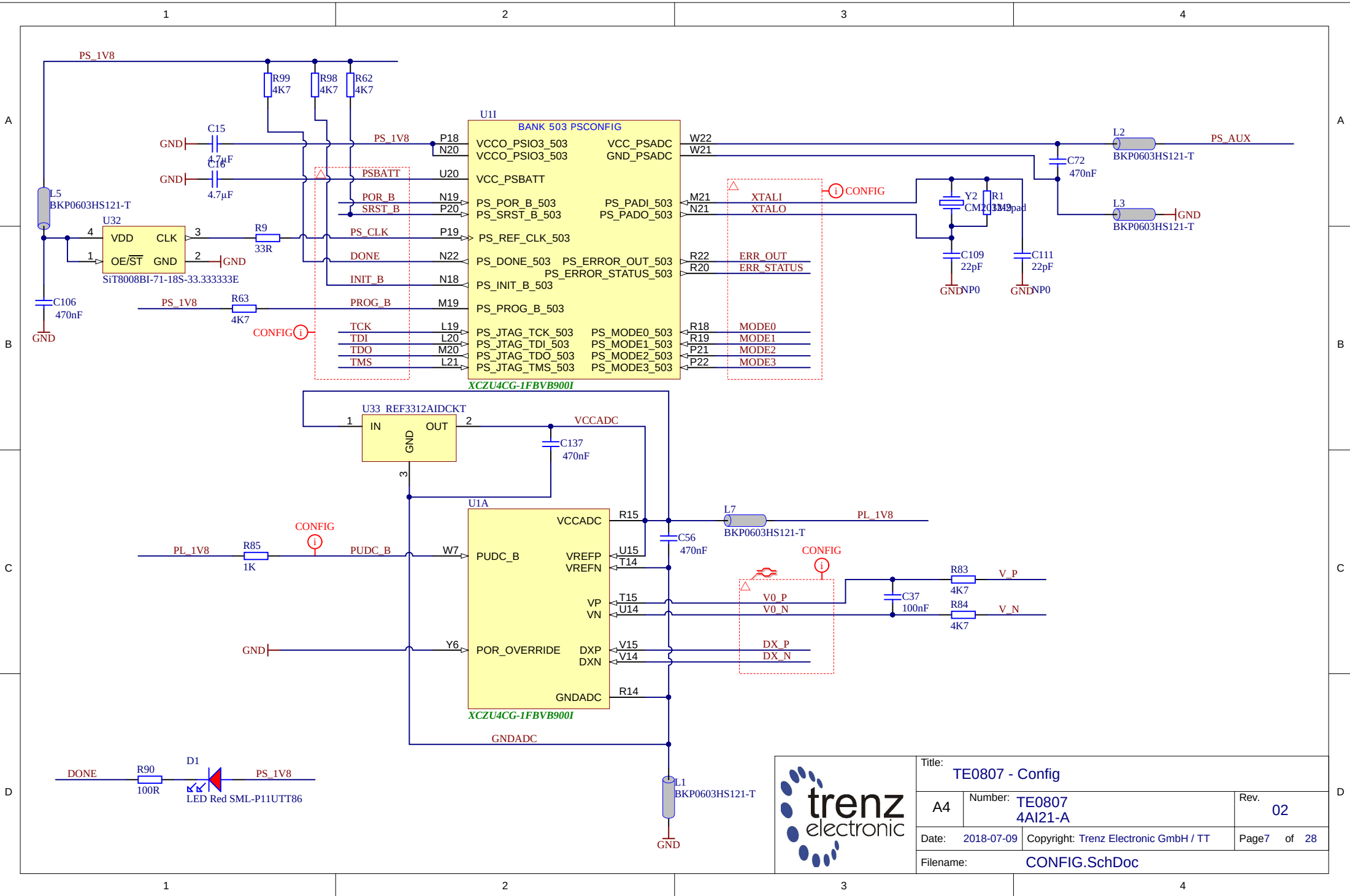
B

C

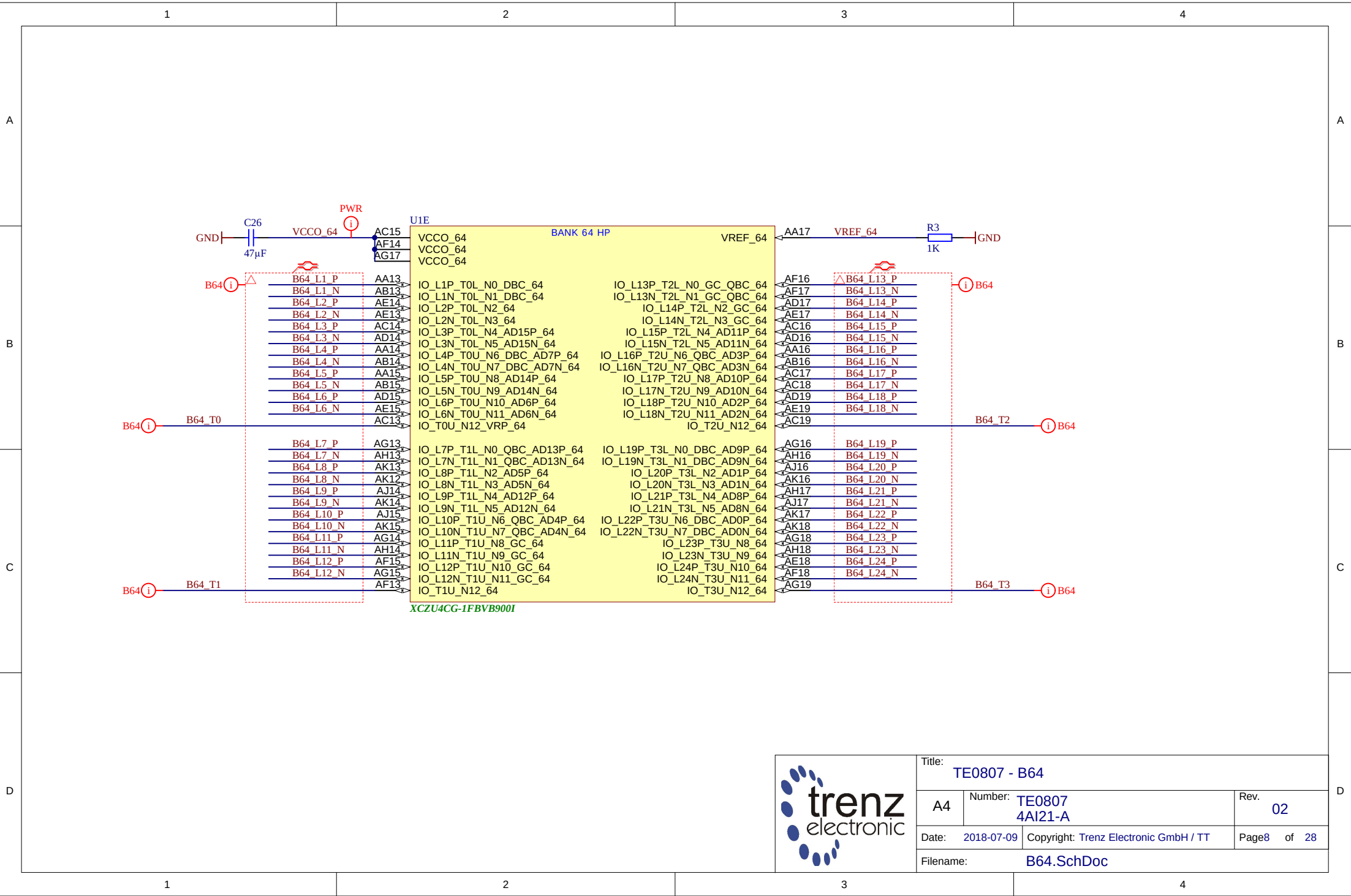
D



Title: TE0807 - B47HD and B48HD			
A4	Number: TE0807 4AI21-A	Rev. 02	
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT		Page 6 of 28
Filename: B_HD.SchDoc			



Title: TE0807 - Config		
A4	Number: TE0807 4A121-A	Rev. 02
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT	Page 7 of 28
Filename: CONFIG.SchDoc		



Title: TE0807 - B64			
A4	Number: TE0807 4AI21-A	Rev. 02	
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT		Page 8 of 28
Filename: B64.SchDoc			

1

2

3

4

A

A

B

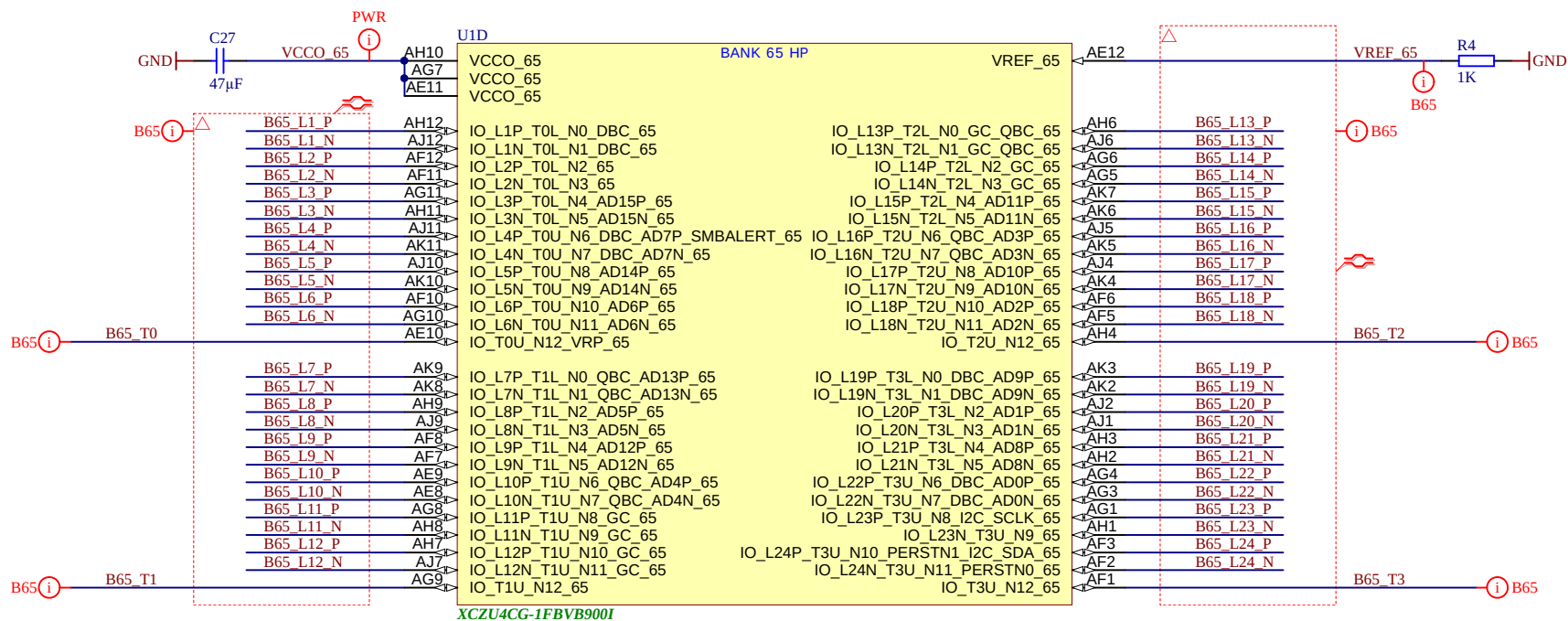
B

C

C

D

D



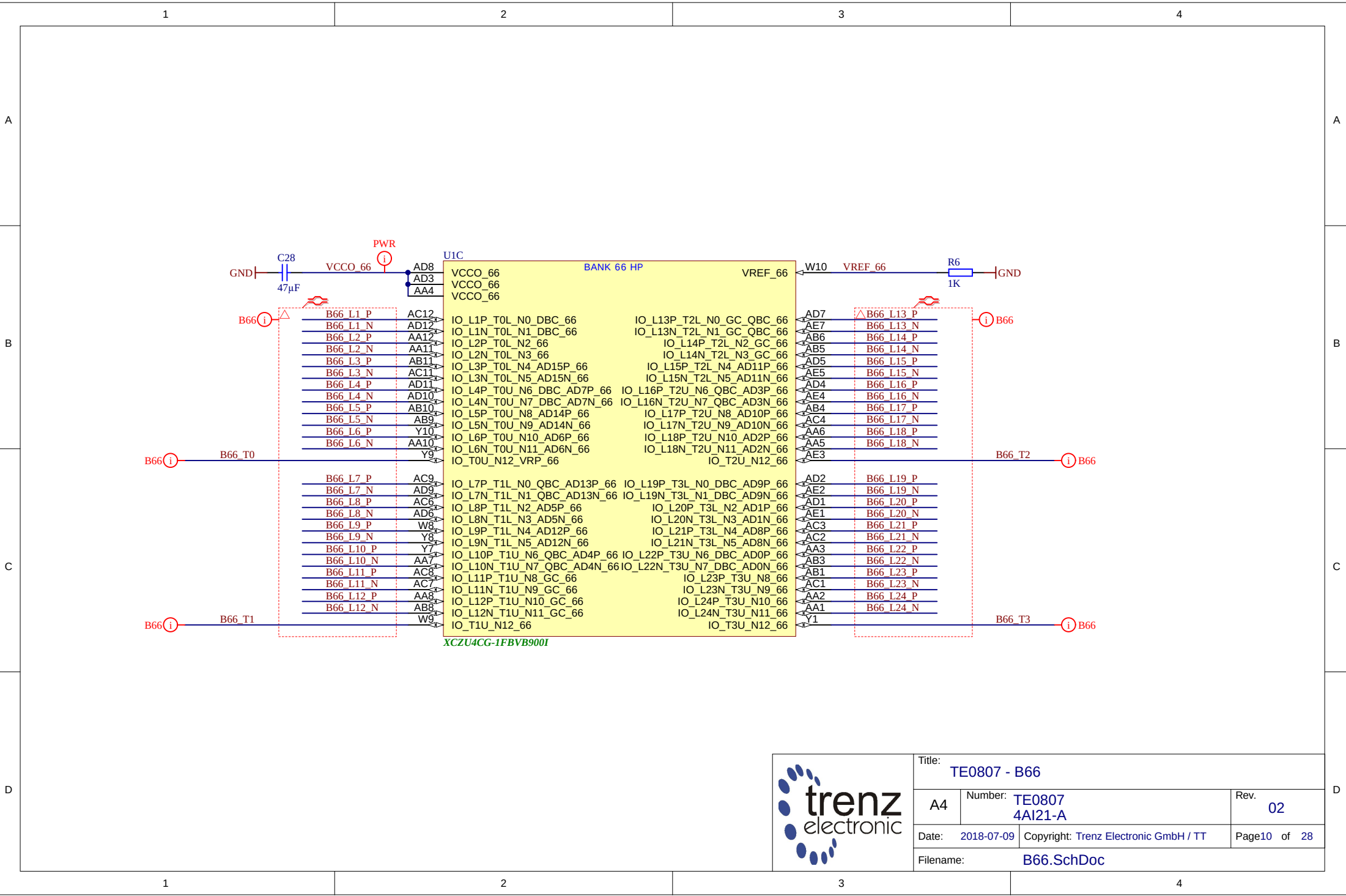
Title: TE0807 - B65			
A4	Number: TE0807 4AI21-A	Rev. 02	
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT		Page 9 of 28
Filename: B65.SchDoc			

1

2

3

4



Title: TE0807 - B66			
A4	Number: TE0807 4AI21-A	Rev. 02	
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT		Page10 of 28
Filename: B66.SchDoc			

GT
①GT
①

U1G

BANK 223 GTH (ZU7 BANK 224)

B224_CLK0_P R8
B224_CLK0_N R7
B224_CLK1_P N8
B224_CLK1_N N7

B224_RX0_P V2
B224_RX0_N V1
B224_RX1_P U4
B224_RX1_N U3
B224_RX2_P T2
B224_RX2_N T1
B224_RX3_P P2
B224_RX3_N P1

MGTHRXN0_223
MGTHRXN1_223
MGTHRXN2_223
MGTHRXN3_223

MGTHRXN0_223
MGTHRXN1_223
MGTHRXN2_223
MGTHRXN3_223

W4 B224_TX0_P
W3 B224_TX0_N
V6 B224_TX1_P
V5 B224_TX1_N
T6 B224_TX2_P
T5 B224_TX2_N
R4 B224_TX3_P
R3 B224_TX3_N

① GT_B224

BANK 224 GTH (ZU7 BANK 225)

B225_CLK0_P L8
B225_CLK0_N L7
B225_CLK1_P J8
B225_CLK1_N J7

B225_RX0_P N4
B225_RX0_N N3
B225_RX1_P M2
B225_RX1_N M1
B225_RX2_P K2
B225_RX2_N K1
B225_RX3_P J4
B225_RX3_N J3

MGTHRXN0_224
MGTHRXN1_224
MGTHRXN2_224
MGTHRXN3_224

MGTHRXN0_224
MGTHRXN1_224
MGTHRXN2_224
MGTHRXN3_224

P6 B225_TX0_P
P5 B225_TX0_N
M6 B225_TX1_P
M5 B225_TX1_N
L4 B225_TX2_P
L3 B225_TX2_N
K6 B225_TX3_P
K5 B225_TX3_N

① GT_B225

BANK 225 GTH (ZU7 BANK 226)

B226_CLK0_P H10
B226_CLK0_N H9
B226_CLK1_P F10
B226_CLK1_N F9

B226_RX0_P H2
B226_RX0_N H1
B226_RX1_P G4
B226_RX1_N G3
B226_RX2_P F2
B226_RX2_N F1
B226_RX3_P E4
B226_RX3_N E3

MGTHRXN0_225
MGTHRXN1_225
MGTHRXN2_225
MGTHRXN3_225

MGTHRXN0_225
MGTHRXN1_225
MGTHRXN2_225
MGTHRXN3_225

H6 B226_TX0_P
H5 B226_TX0_N
G8 B226_TX1_P
G7 B226_TX1_N
F6 B226_TX2_P
F5 B226_TX2_N
E8 B226_TX3_P
E7 B226_TX3_N

① GT_B226

BANK 226 GTH (ZU7 BANK 227)

B227_CLK0_P D10
B227_CLK0_N D9
B227_CLK1_P B10
B227_CLK1_N B9

B227_RX0_P D2
B227_RX0_N D1
B227_RX1_P C4
B227_RX1_N C3
B227_RX2_P B2
B227_RX2_N B1
B227_RX3_P A4
B227_RX3_N A3

MGTHRXN0_226
MGTHRXN1_226
MGTHRXN2_226
MGTHRXN3_226

MGTHRXN0_226
MGTHRXN1_226
MGTHRXN2_226
MGTHRXN3_226

D6 B227_TX0_P
D5 B227_TX0_N
C8 B227_TX1_P
C7 B227_TX1_N
B6 B227_TX2_P
B5 B227_TX2_N
A8 B227_TX3_P
A7 B227_TX3_N

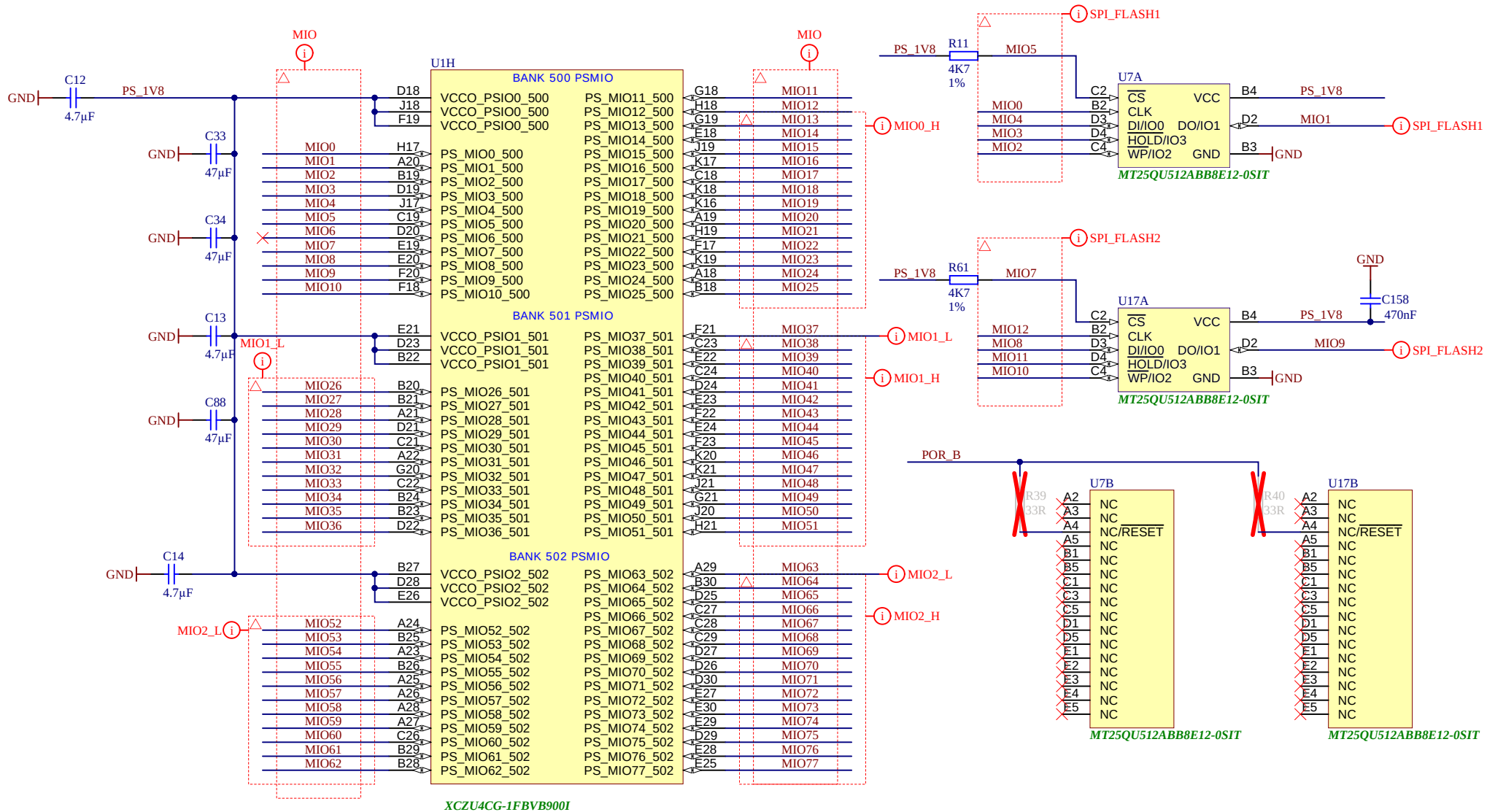
① GT_B227

XCZU4CG-1FBVB9001

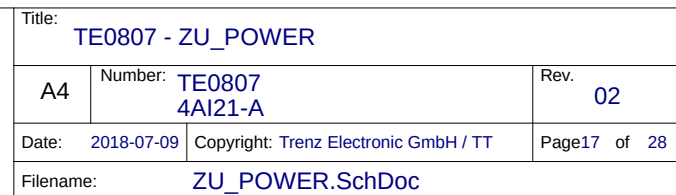


Title: TE0807 - GTH_Banks

A4	Number: TE0807 4A121-A	Rev. 02
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT	Page11 of 28
Filename: B_GT.SchDoc		



		Title: TE0807 - MIO Banks	
		A4 Number: TE0807 4AI21-A	Rev. 02
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT		Page13 of 28
Filename: B_MIO.SchDoc			

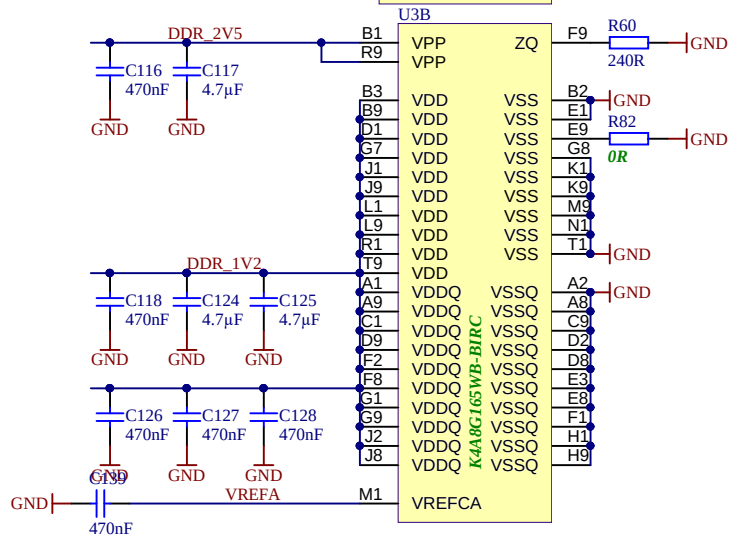
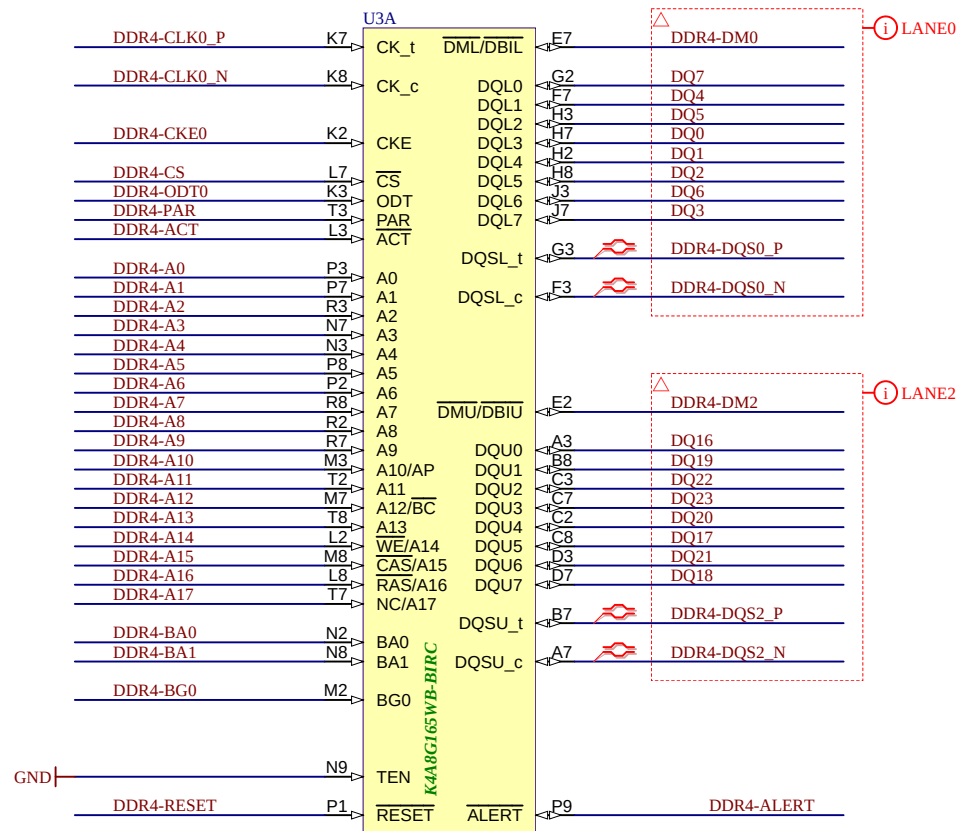


1

2

3

4



Title: TE0807 - DDR4_2_RAM		
A4	Number: TE0807 4AI21-A	Rev. 02
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT	Page19 of 28
Filename: DDR4-RAM_2.SchDoc		

1

2

3

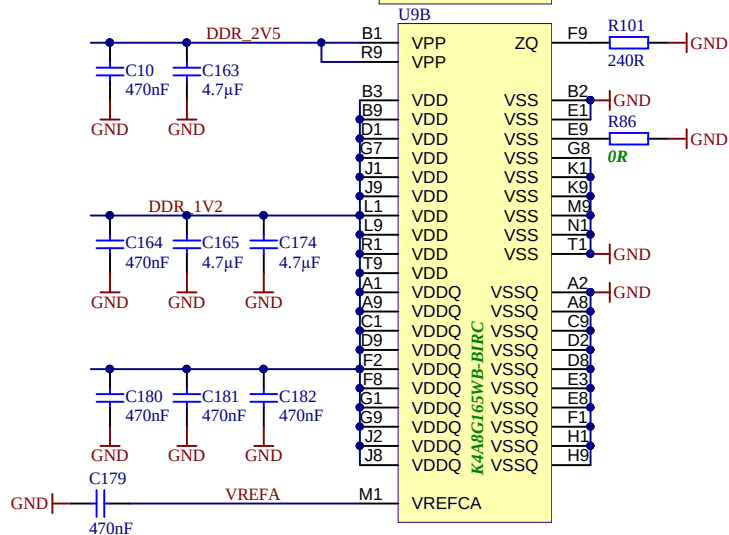
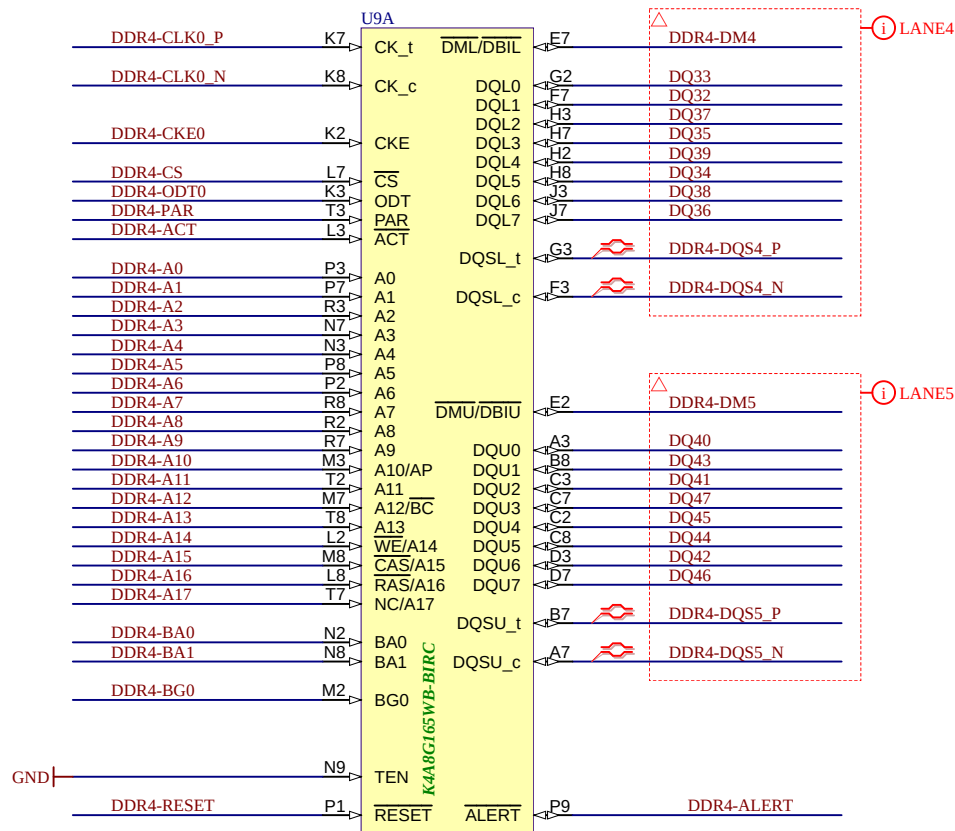
4

1

2

3

4



Title: TE0807 - DDR4_3_RAM		
A4	Number: TE0807 4AI21-A	Rev. 02
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT	Page20 of 28
Filename: DDR4-RAM_3.SchDoc		

1

2

3

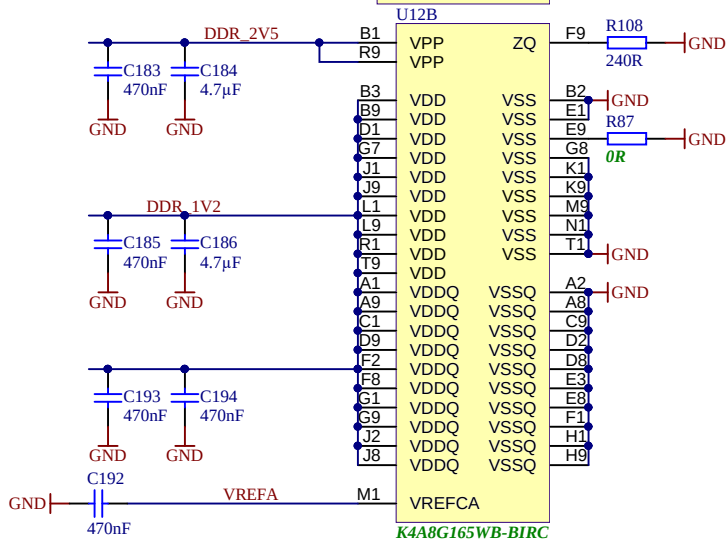
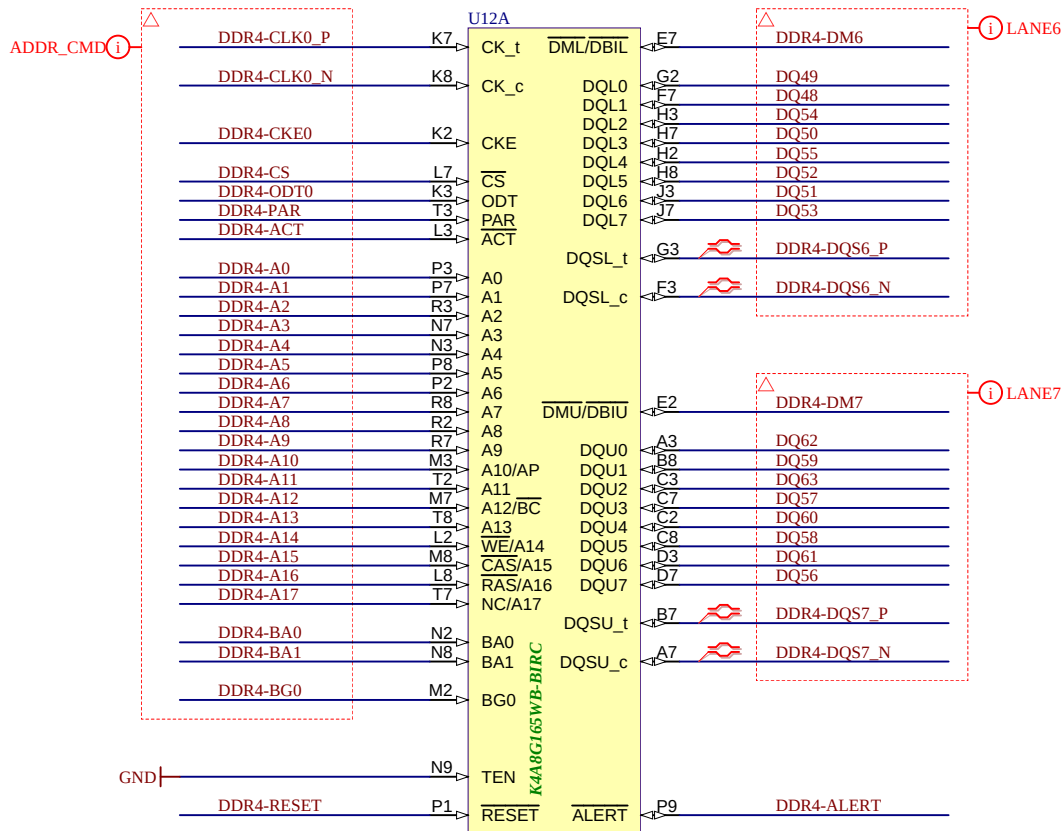
4

1

2

3

4



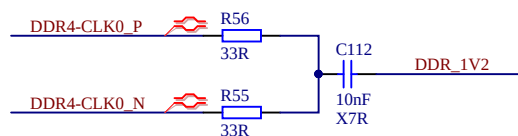
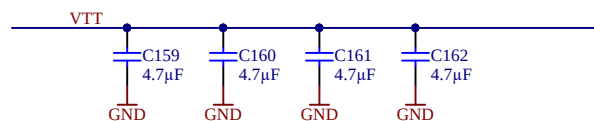
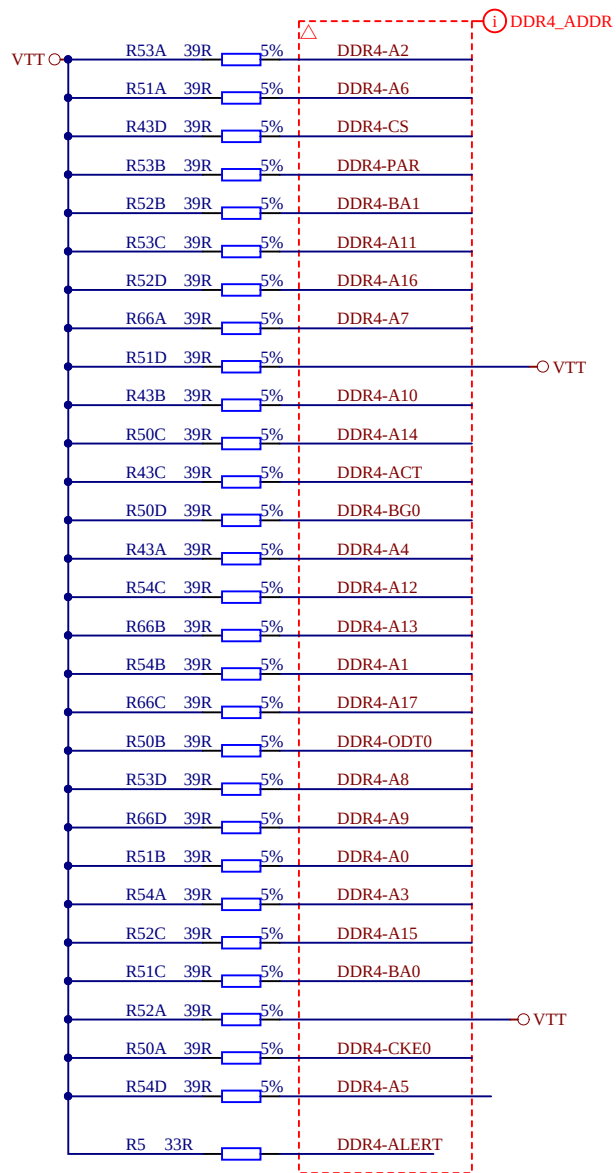
Title: TE0807 - DDR4_4_RAM			
A4	Number: TE0807 4AI21-A	Rev. 02	
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT		Page21 of 28
Filename: DDR4-RAM_4.SchDoc			

1

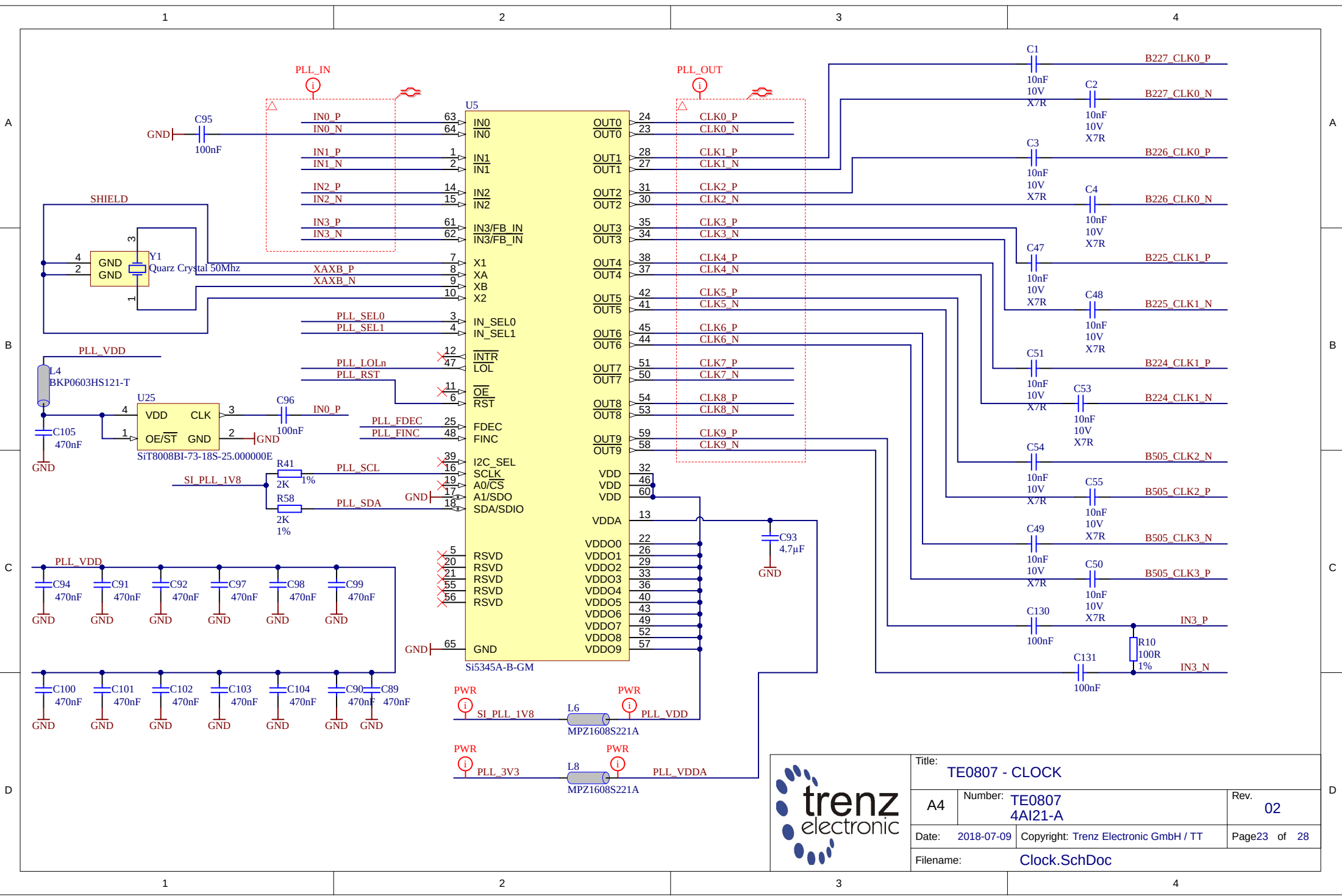
2

3

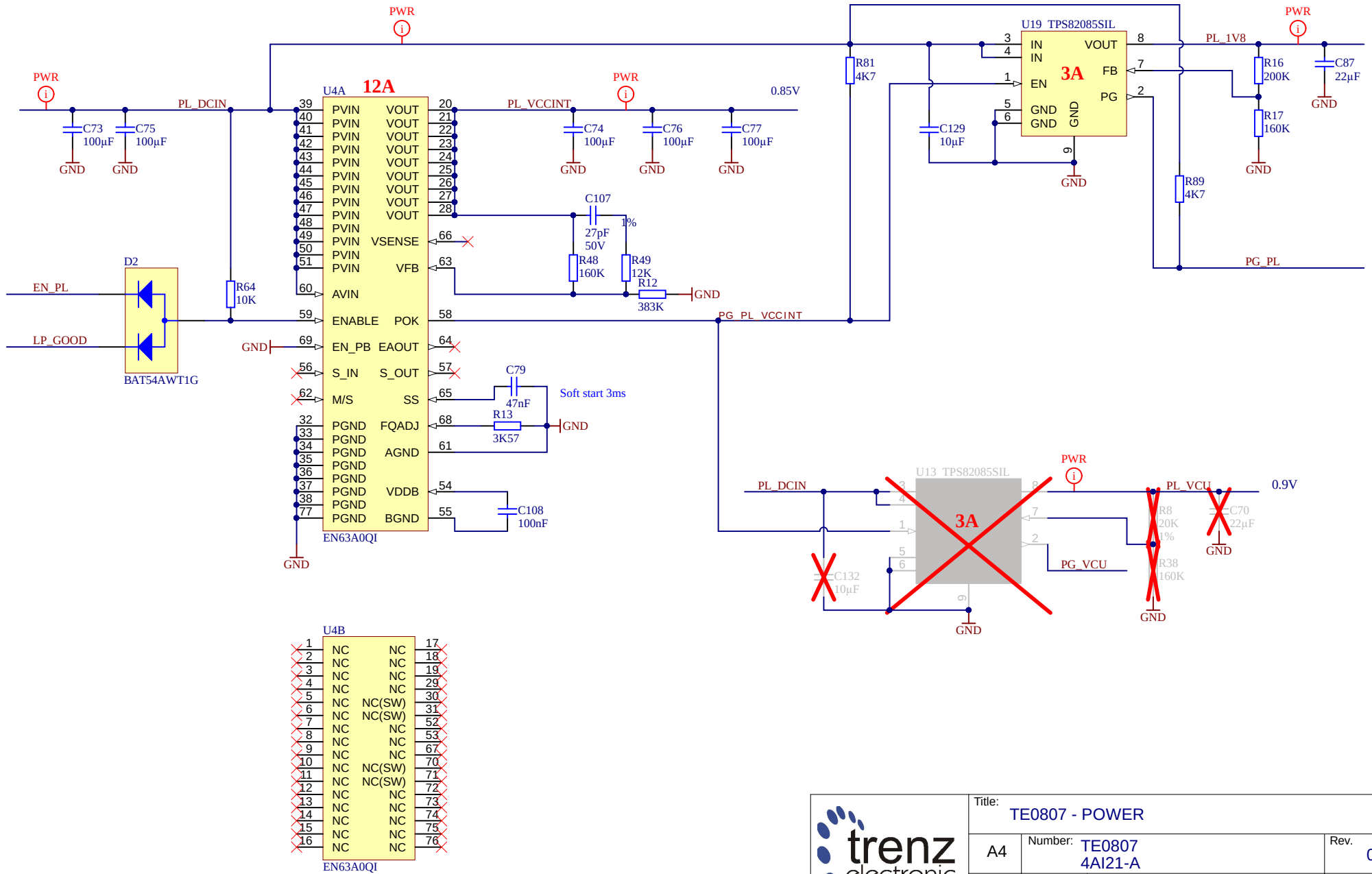
4



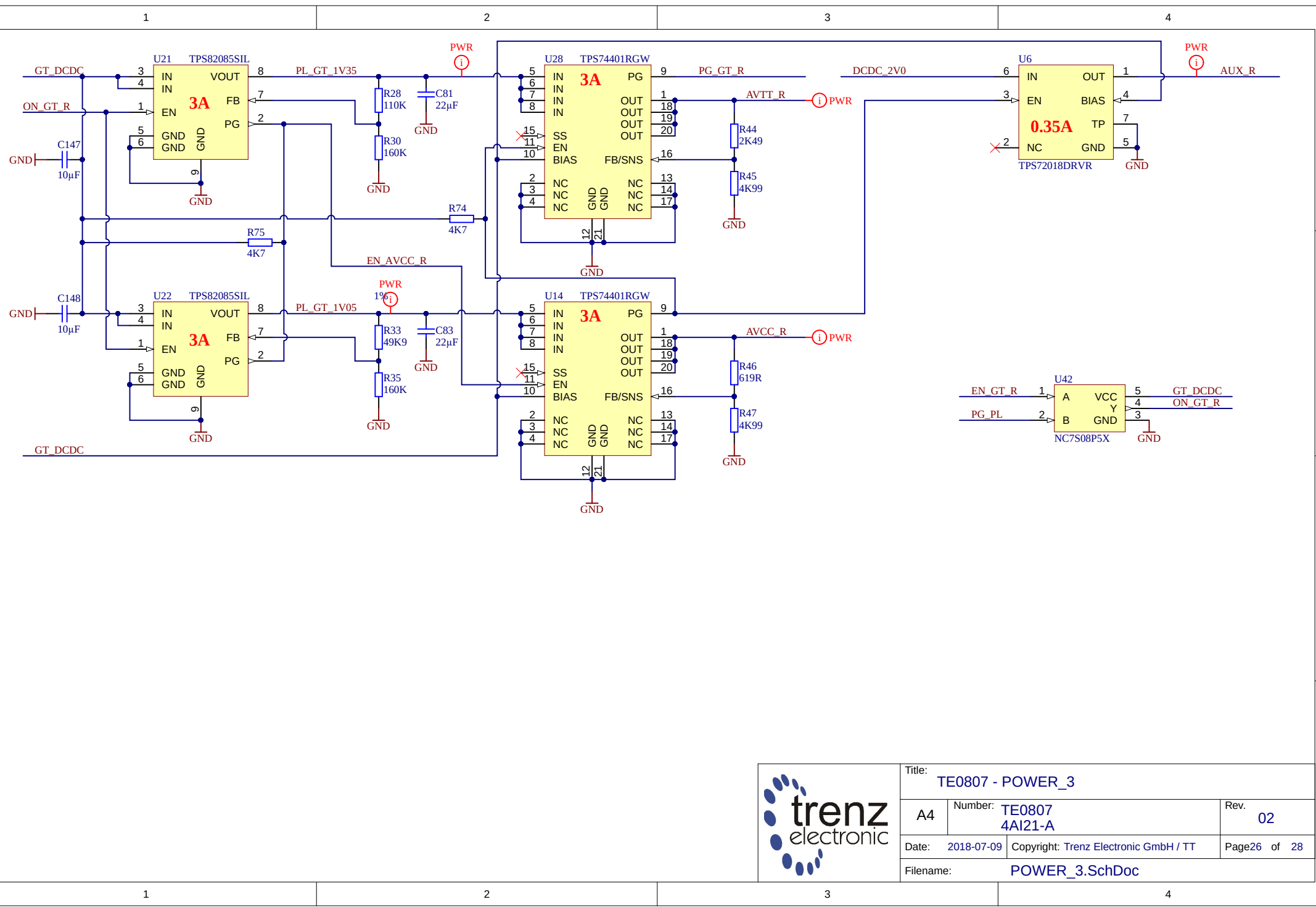
	Title: TE0807 - DDR4_TERM	
	A4	Rev. 02
Date: 2018-07-09	Number: TE0807 4AI21-A	Page22 of 28
Filename: DDR4-TERM.SchDoc		



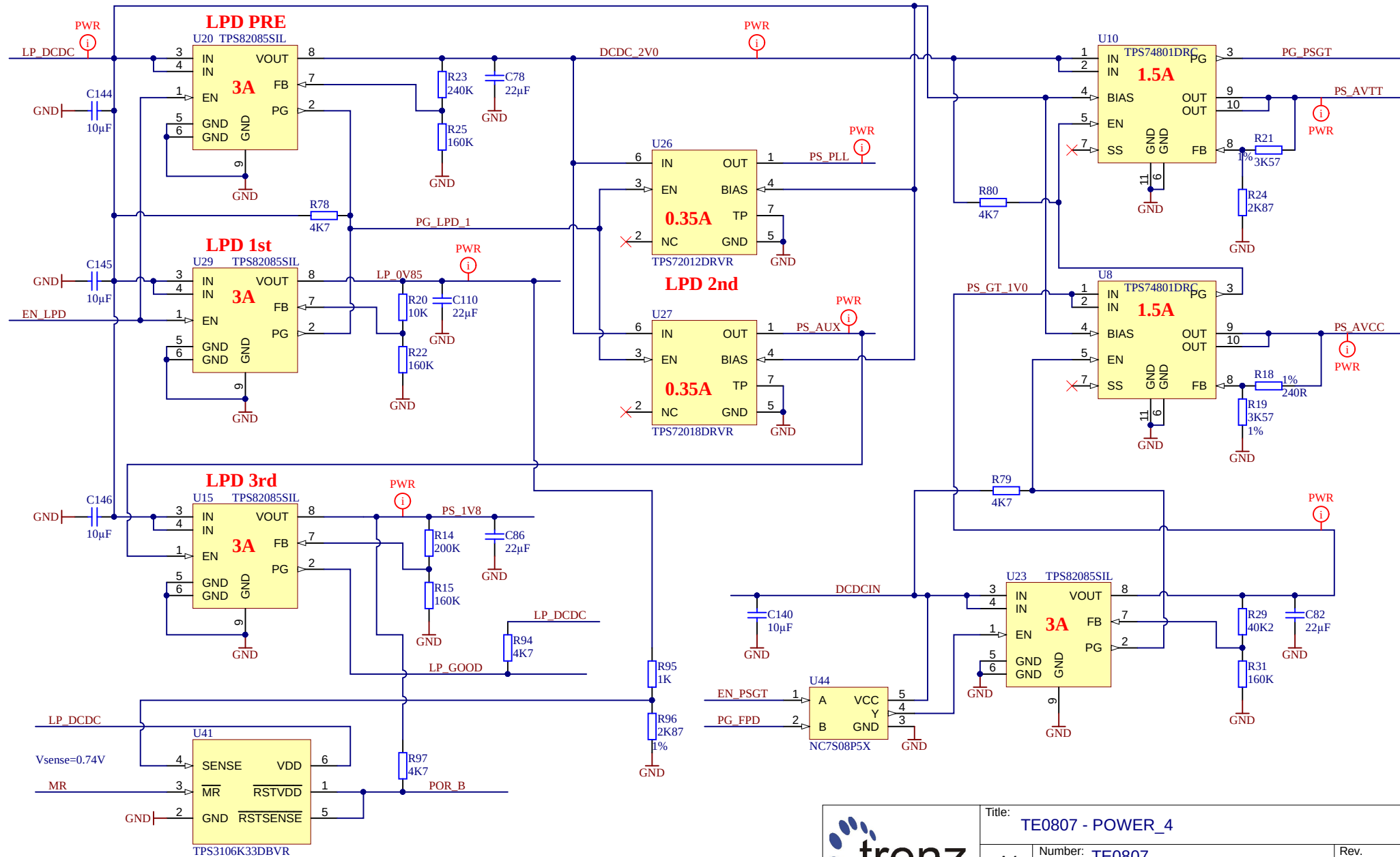
Title: TE0807 - CLOCK			
A4	Number: TE0807 4A121-A	Rev. 02	
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT		Page23 of 28
Filename: Clock.SchDoc			



Title: TE0807 - POWER		
A4	Number: TE0807 4A121-A	Rev. 02
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT	Page24 of 28
Filename: POWER.SchDoc		



	Title: TE0807 - POWER_3		
	A4	Number: TE0807 4AI21-A	Rev. 02
	Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT	Page 26 of 28
	Filename: POWER_3.SchDoc		



Title: TE0807 - POWER_4		
A4	Number: TE0807 4A121-A	Rev. 02
Date: 2018-07-09	Copyright: Trenz Electronic GmbH / TT	
Filename: POWER_4.SchDoc		Page 27 of 28

1

2

3

4

CHANGES REV01 to REV02

- 1) Added EEPROM (U11,24AA025E48T-I/OT)
- 2) Added optional resistors R39/R40 for QSPI flash (do not populated)
- 3) Fixed VCU connection (added additional DCDC and capacitors), recommendation from Xilinx
- 4) Changed obsolete DDR4 chips (NT5AD256M16B2-GN -> K4A8G165WB-BIRC)
- 5) Added net "PG_VCU" to J2 (pin97)
- 6) full update lib

A

B

C

D

		Title: TE0807 - Changes list	
		A4	Number: TE0807 4AI21-A
		Date: 2018-07-09	Rev. 02
		Copyright: Trenz Electronic GmbH	Page28 of 28
		Filename: Revision_Changes.SchDoc	

1

2

3

4