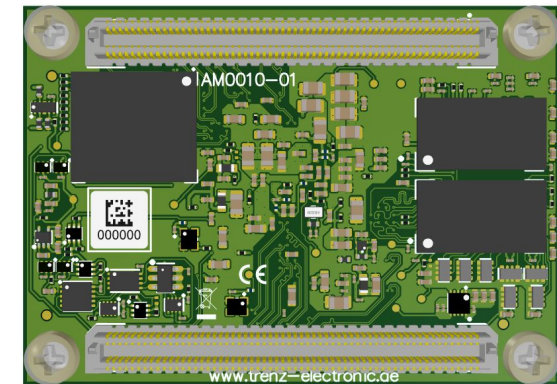
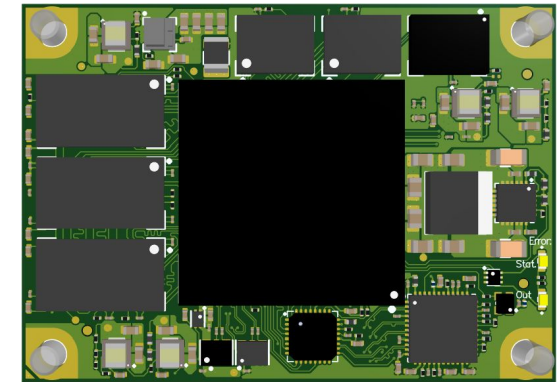
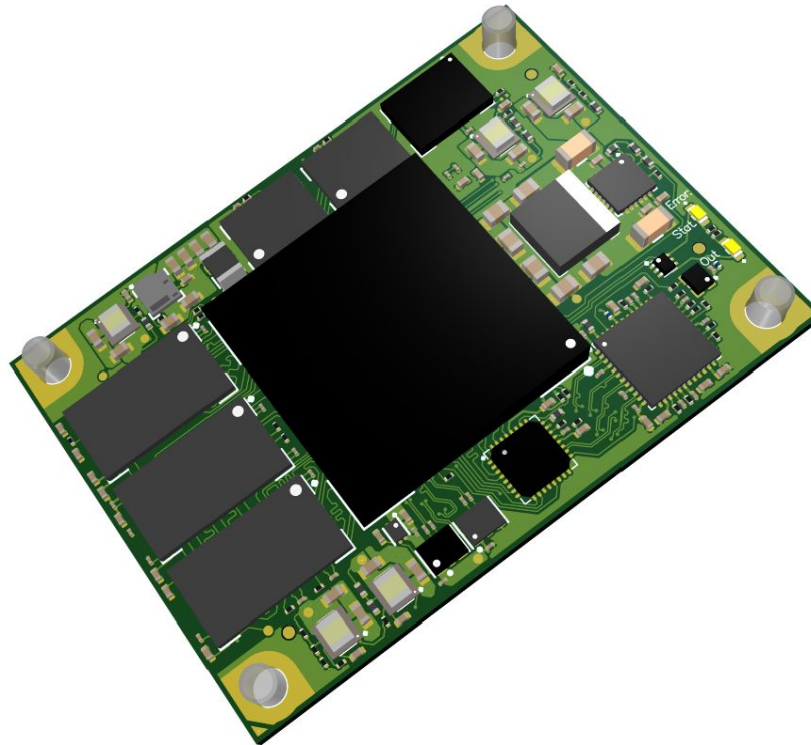




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Project is protected under copyright and we strongly and strictly prohibit the reverse engineering or recreation, even if the design is just adapted or modified. AM0010 is protected under such right and in case of plagiarism we will have to do anything necessary in order to protect our assets.

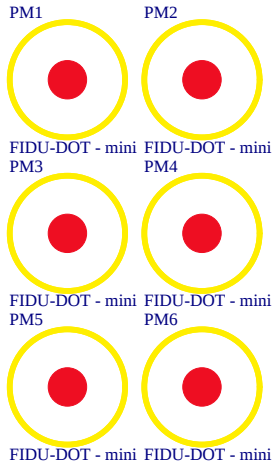
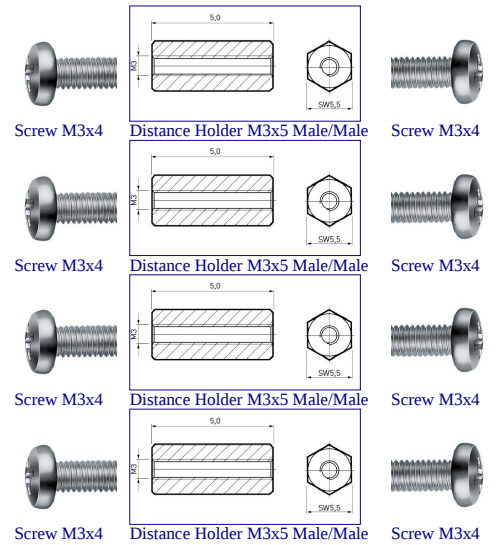
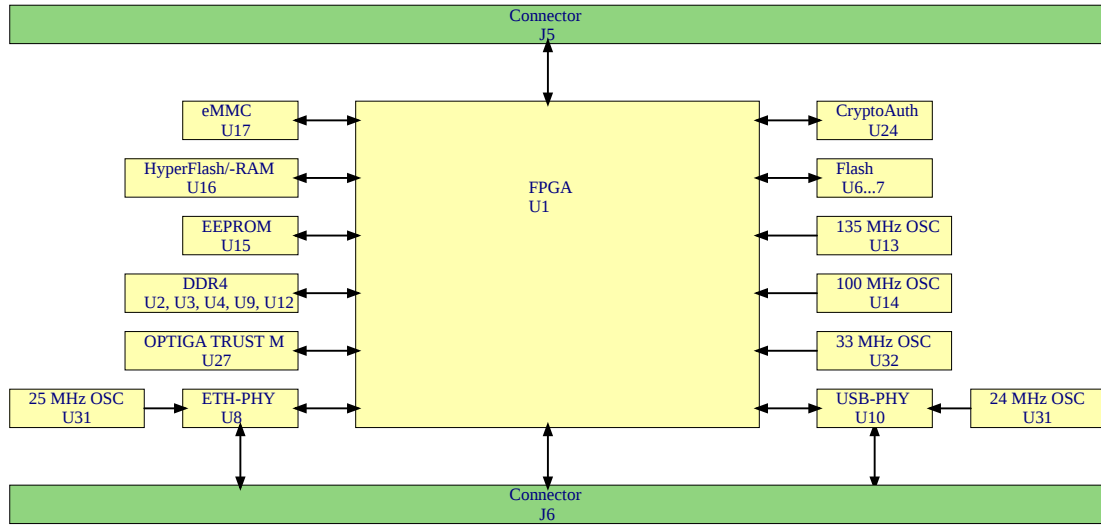
Schematics and other handouts serve for informational purposes only!

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	Date: 04.10.2021	Copyright: Trenz Electronic GmbH	Page 1 of 30
	Filename: Legal Notices Modules.SchDoc		

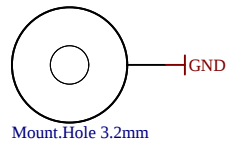
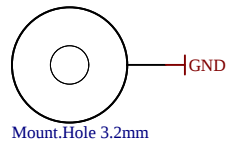
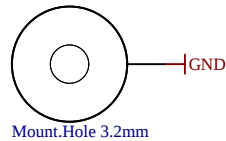
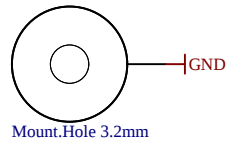
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B2B_Connector_1.SchDoc
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U_MPSoC
MPSoC.SchDoc
U_DDR4-RAM
DDR4-RAM.SchDoc
U_DDR4-RAM_2
DDR4-RAM_2.SchDoc
U_DDR4-RAM_3
DDR4-RAM_3.SchDoc
U_DDR4-RAM_4
DDR4-RAM_4.SchDoc
U_DDR4-RAM_5
DDR4-RAM_5.SchDoc
U_DDR4-CAPS
DDR4-CAPS.SchDoc
U_DDR4-TERM
DDR4-TERM.SchDoc
U_ETHPHY
ETHPHY.SchDoc
U_USBPHY
USBPHY.SchDoc
U_eMMC
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U_POWER_1
POWER_1.SchDoc
U_POWER_2
POWER_2.SchDoc
U_REV_CH
Revision_Changes.SchDoc
U_Legal_Notices_Modules
Legal_Notices_Modules.SchDoc

Special notes:



Serial
Serial
Serialnumber 6,3 x 6.3mm



Title: AM0010		
A4	Number: AM0010 3BE21MA	Rev. 01
Date: 04.10.2021	Copyright: Trenz Electronic GmbH	Page 2 of 30
Filename: AM0010.SchDoc		

Assembly variant 3BE21MA
Created by ED
Modified by ED
Modified at 02.07.21
SVN Revision

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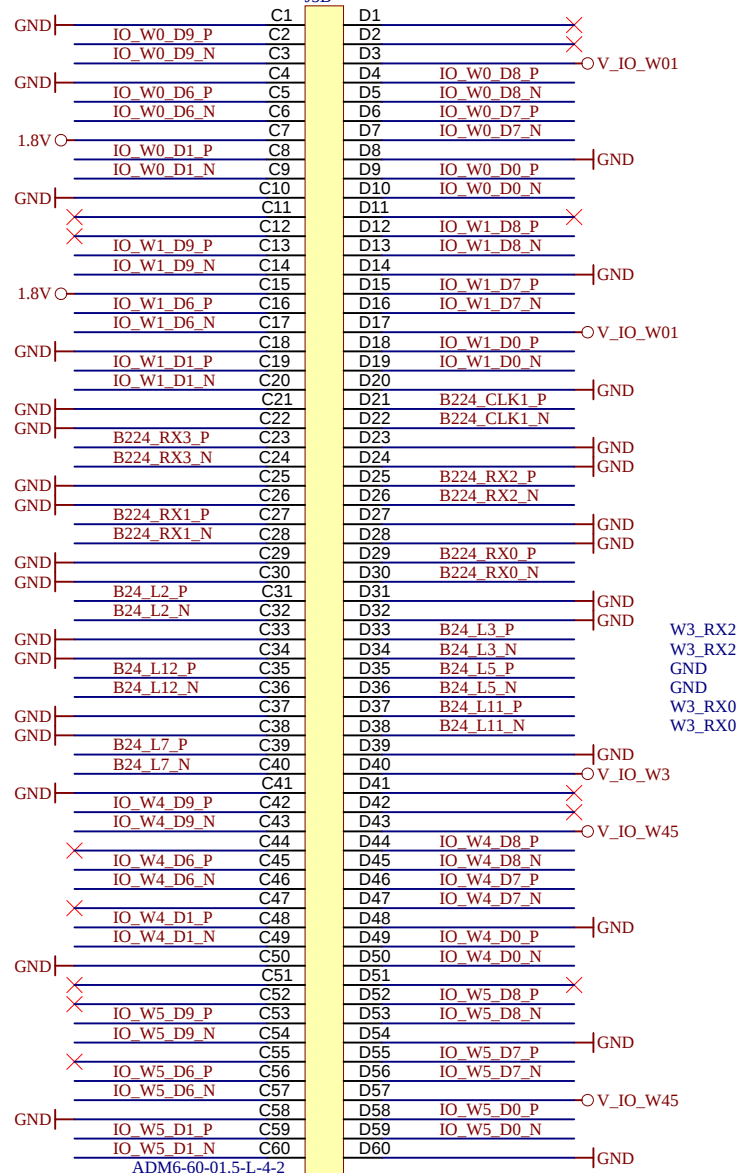
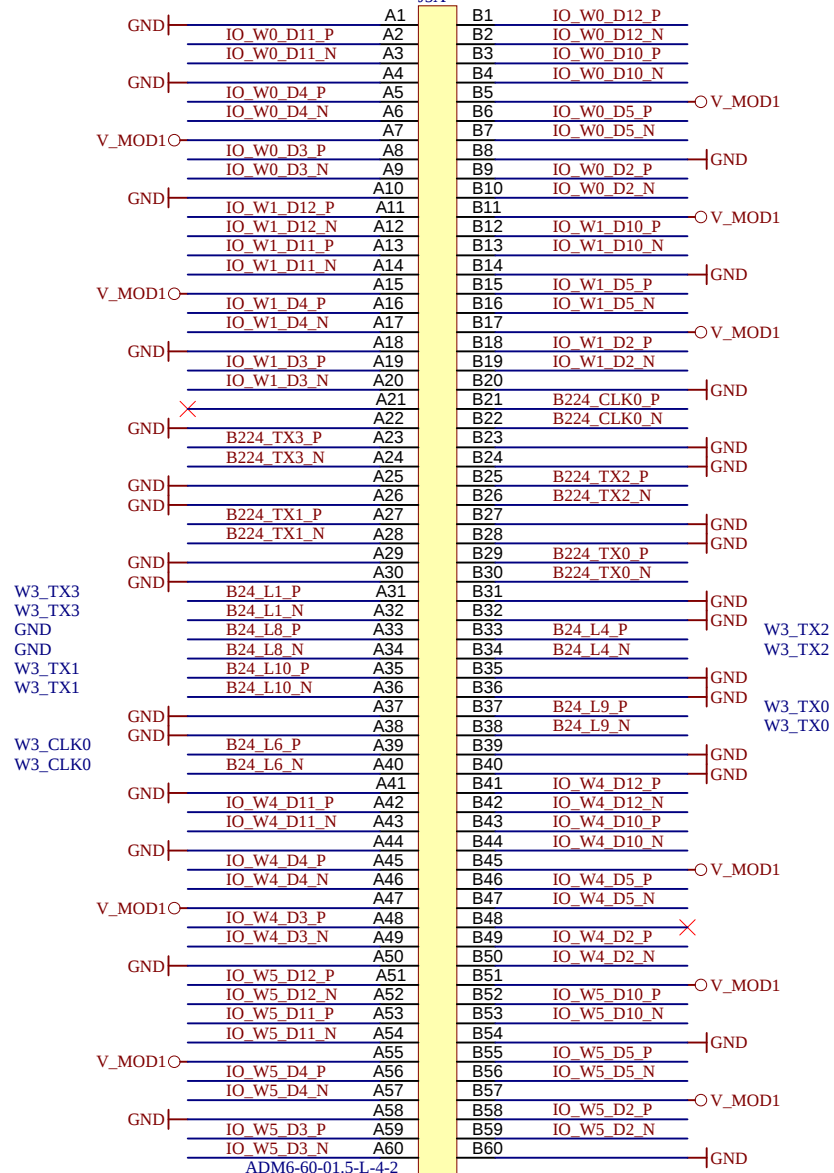
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D

Connector W

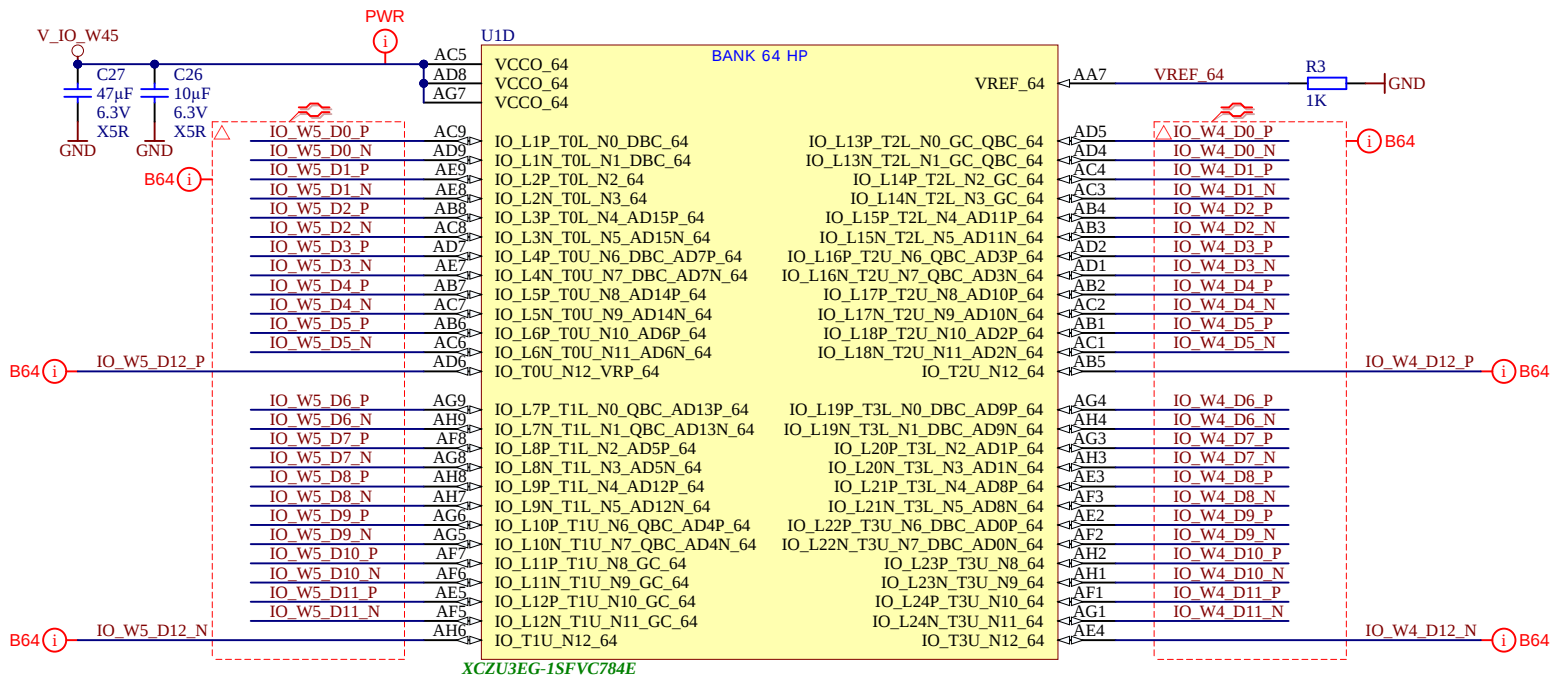
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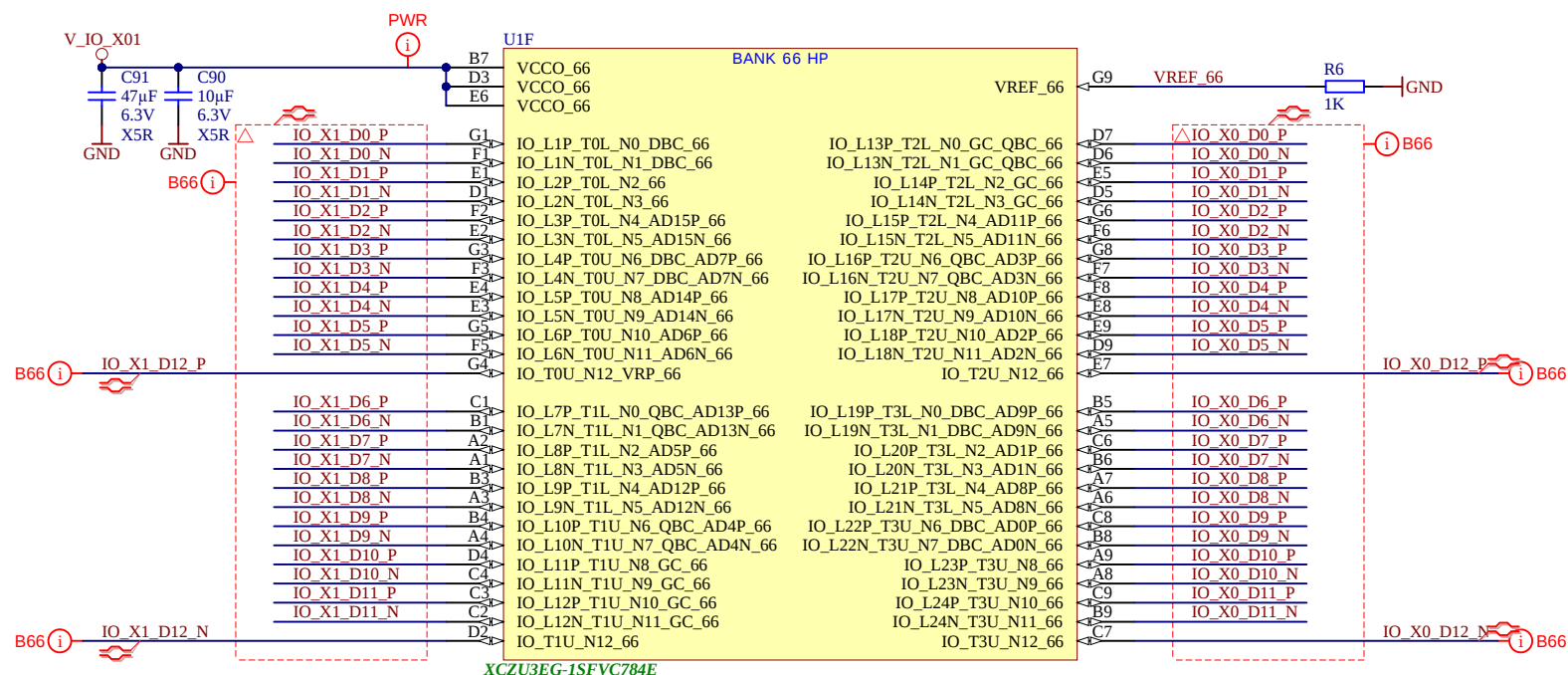


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Date: 04.10.2021		Copyright: Trenz Electronic GmbH	Page 3 of 30
Filename: B2B_Connector_1.SchDoc			

	1	2	3	4									
A	U_B64 B64.SchDoc U_B65 B65.SchDoc U_B66 B66.SchDoc U_B_HD B_HD.SchDoc U_B_MIO B_MIO.SchDoc U_PS_DDR PS_DDR.SchDoc U_B_PS_GT B_PS_GT.SchDoc U_B_GT B_GT.SchDoc U_CONFIG CONFIG.SchDoc U_ZU_POWER ZU_POWER.SchDoc U_ZU_PS_POWER ZU_PS_POWER.SchDoc 												
B													
C													
D				Title: AM0010 – MPSoC <table><tr><td>A4</td><td>Number: MPSoC 3BE21MA</td><td>Rev. 01</td></tr><tr><td>Date: 04.10.2021</td><td>Copyright: Trenz Electronic GmbH</td><td>Page 5 of 30</td></tr><tr><td colspan="3">Filename: MPSoC.SchDoc</td></tr></table>	A4	Number: MPSoC 3BE21MA	Rev. 01	Date: 04.10.2021	Copyright: Trenz Electronic GmbH	Page 5 of 30	Filename: MPSoC.SchDoc		
A4	Number: MPSoC 3BE21MA	Rev. 01											
Date: 04.10.2021	Copyright: Trenz Electronic GmbH	Page 5 of 30											
Filename: MPSoC.SchDoc													
	1	2	3	4									



			Title: AM0010 – B64	
			A4	Number: B64 3BE21MA
			Date: 04.10.2021	Rev. 01
			Copyright: Trenz Electronic GmbH	Page 6 of 30
Filename: B64.SchDoc				



Title: AM0010 – B66		
A4	Number: B66 3BE21MA	Rev. 01
Date: 04.10.2021	Copyright: Trenz Electronic GmbH	Page 8 of 30
Filename: B66.SchDoc		

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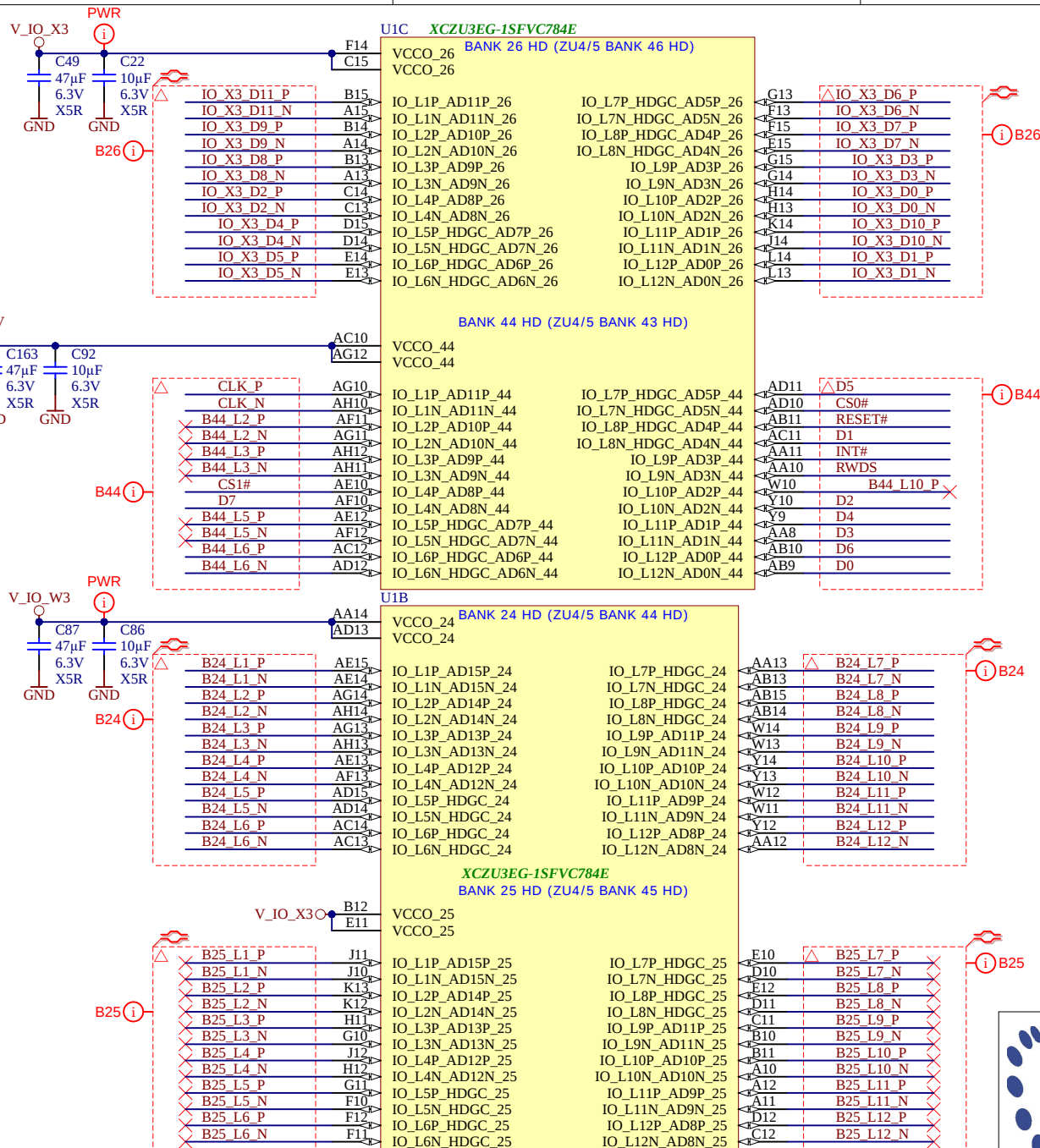
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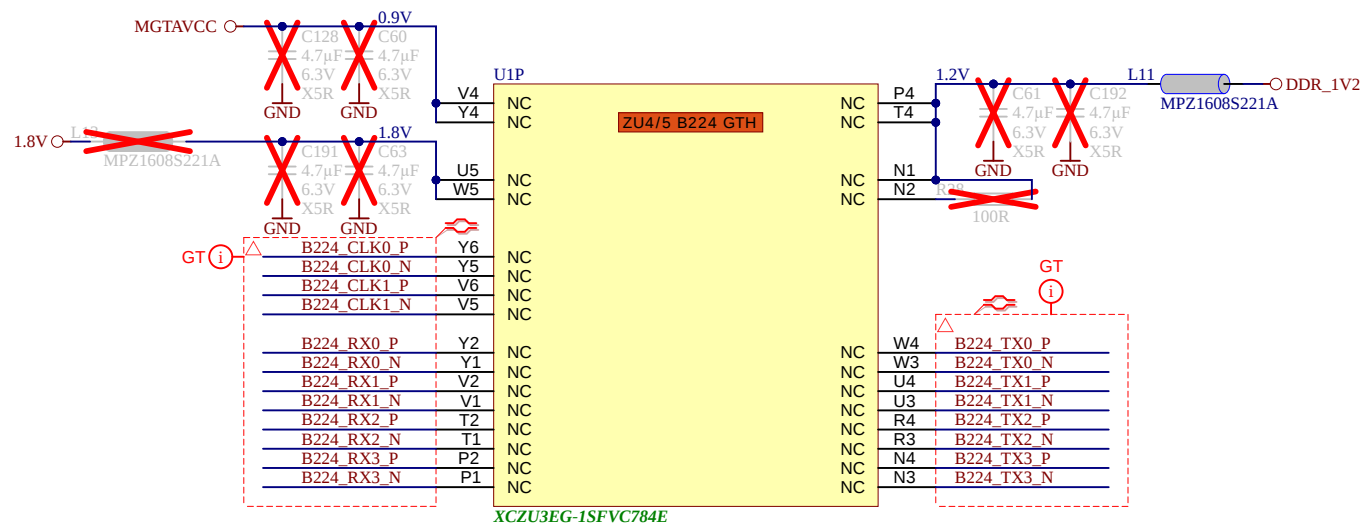
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Date: 04.10.2021	Copyright: Trenz Electronic GmbH	Page 9 of 30
Filename: B_HD.SchDoc		



		Title: AM0010 – B_GT	
		A4	Number: B_GT 3BE21MA
Date: 04.10.2021		Copyright: Trenz Electronic GmbH	
Filename: B_GT.SchDoc		Page 10 of 30	
		Rev. 01	

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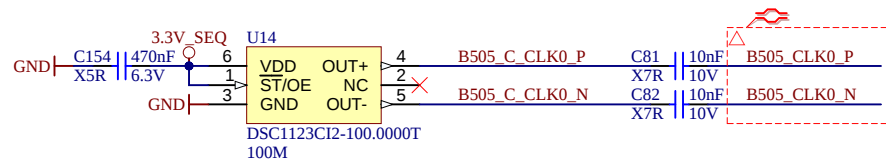
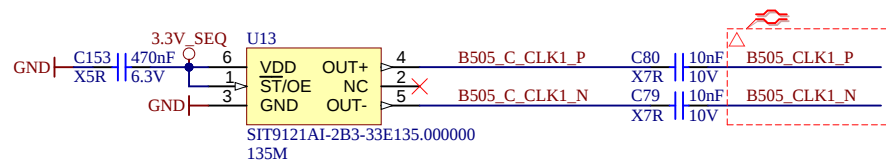
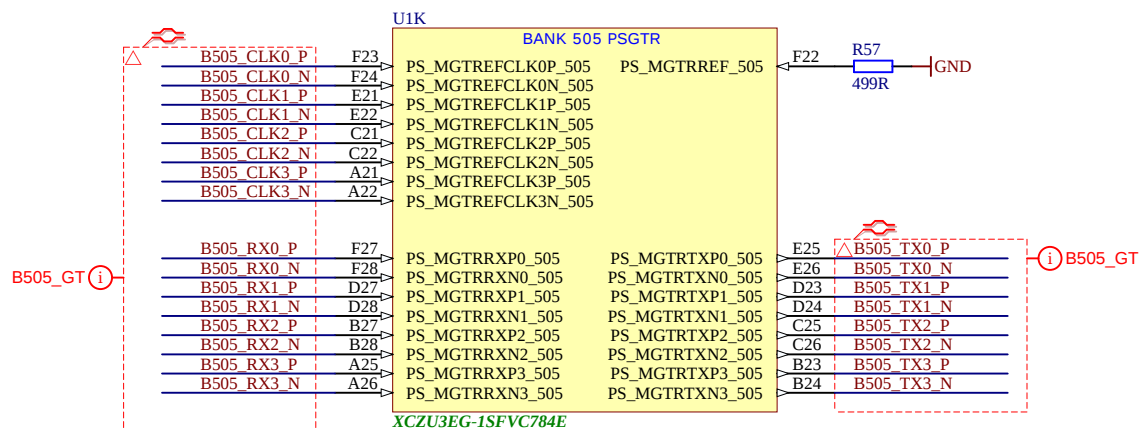
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	Title: AM0010 – B_PS_GT	
	A4	Number: B_PS_GT 3BE21MA
Date: 04.10.2021		Rev. 01
Copyright: Trenz Electronic GmbH		Page 12 of 30
Filename: B_PS_GT.SchDoc		

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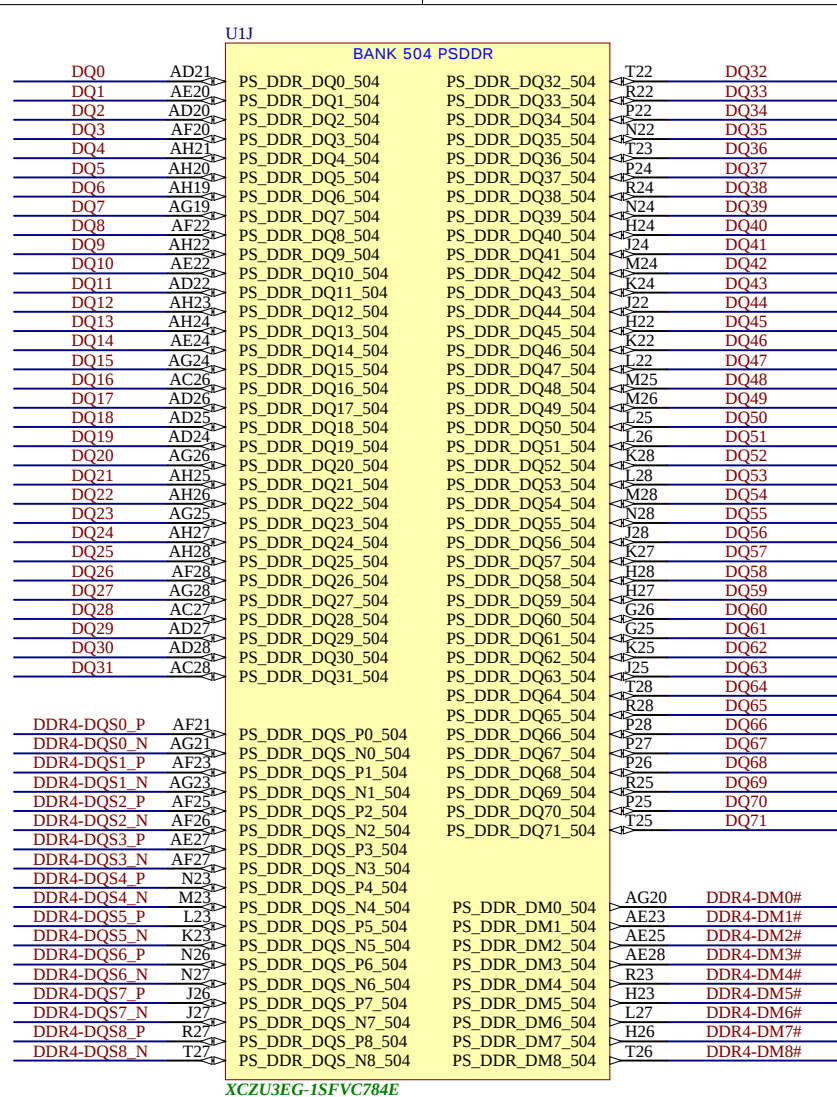
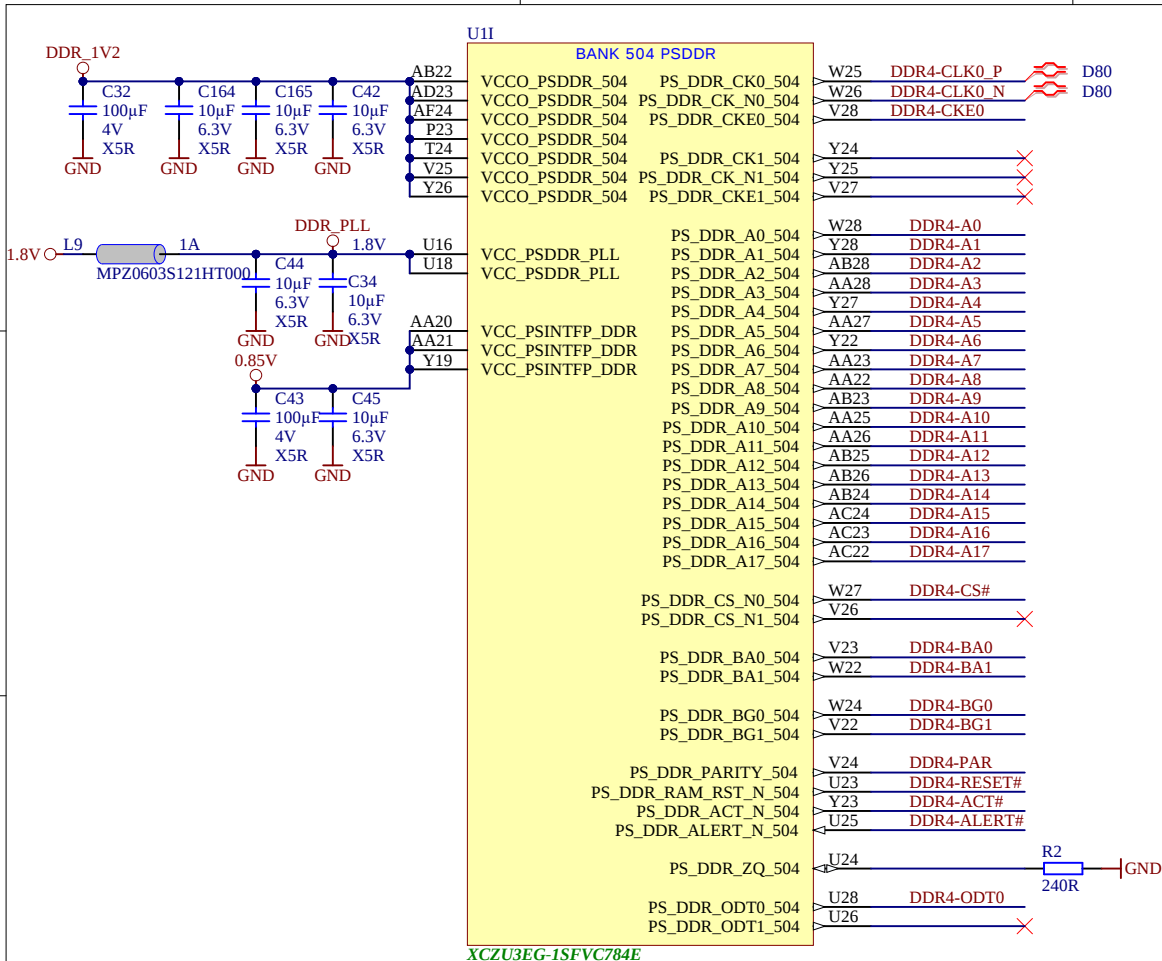
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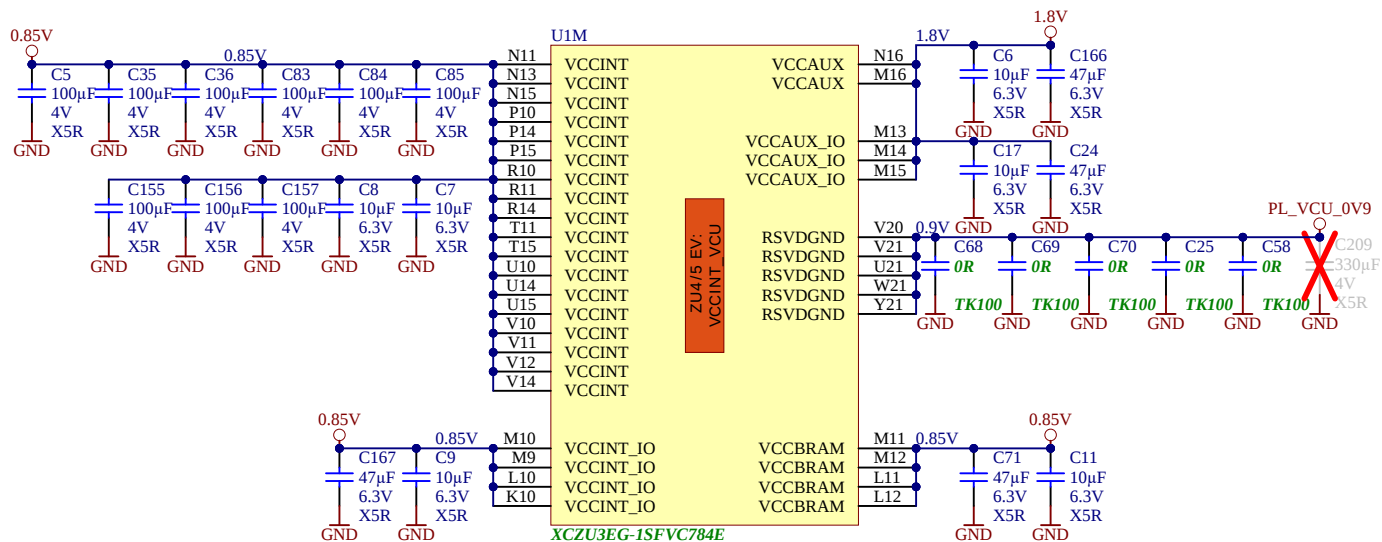
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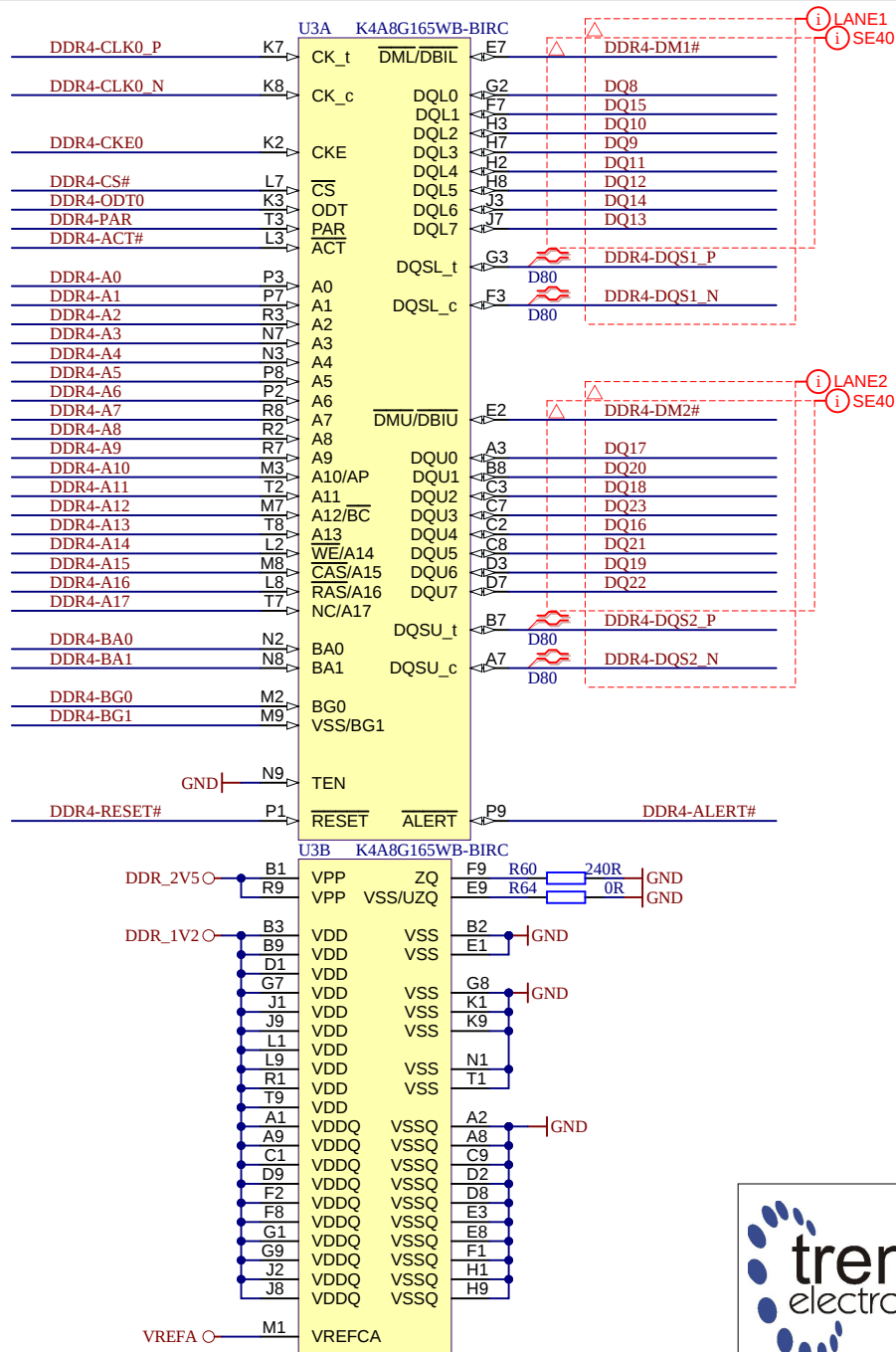
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Title: AM0010 – DDR4-RAM_2		
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Date: 04.10.2021	Copyright: Trenz Electronic GmbH	Page 18 of 30
Filename: DDR4-RAM_2.SchDoc		

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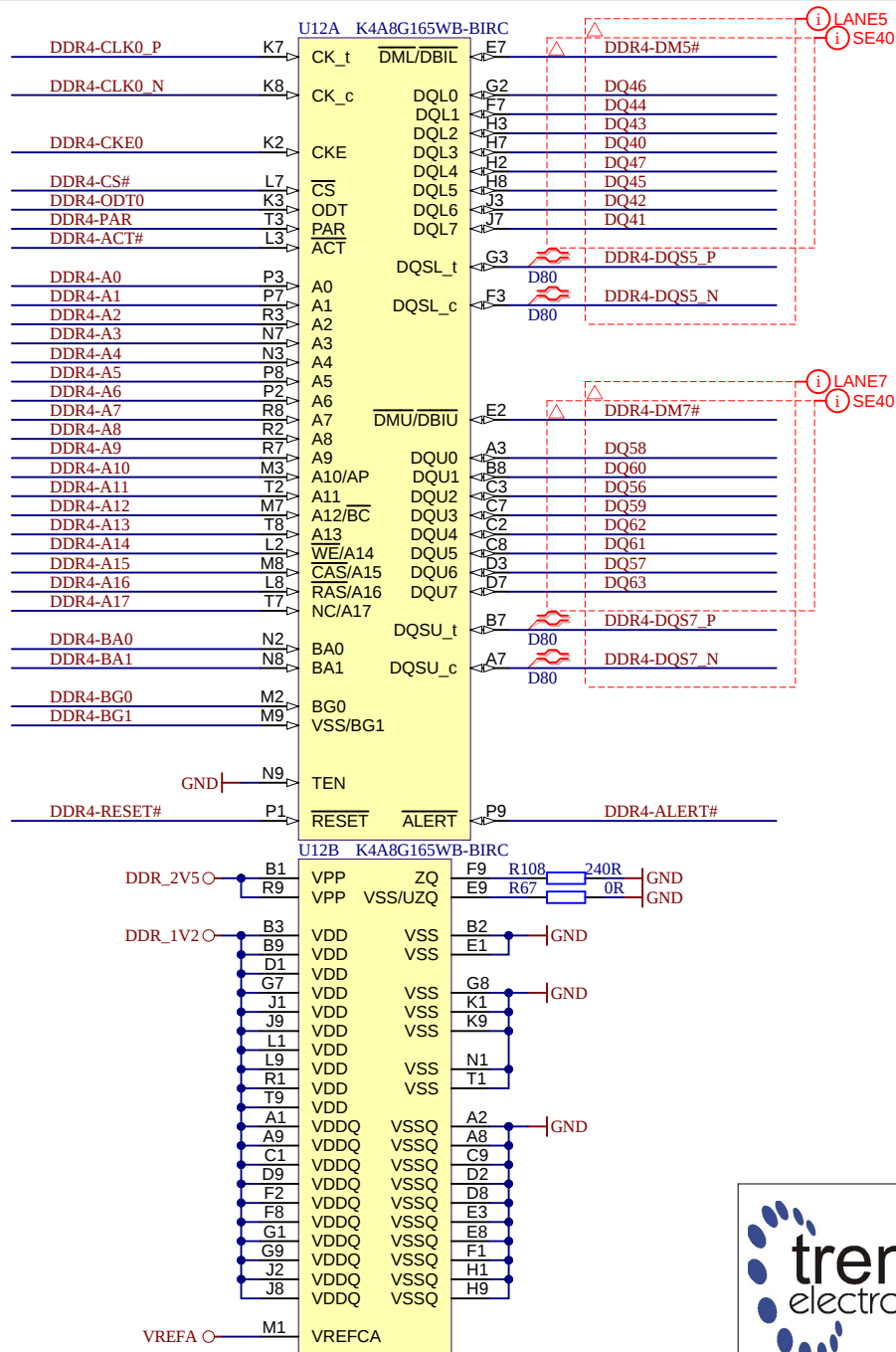
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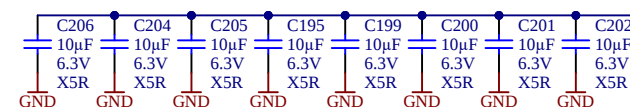
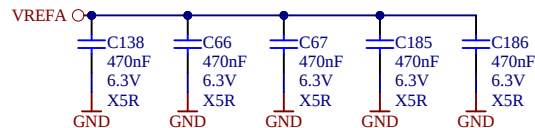
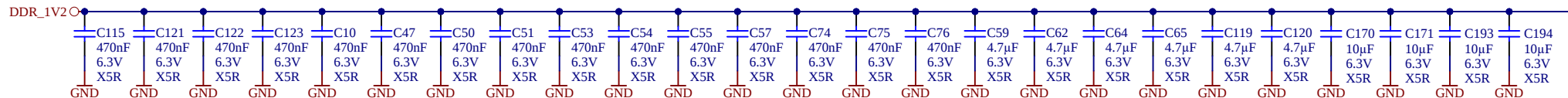
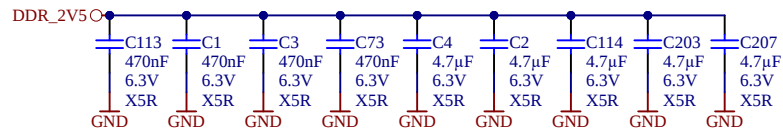
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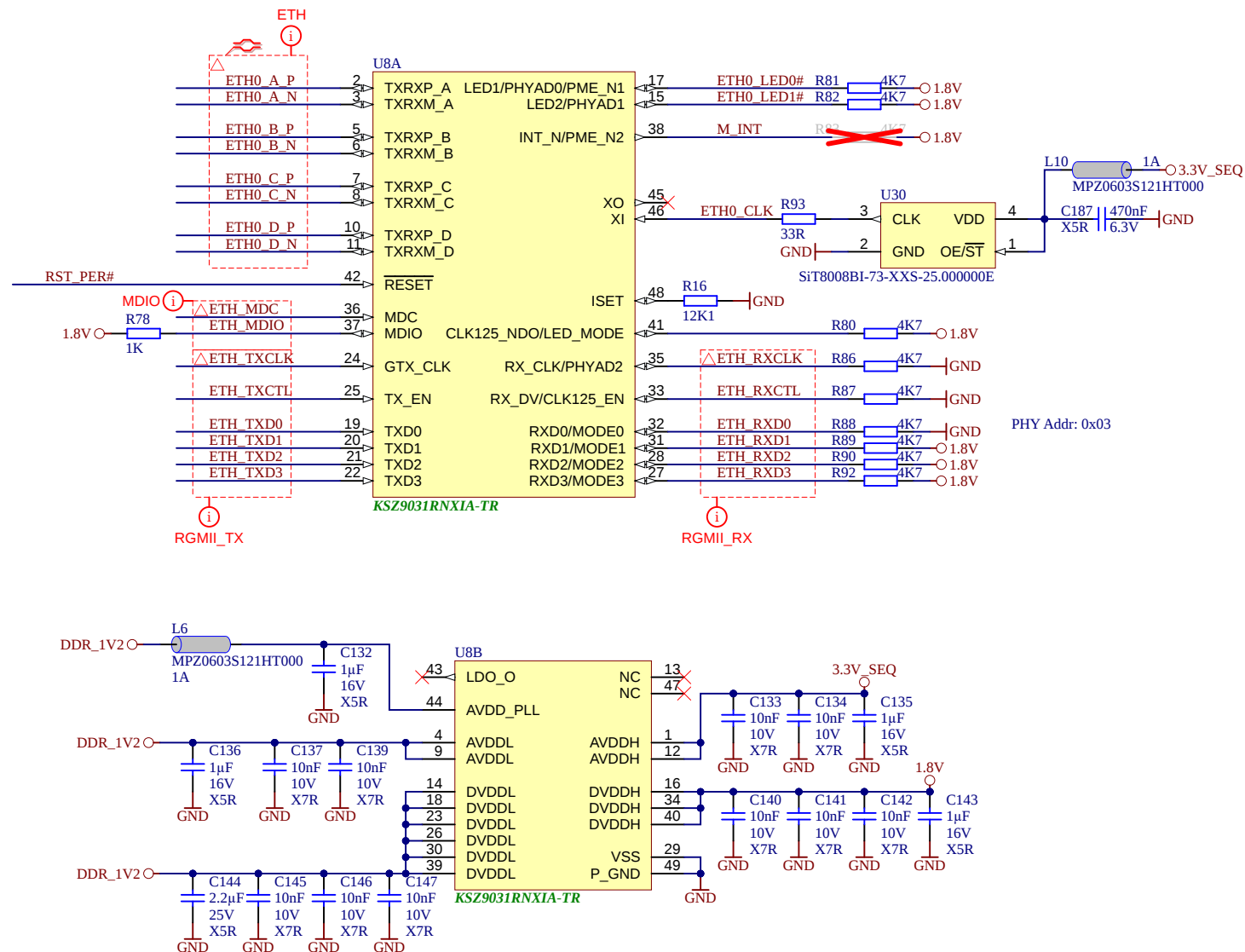
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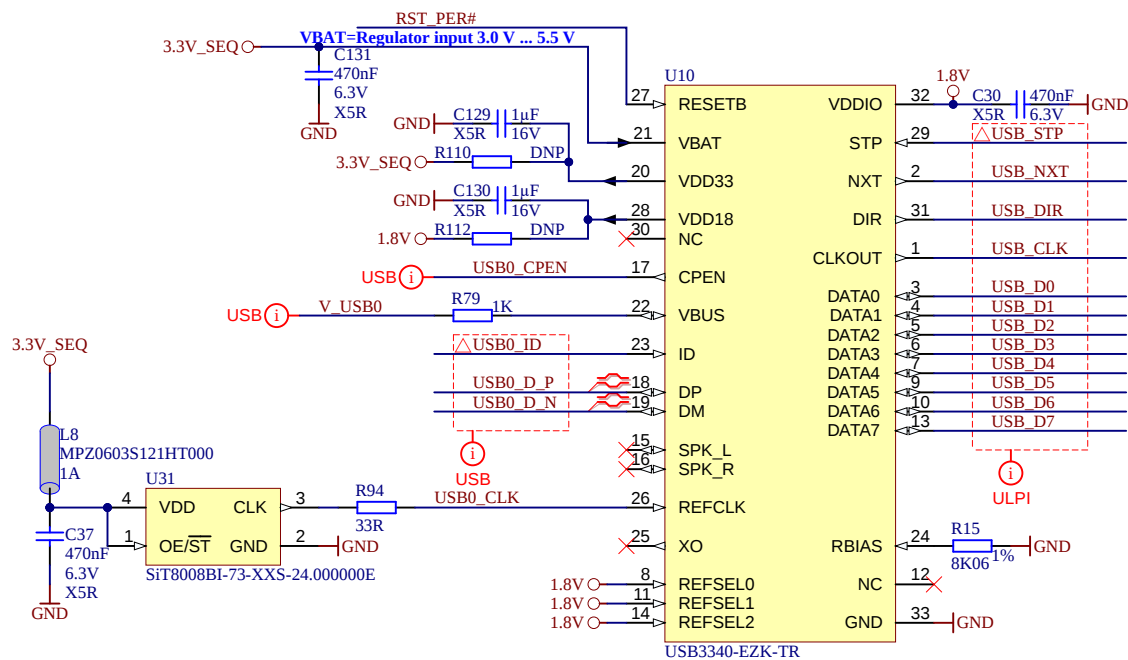


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Filename: DDR4-CAPS.SchDoc		Page 23 of 30	

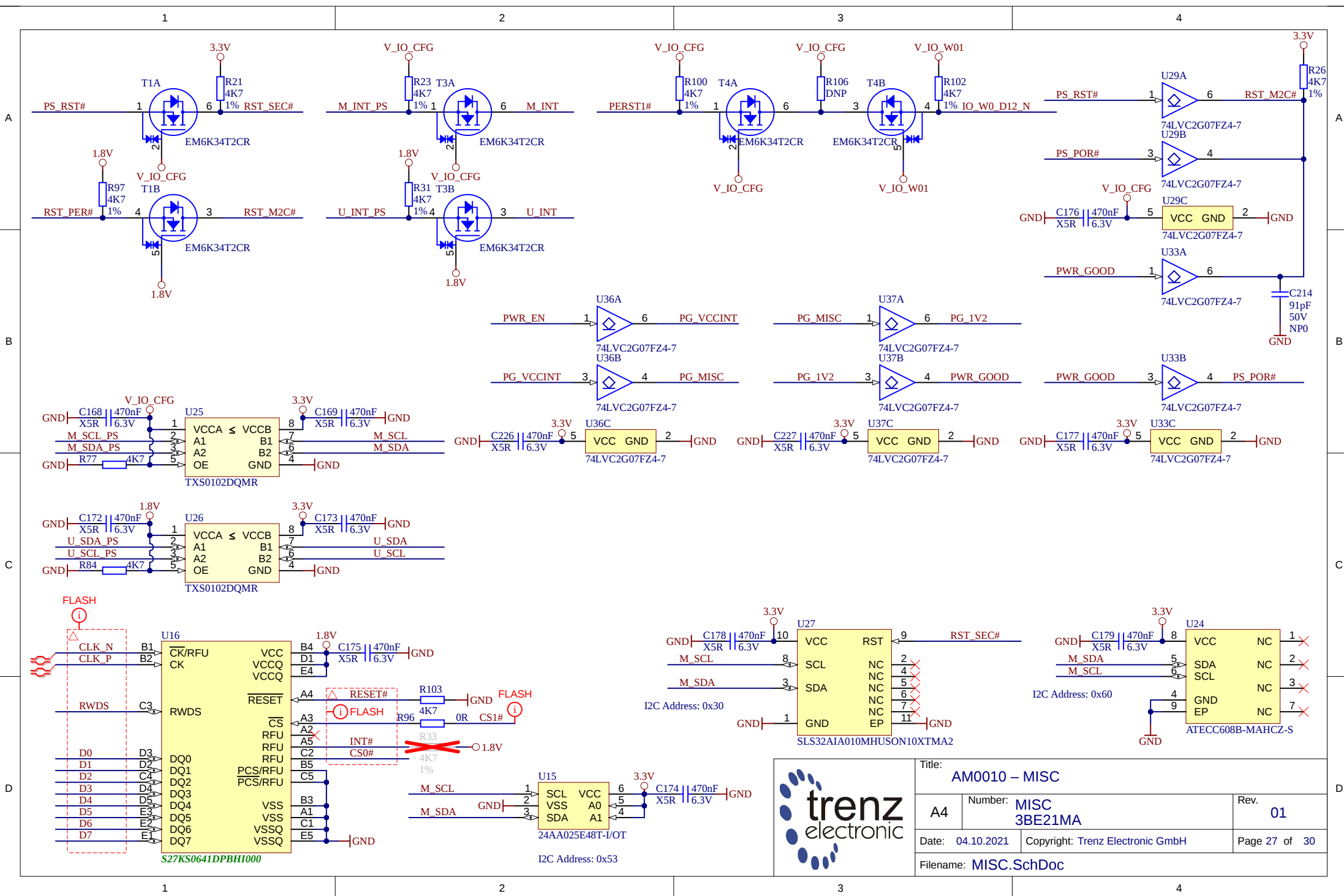




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	Date: 04.10.2021	Copyright: Trenz Electronic GmbH	Page 25 of 30
	Filename: ETHPHY.SchDoc		



		Title: AM0010 – USBPHY	
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Date: 04.10.2021		Copyright: Trenz Electronic GmbH	
Filename: USBPHY.SchDoc		Page 26 of 30	



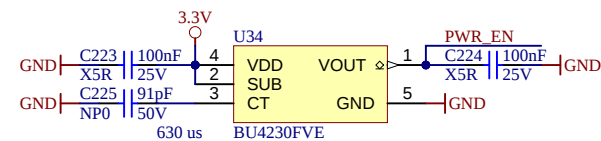
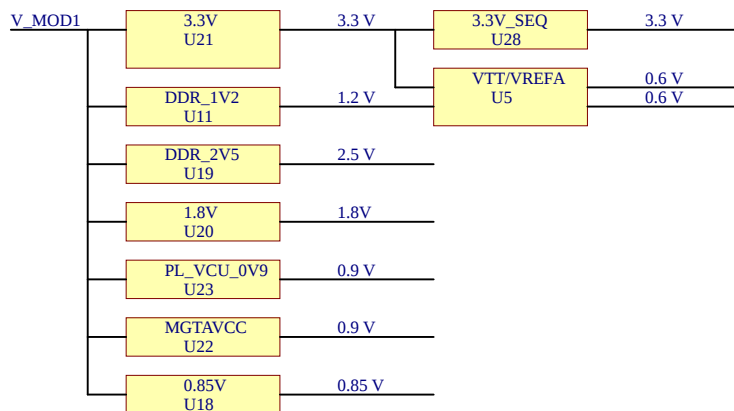
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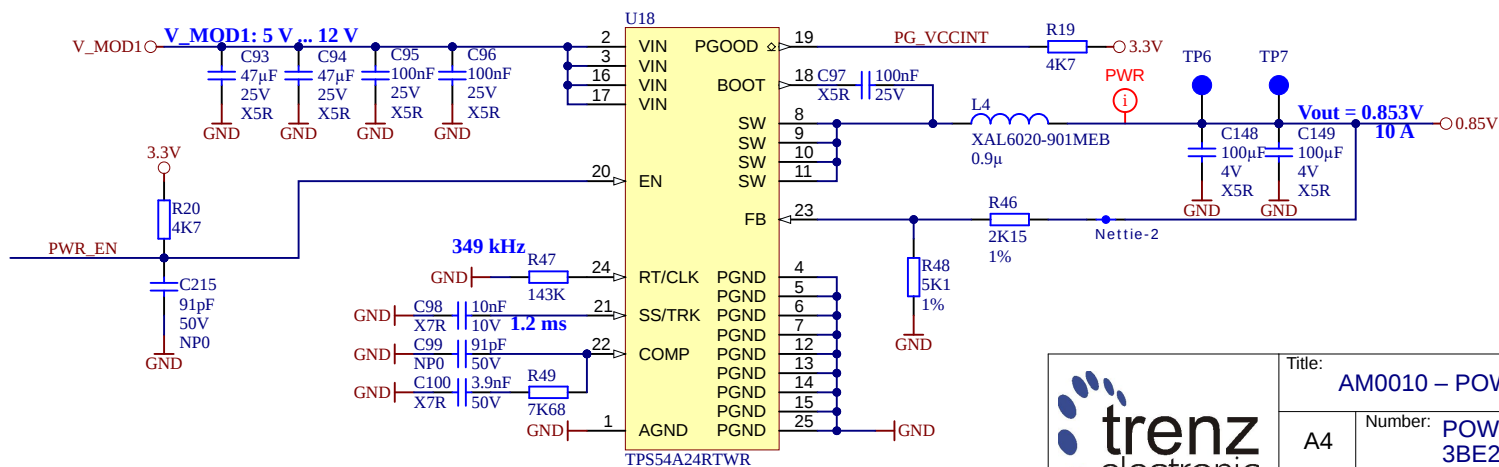
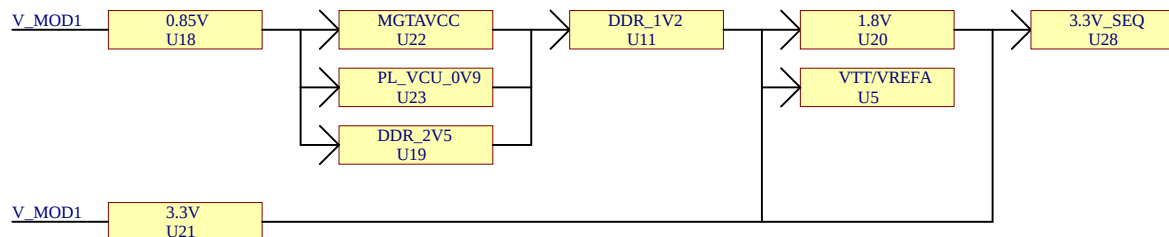
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Power Supply Structure



Power Supply Sequencing



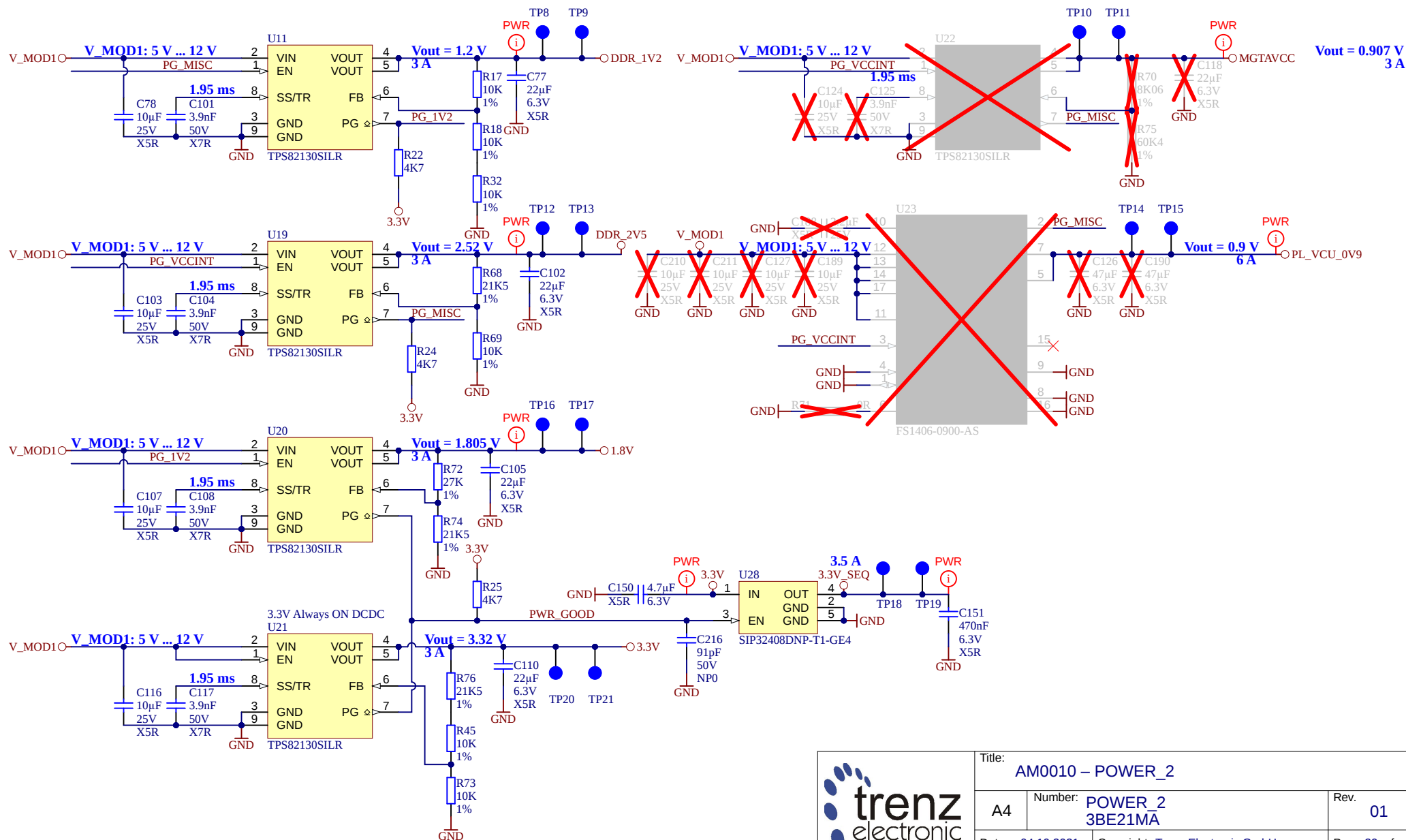
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Title: AM0010 – POWER_2		
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REV	Description	
-01	Initial revision	AL/ED

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Date: 04.10.2021		Copyright: Trenz Electronic GmbH	Rev. 01
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