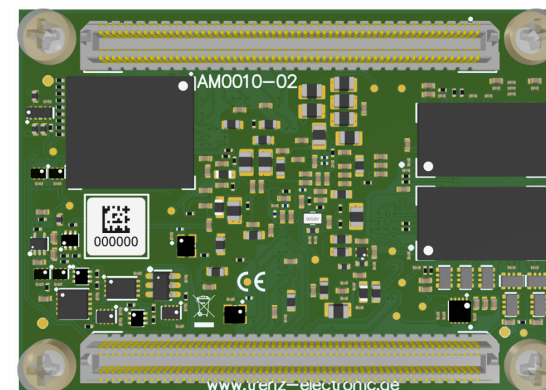
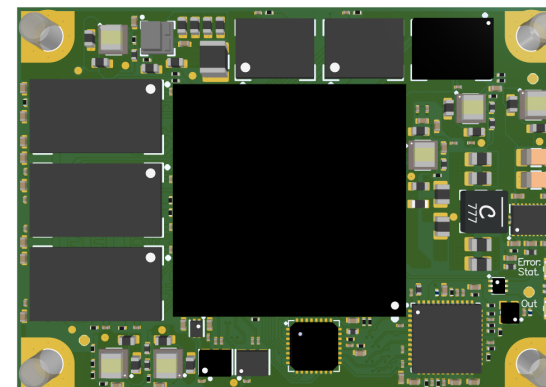
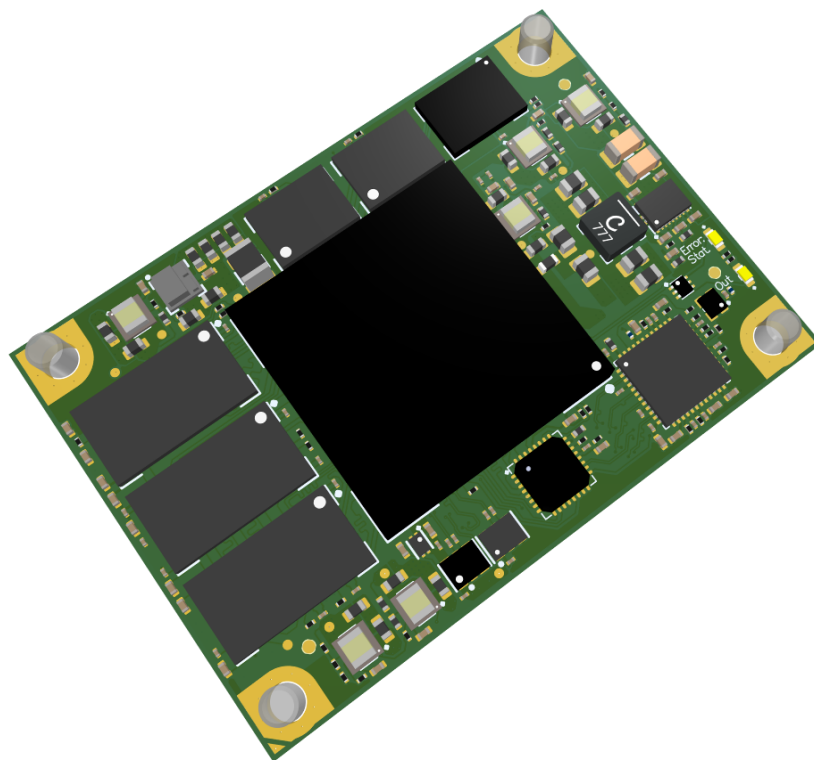




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Schematics and other handouts serve for informational purposes only!

		Title: AM0010 – Legal Notices Modules	
		A4	Number: Legal Notices Modules 4DE81MA
		Date: 01.08.2022	Rev. 02
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		Filename: Legal Notices Modules.SchDoc	

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U_B2B_Connector_1
B2B_Connector_1.SchDoc

U_B2B_Connector_2
B2B_Connector_2.SchDoc

U_MPSoC
MPSoC.SchDoc

U_DDR4-RAM
DDR4-RAM.SchDoc

U_DDR4-RAM_2
DDR4-RAM_2.SchDoc

U_DDR4-RAM_3
DDR4-RAM_3.SchDoc

U_DDR4-RAM_4
DDR4-RAM_4.SchDoc

U_DDR4-RAM_5
DDR4-RAM_5.SchDoc

U_DDR4-CAPS
DDR4-CAPS.SchDoc

U_DDR4-TERM
DDR4-TERM.SchDoc

U_ETHPHY
ETHPHY.SchDoc

U_USBPHY
USBPHY.SchDoc

U_eMMC
eMMC.SchDoc

U_MISC
MISC.SchDoc

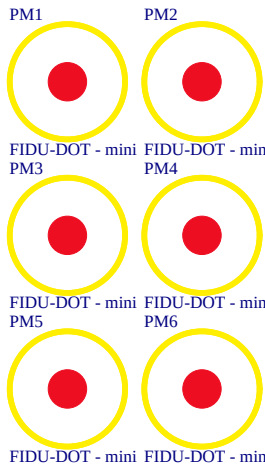
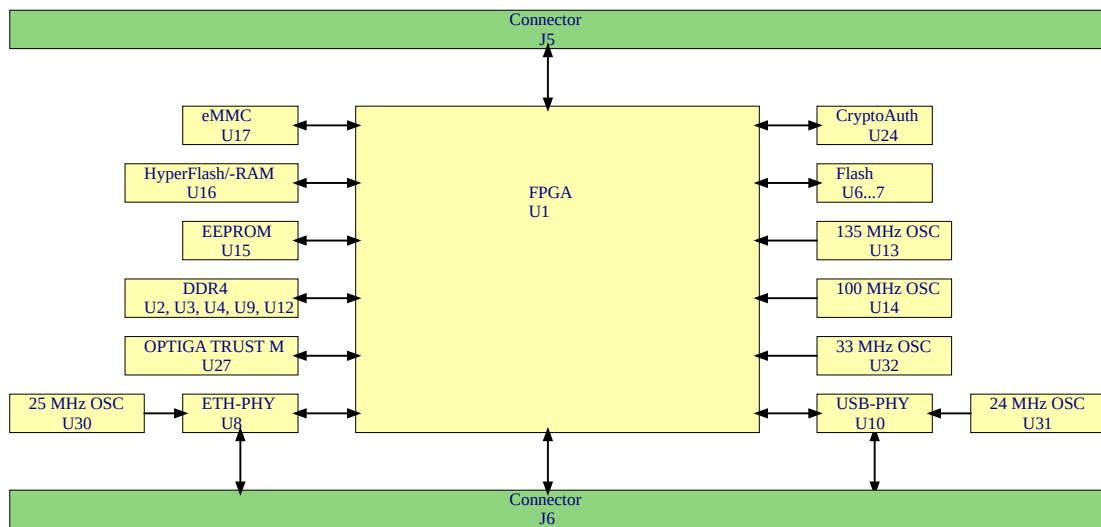
U_POWER_1
POWER_1.SchDoc

U_POWER_2
POWER_2.SchDoc

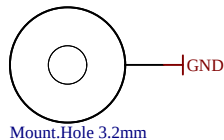
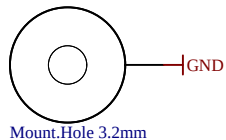
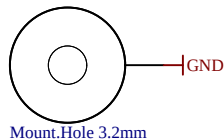
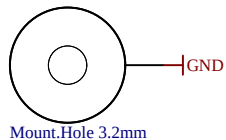
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Revision_Changes.SchDoc

U_Legal_Notices_Modules
Legal_Notices_Modules.SchDoc

Special notes:



Serial
Serial
Serialnumber 6,3 x 6.3mm



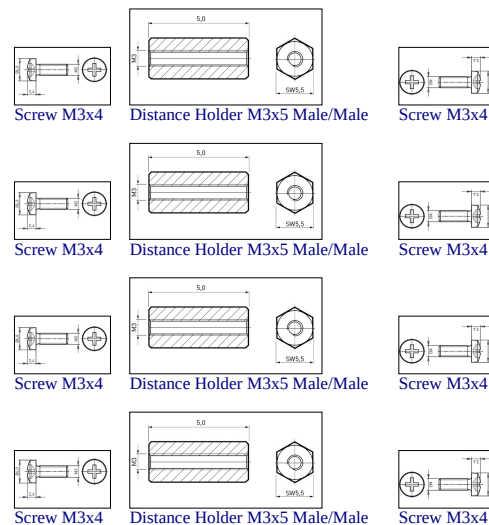
UKCA

UKCA Logo on Top Overlay

UKCA-TOPOVERLAY



Title: AM0010		
A4	Number: AM0010 4DE81MA	Rev. 02
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Assembly variant 4DE81MA
Created by ED
Modified by ED
Modified at 2025-03-18

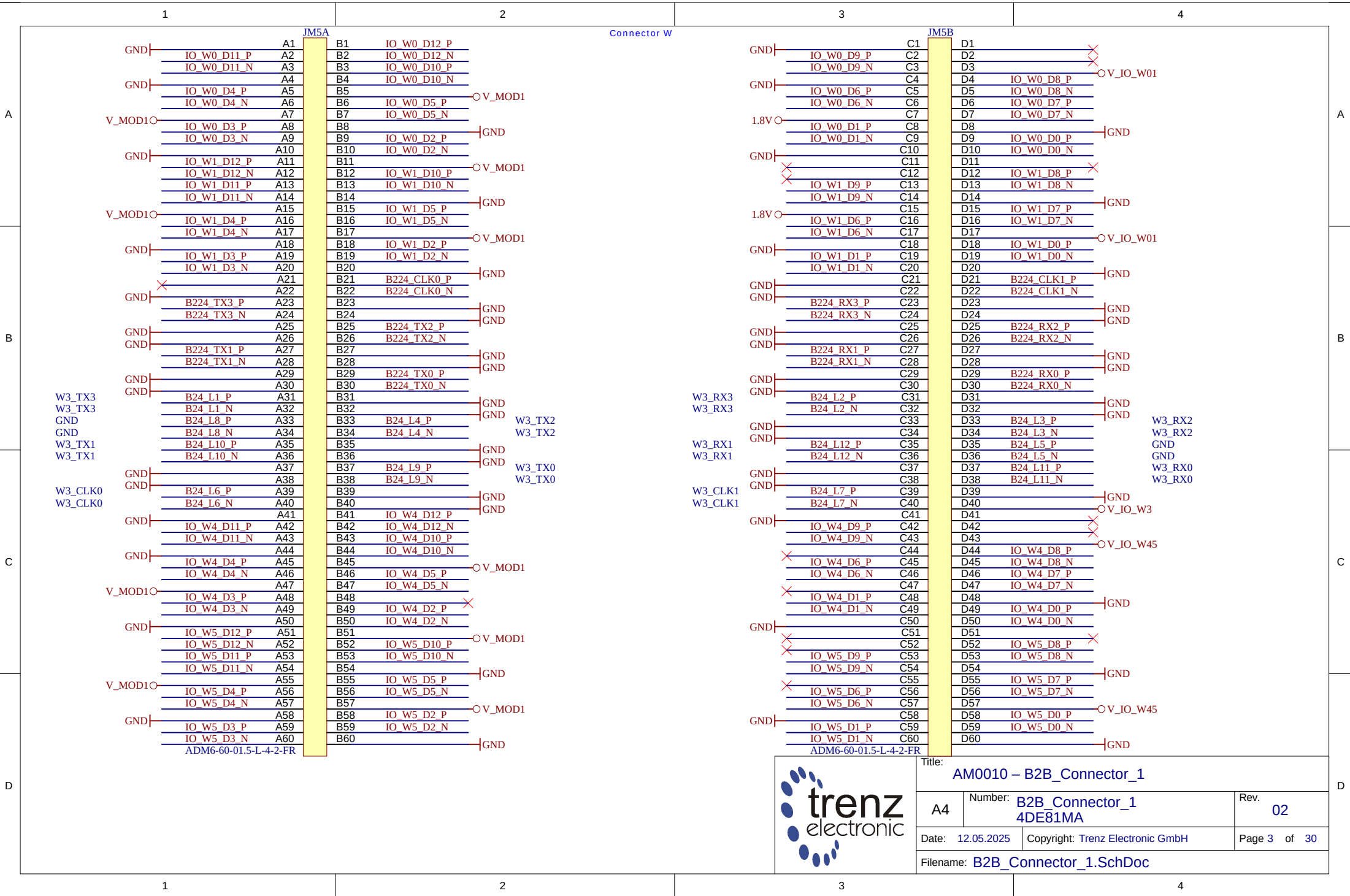
SVN Revision

1

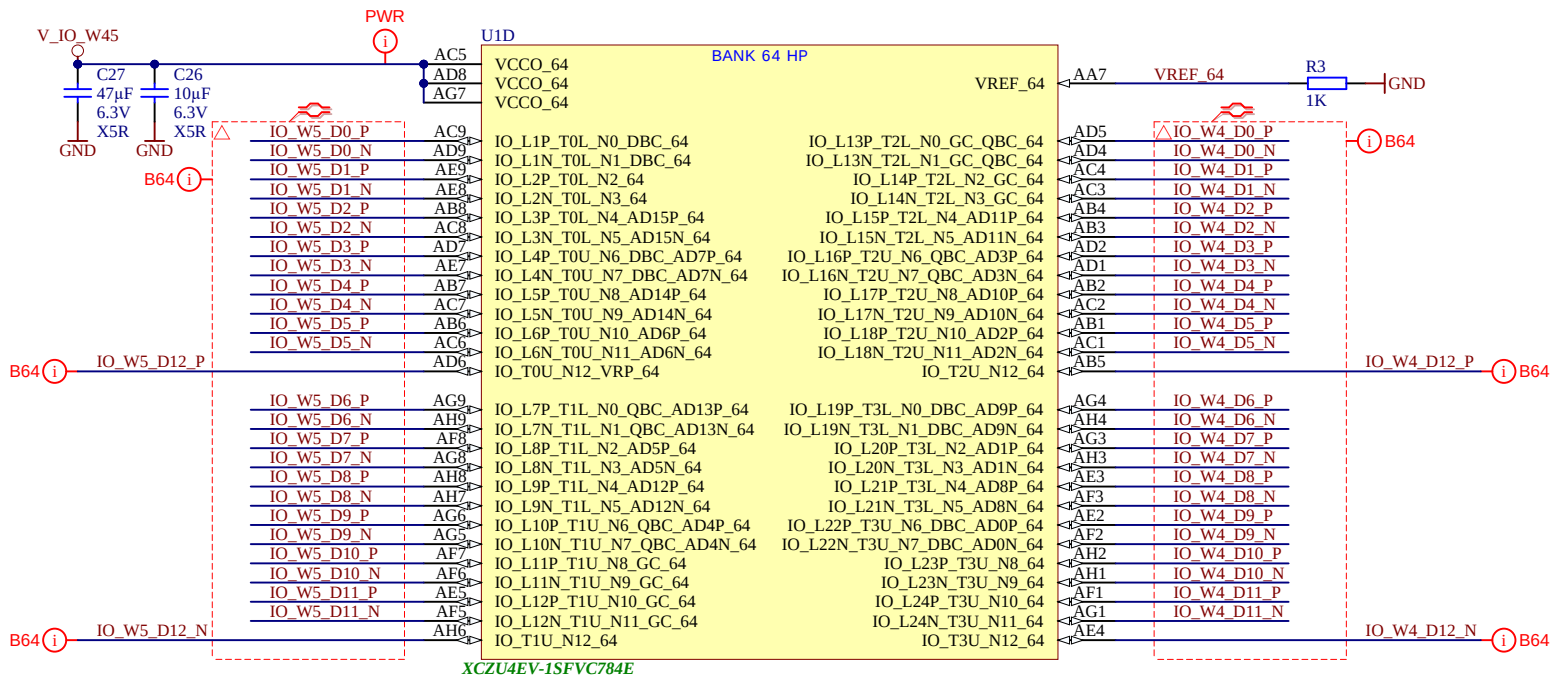
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Title: AM0010 – B2B_Connector_1			
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Filename: B2B_Connector_1.SchDoc			



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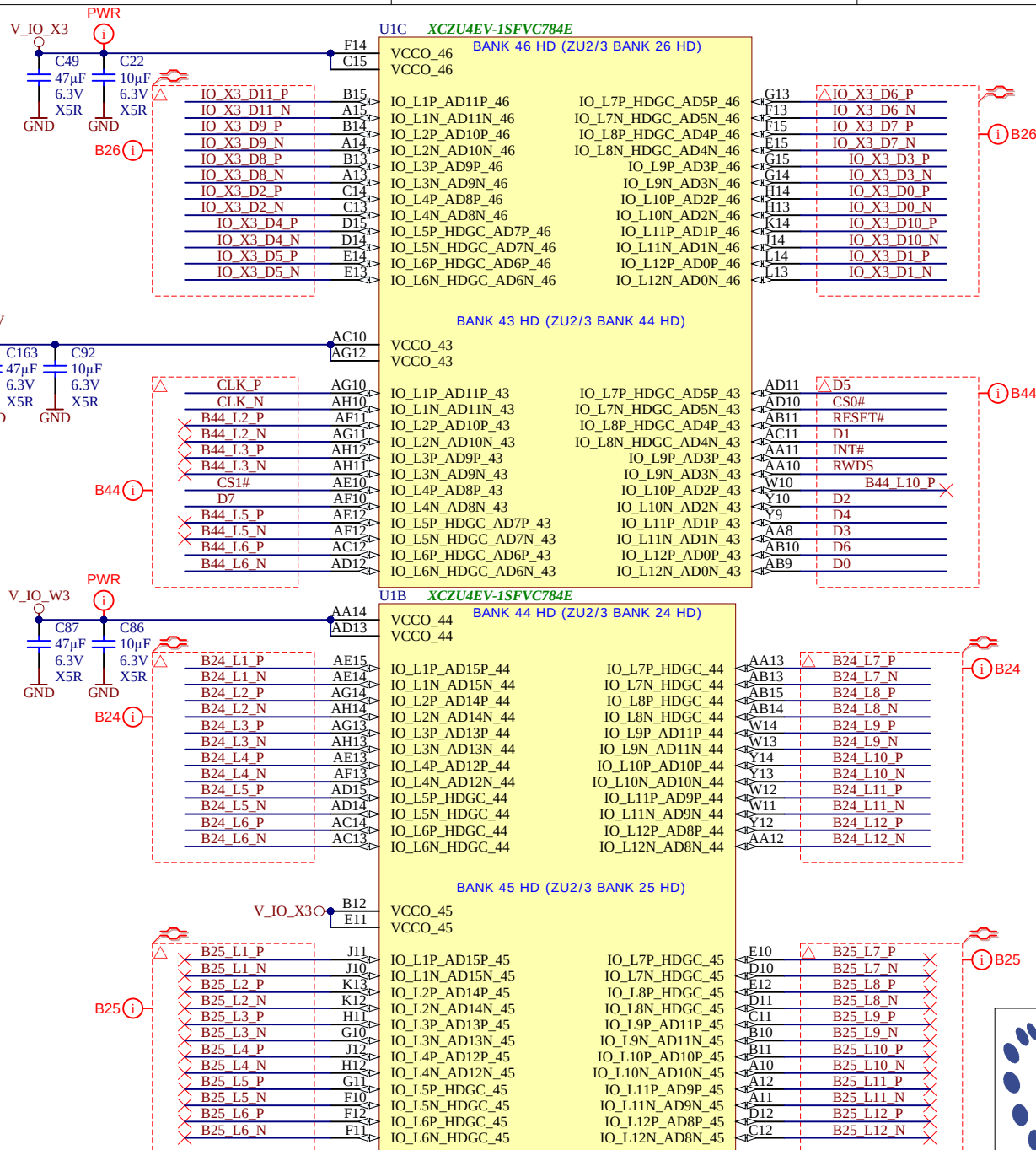
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Title: AM0010 – B_HD		
A4	Number: B_HD 4DE81MA	Rev. 02
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Filename: B_HD.SchDoc		

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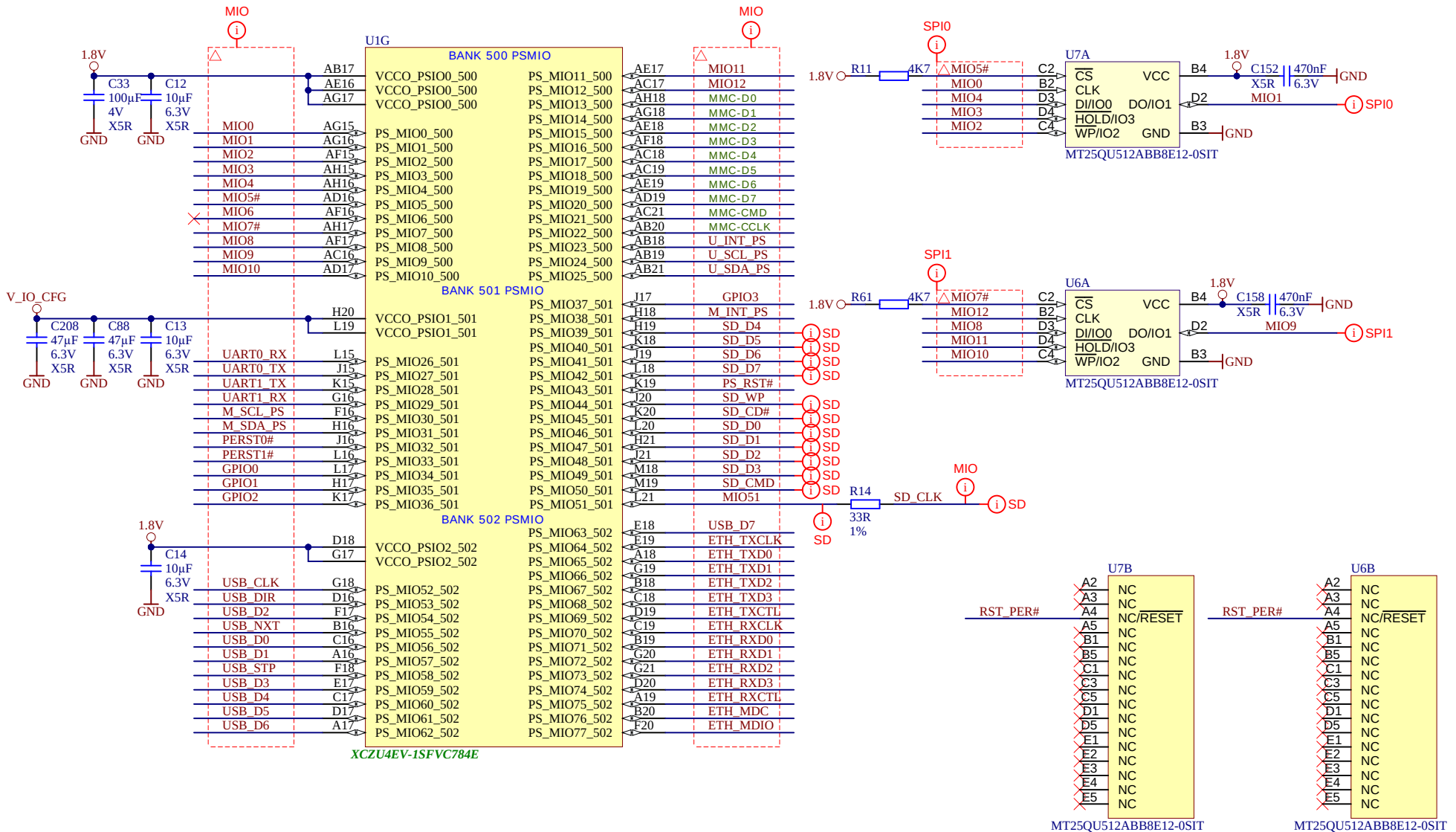
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Title: AM0010 – B_MIO		
A4	Number: B_MIO 4DE81MA	Rev. 02
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Filename: B_MIO.SchDoc		

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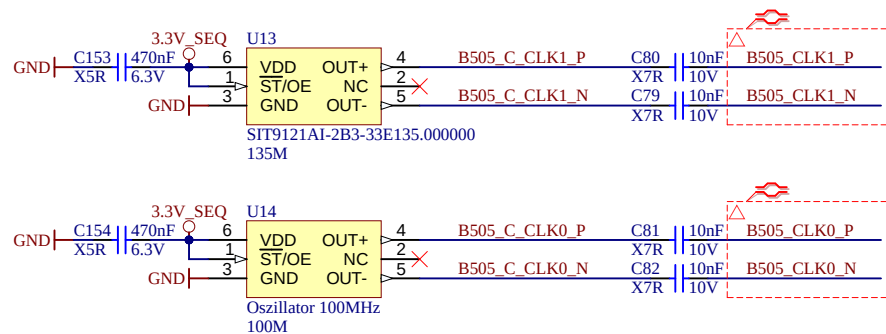
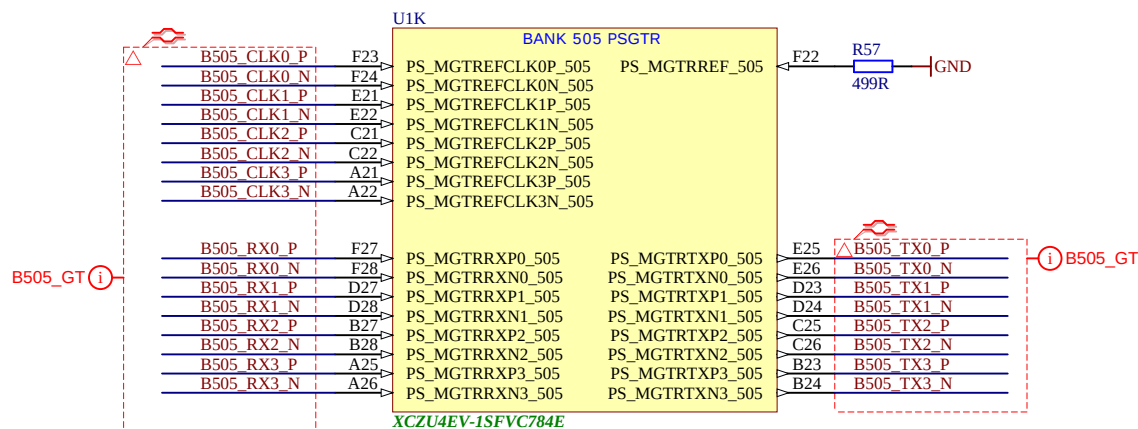
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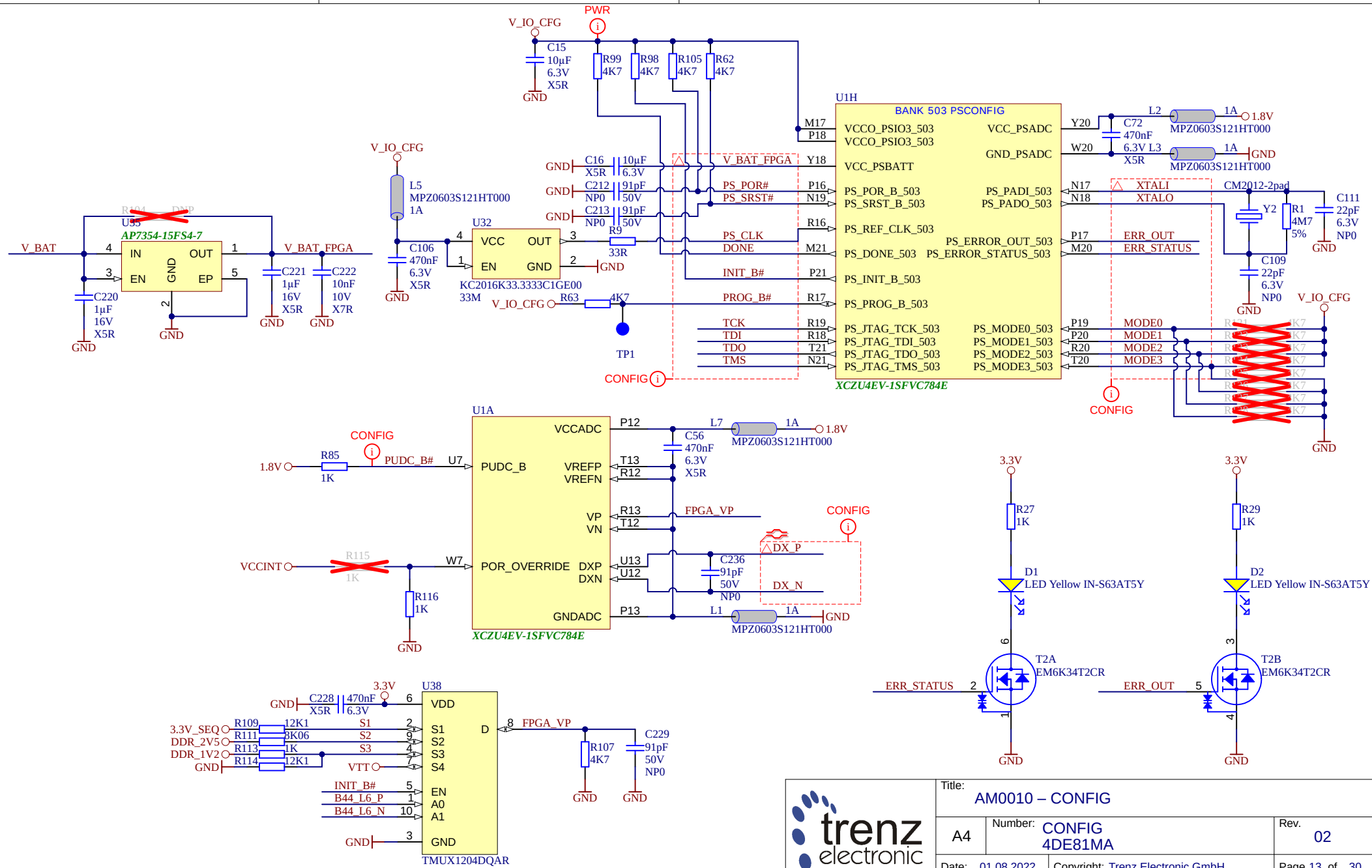
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	Filename: B_PS_GT.SchDoc		

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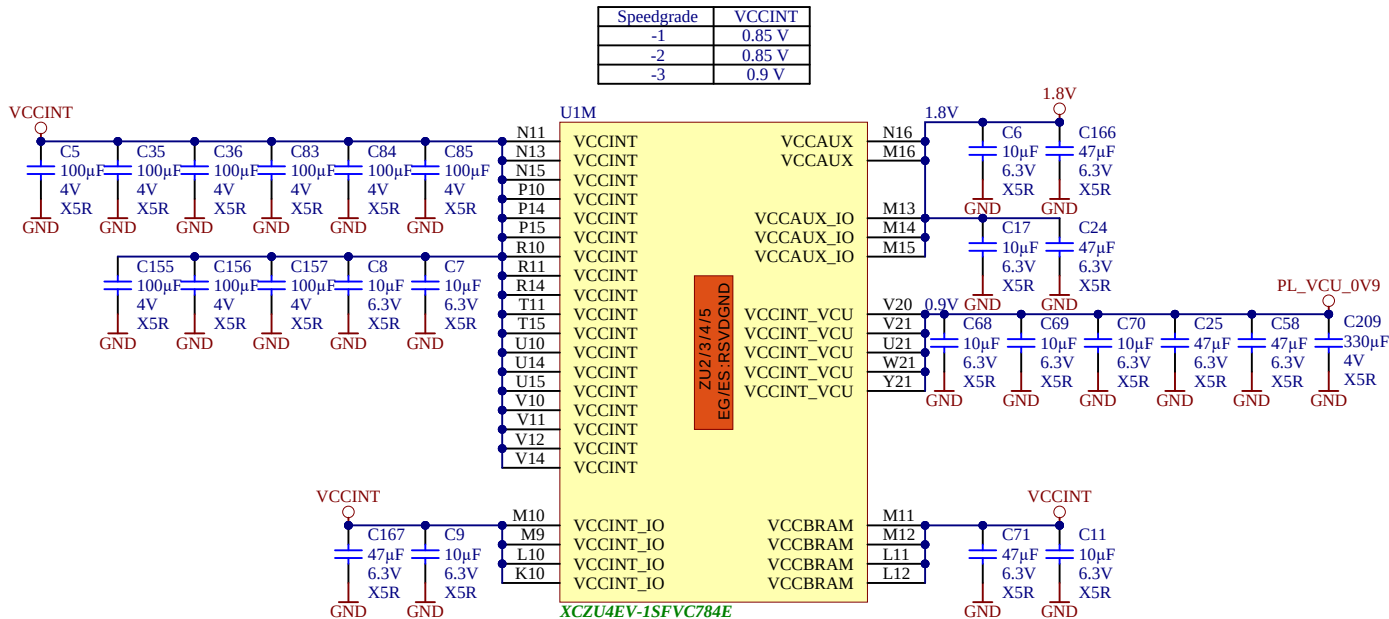
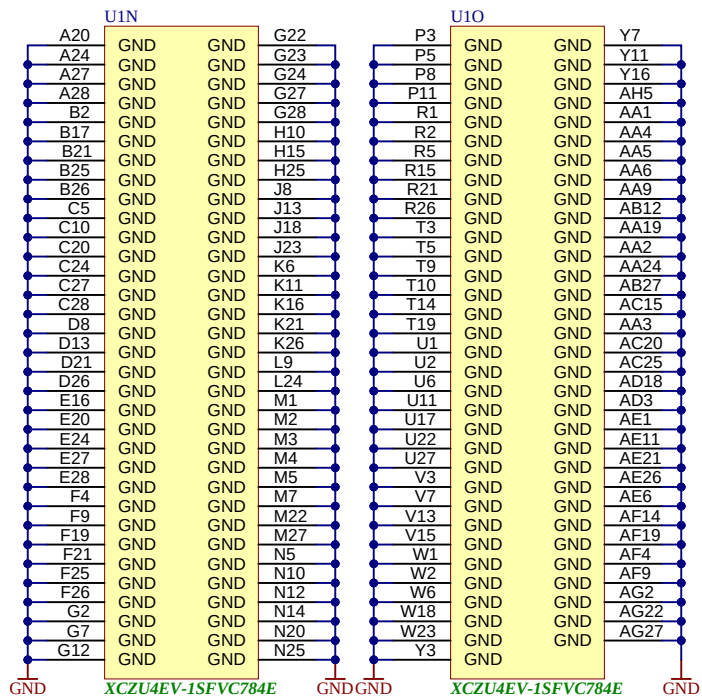
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Title: AM0010 – CONFIG		
A4	Number: CONFIG 4DE81MA	Rev. 02
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Filename: CONFIG.SchDoc		Page 13 of 30



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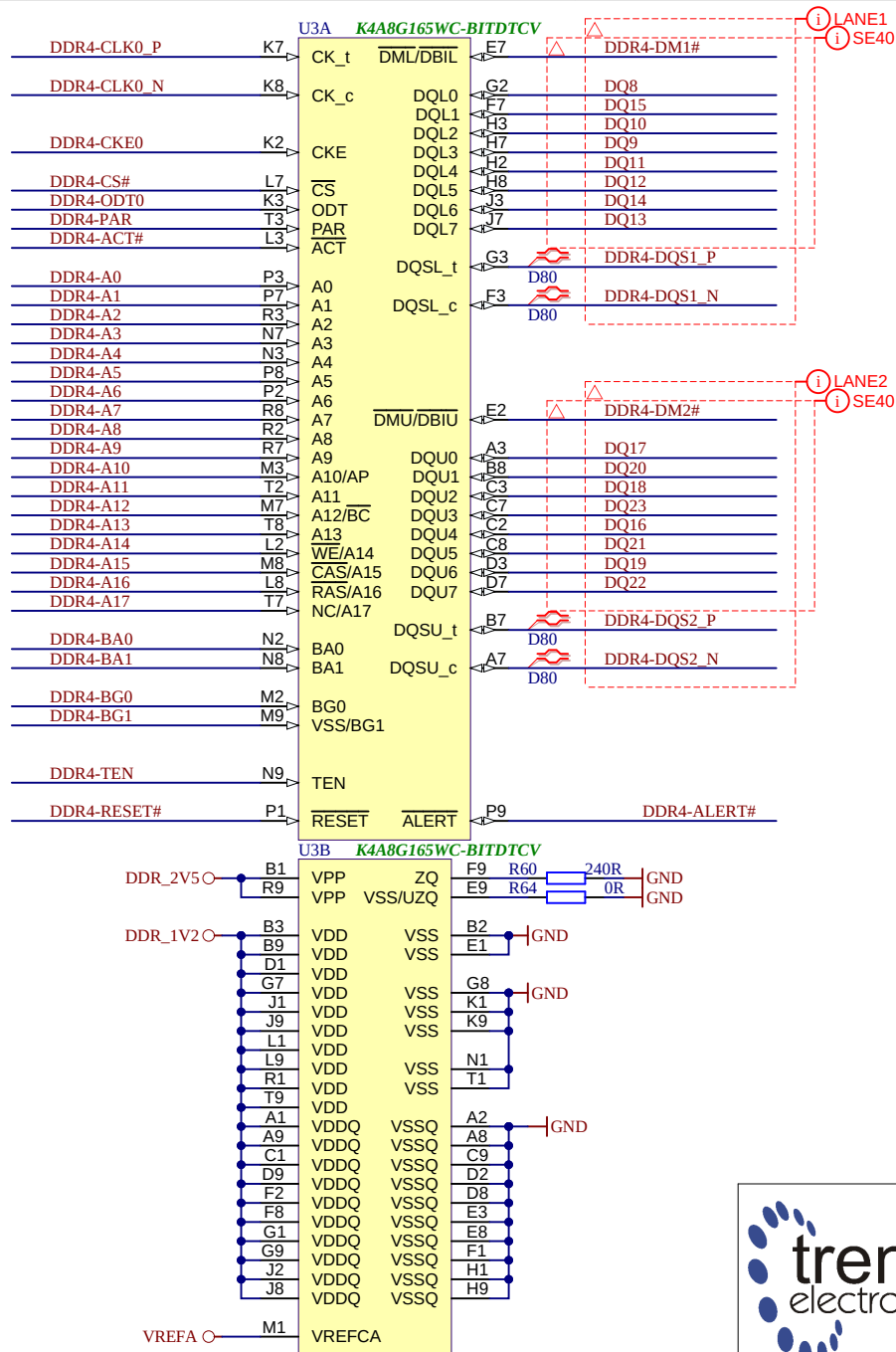
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Title: AM0010 – DDR4-RAM_2			
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Filename: DDR4-RAM_2.SchDoc			

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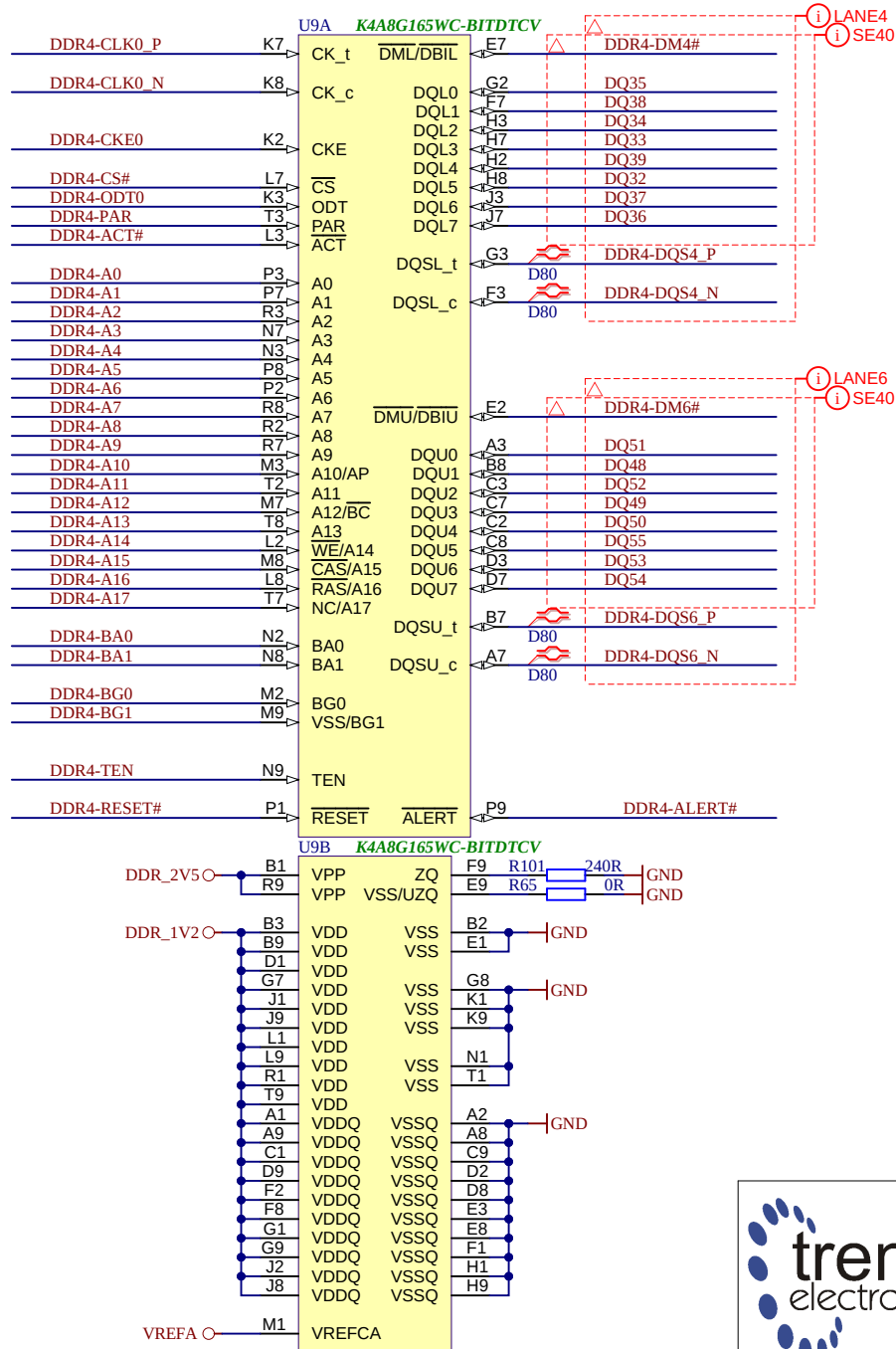
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Title: AM0010 – DDR4-RAM_3		
A4	Number: DDR4-RAM_3 4DE81MA	Rev. 02
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Filename: DDR4-RAM_3.SchDoc		

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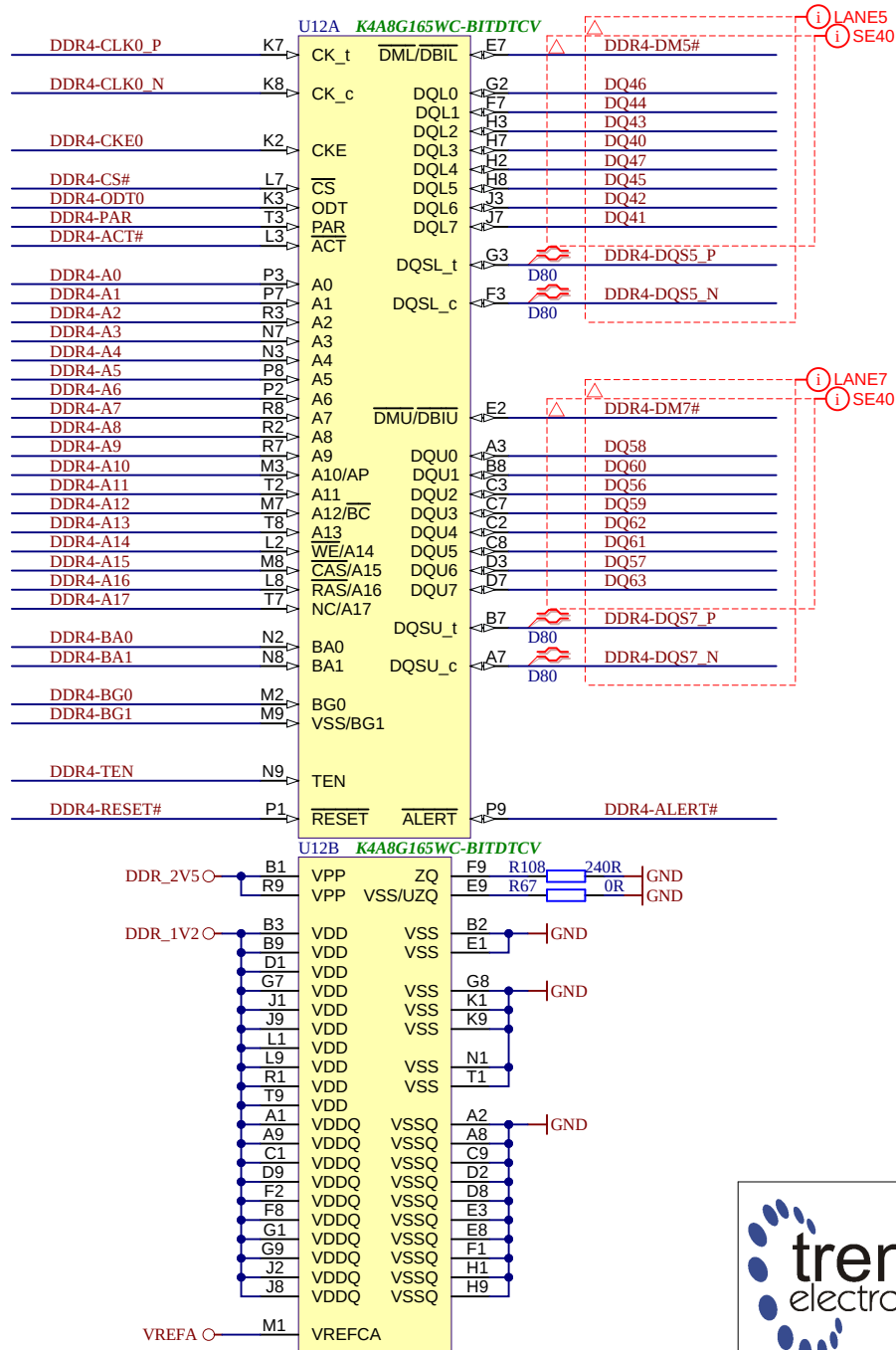
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Title: AM0010 – DDR4-RAM_4			
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Filename: DDR4-RAM_4.SchDoc			

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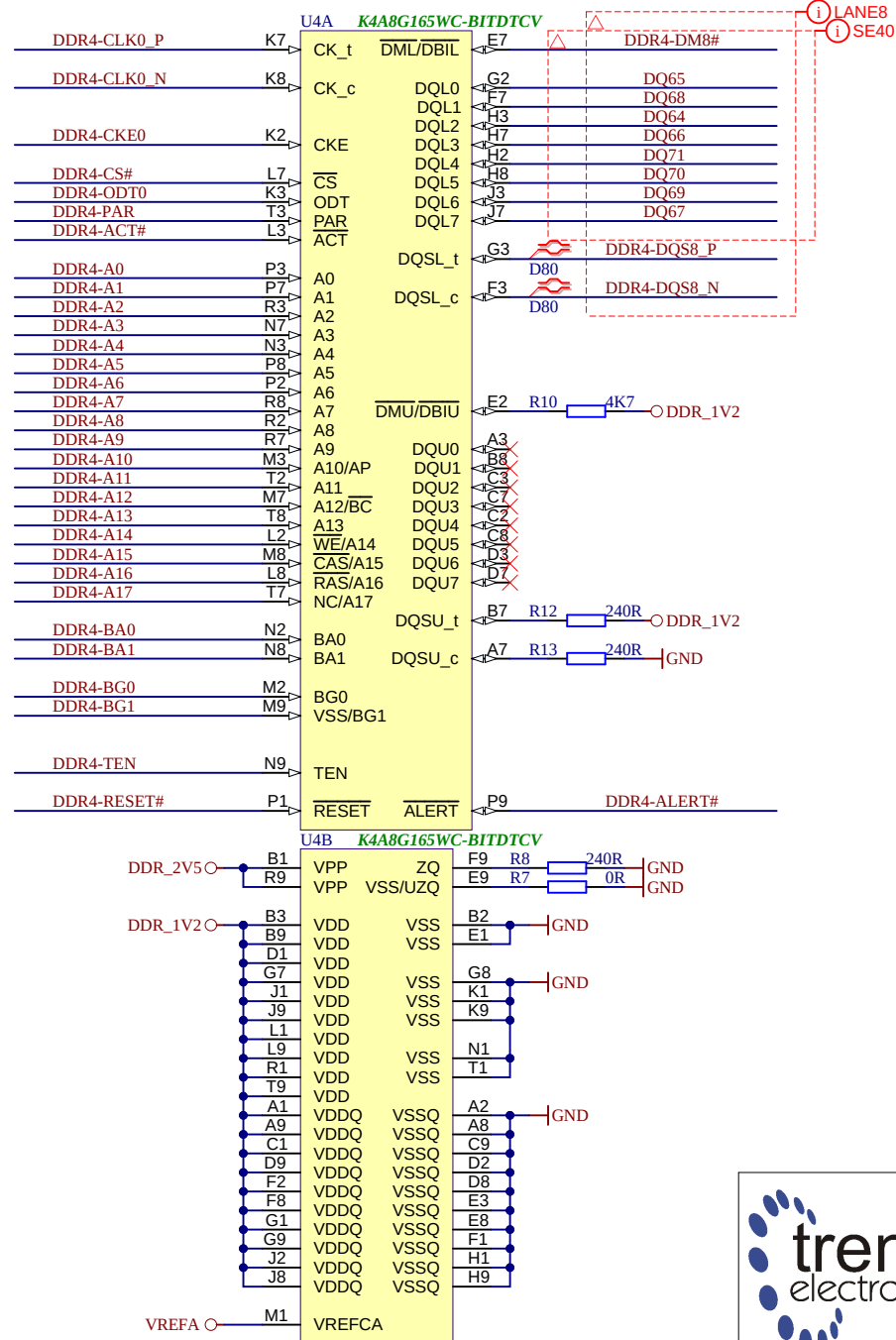
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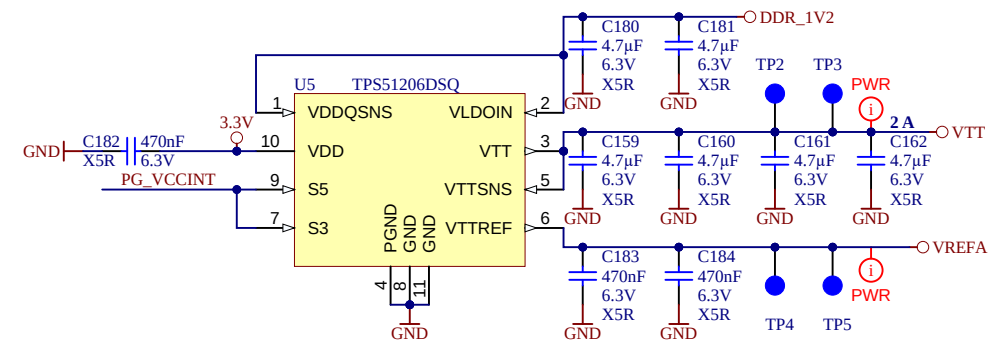
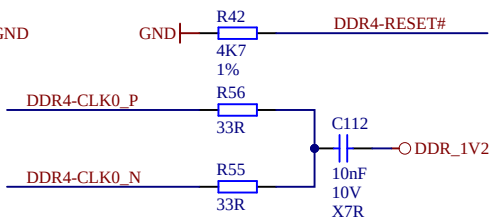
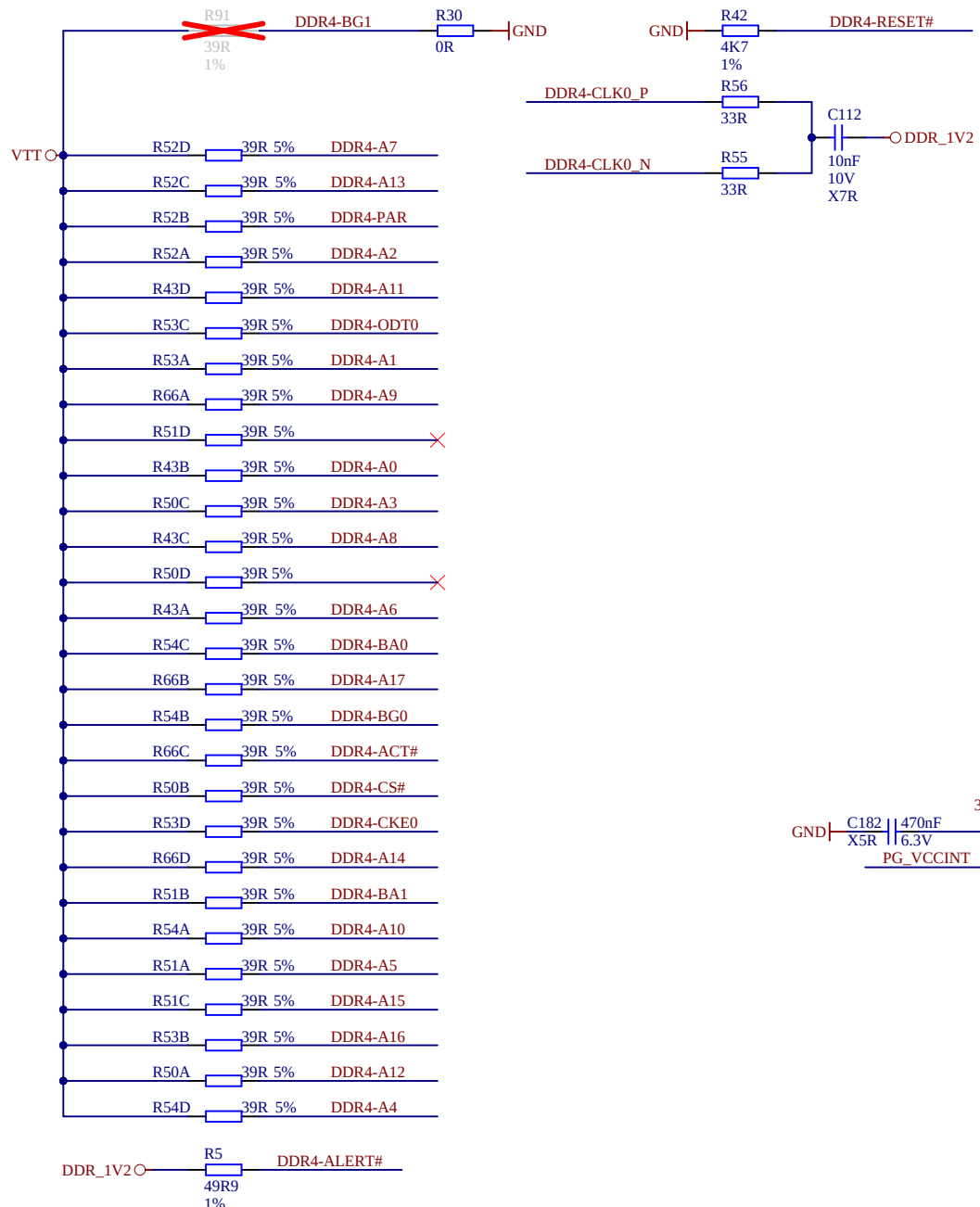
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Date: 01.08.2022	Copyright: Trenz Electronic GmbH	Page 21 of 30
Filename: DDR4-RAM_5.SchDoc		

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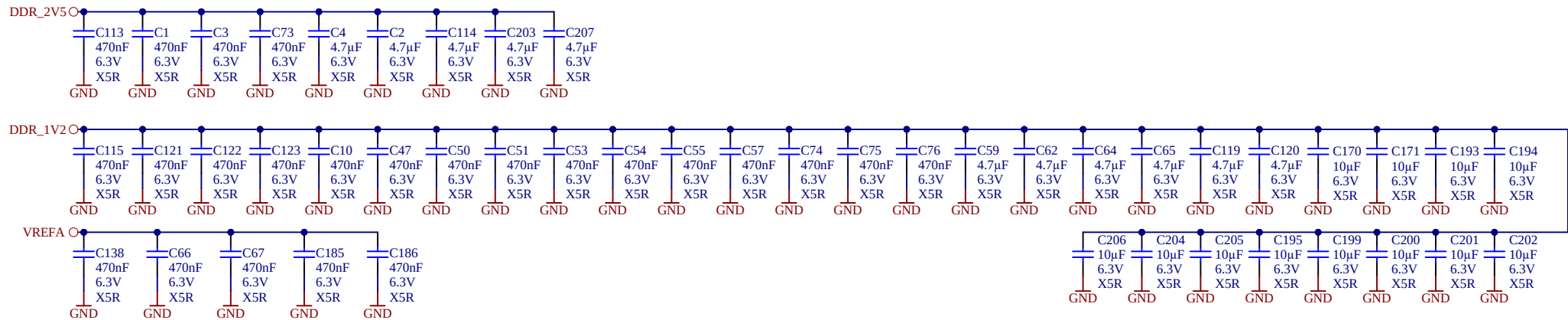
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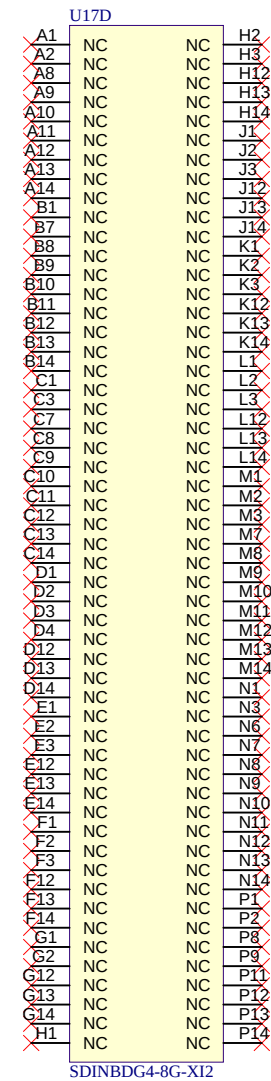
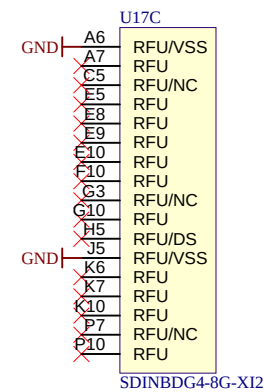
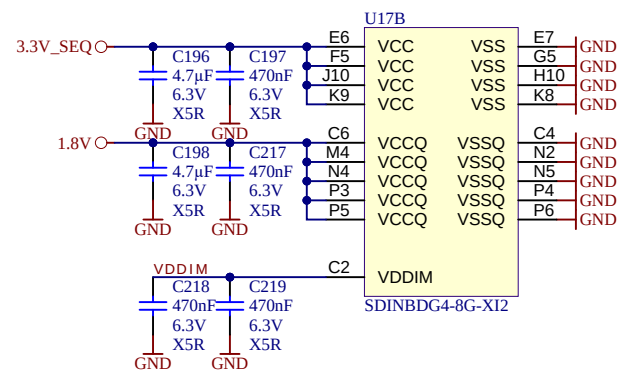
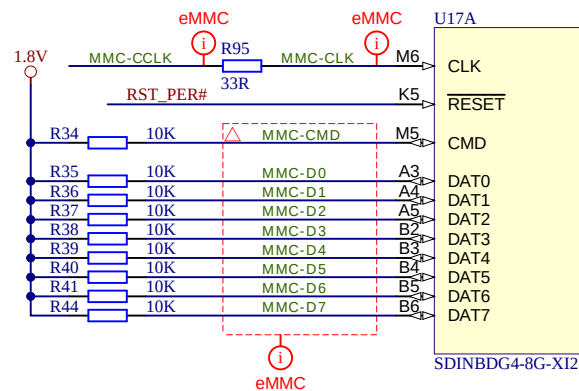
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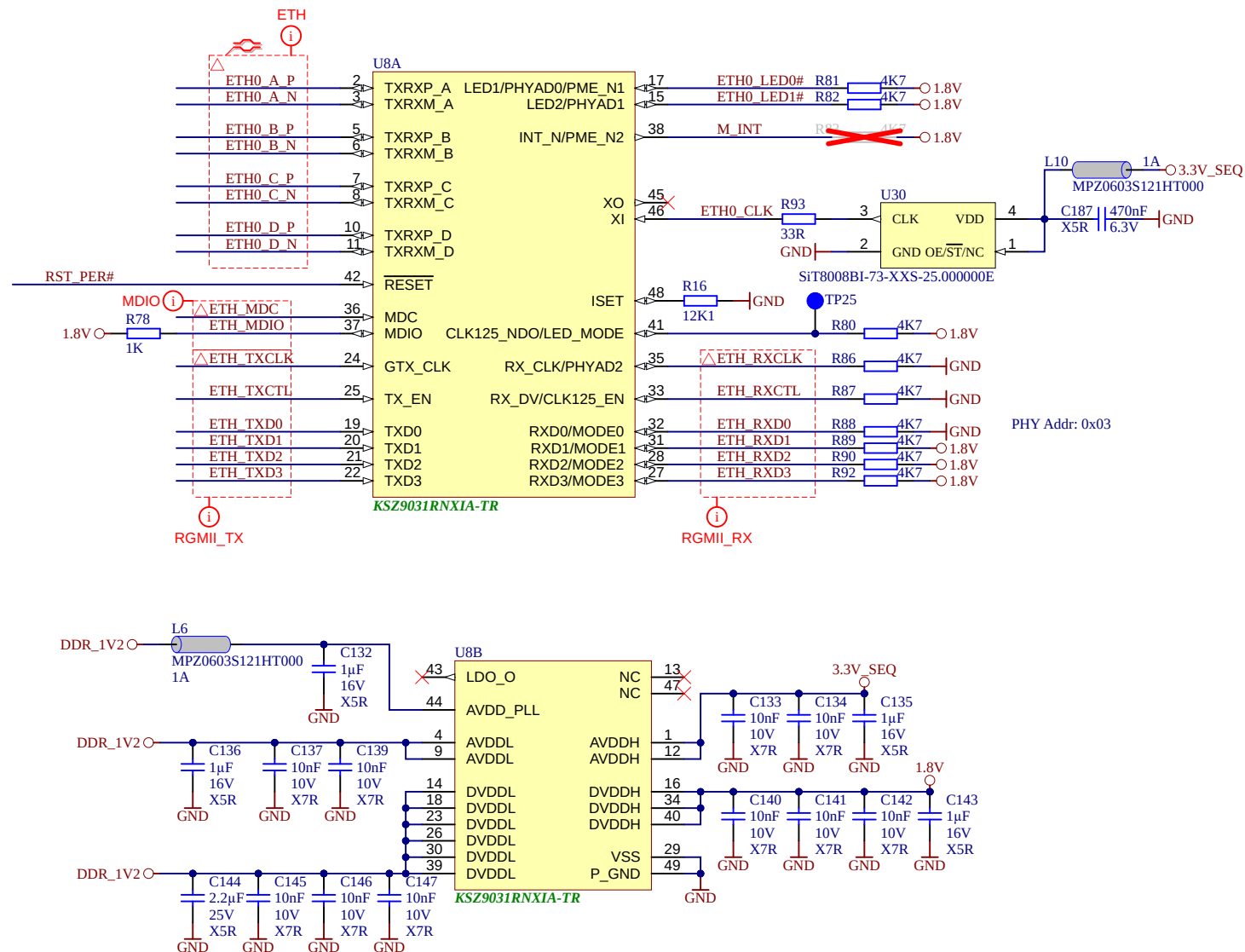


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A4	Number: DDR4-TERM 4DE81MA	Rev. 02
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Filename: DDR4-TERM.SchDoc		



	Title: AM0010 – DDR4-CAPS		
	A4	Number: DDR4-CAPS 4DE81MA	Rev. 02
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	Filename: DDR4-CAPS.SchDoc		





	Title: AM0010 – ETHPHY		
	A4	Number: ETHPHY 4DE81MA	Rev. 02
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	Filename: ETHPHY.SchDoc		



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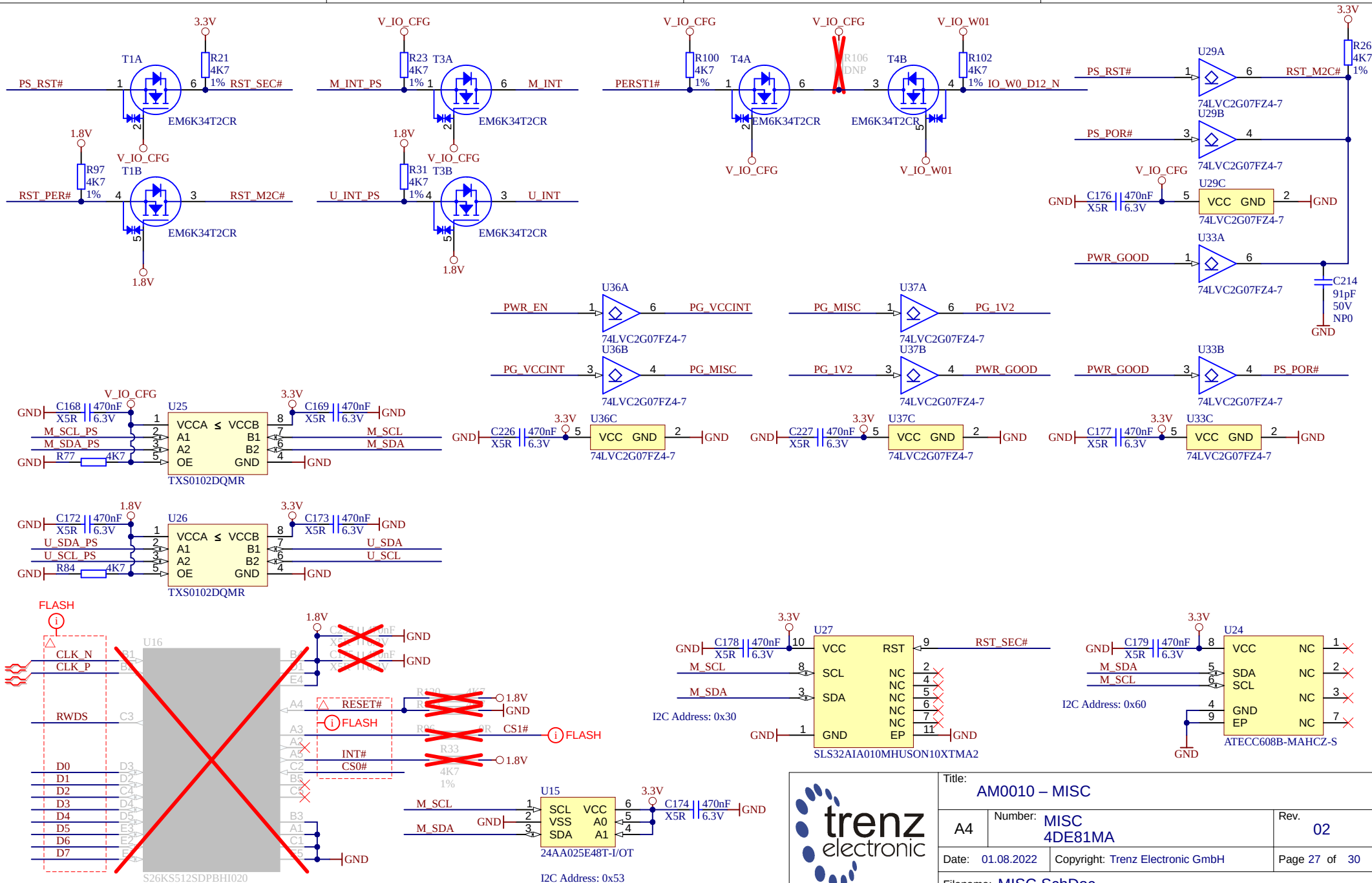
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Filename: MISC.SchDoc		

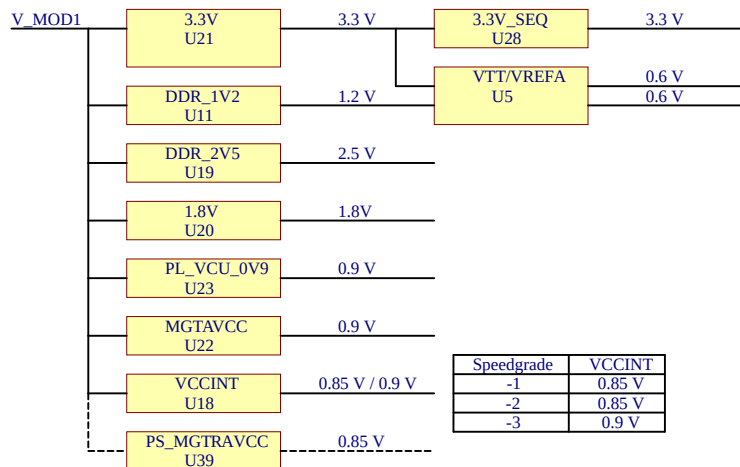
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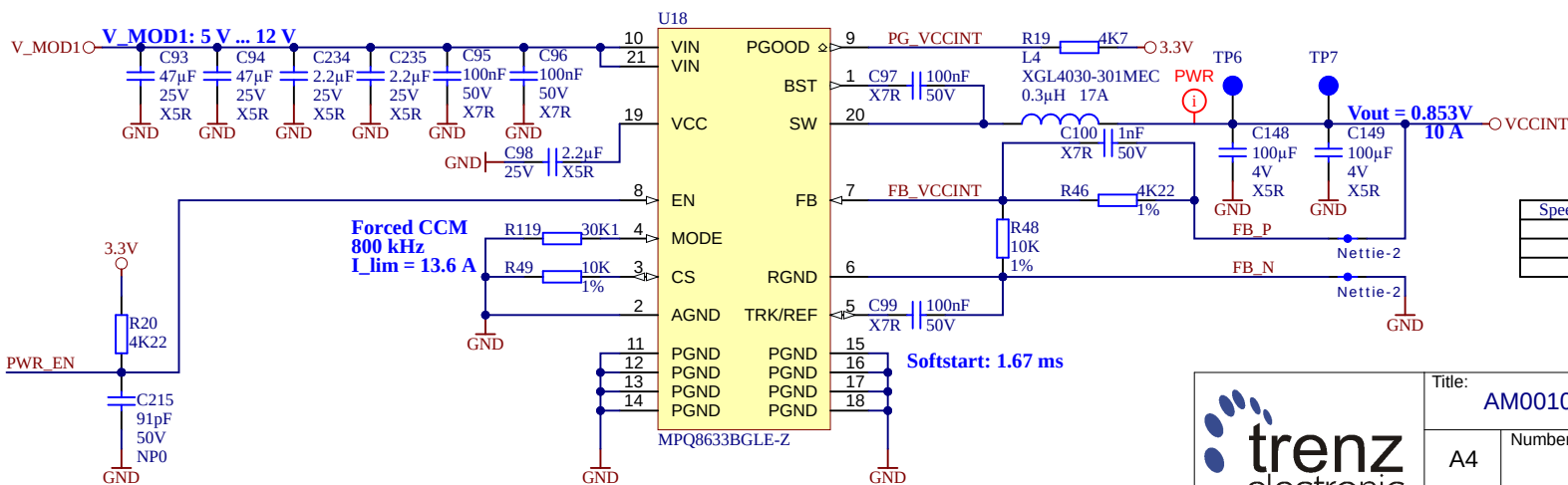
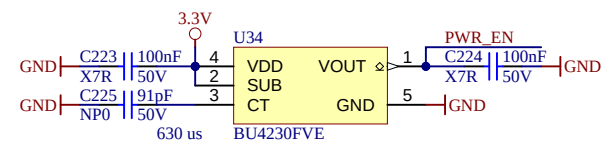
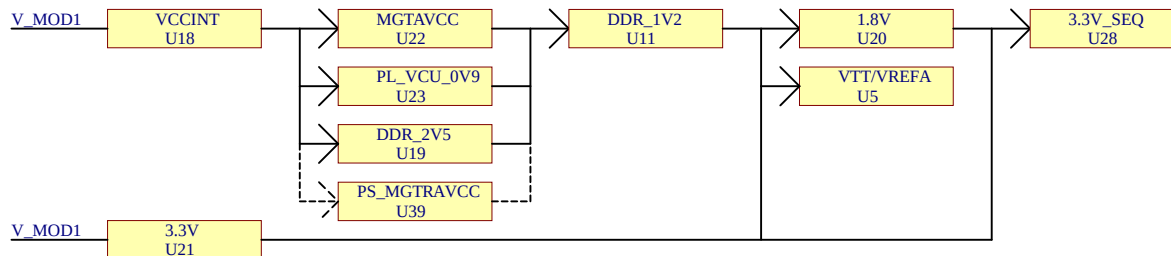
4

Power Supply Structure



Speedgrade	VCCINT
-1	0.85 V
-2	0.85 V
-3	0.9 V

Power Supply Sequencing



Speedgrade	R46	R48	C100	VCCINT
-1	4K22	10K	1 nF	0.853 V
-2	4K22	10K	1 nF	0.853 V
-3	10K	20K	680 pF	0.900 V



Title: AM0010 – POWER_1		
A4	Number: POWER_1 4DE81MA	Rev. 02
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