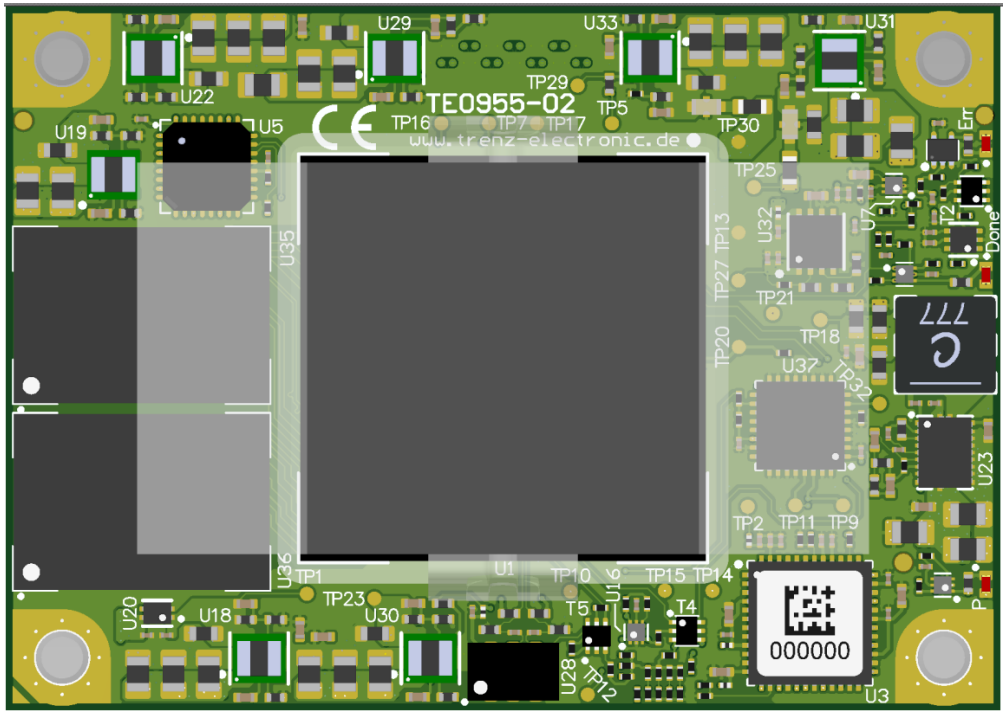

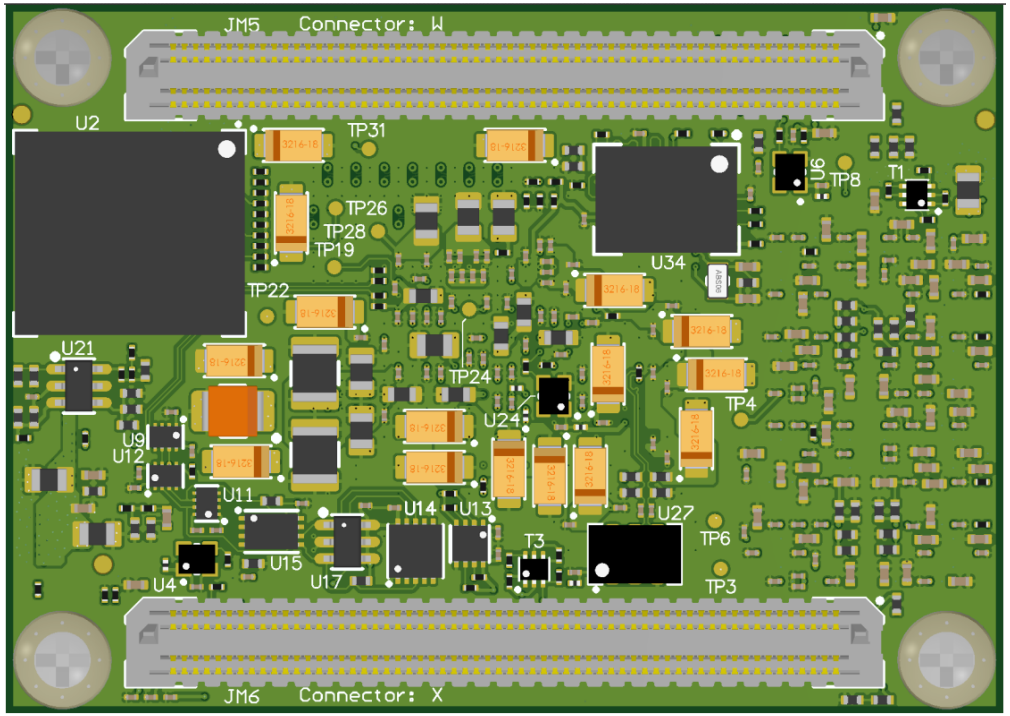

Regarding the usage of our schematics and alike documentation for Trenz module TE0955.

Project is protected under copyright and we strongly and strictly prohibit the reverse engineering or recreation, even if the design is just adapted or modified. TE0955 is protected under such right and in case of plagiarism we will have to do anything necessary in order to protect our assets.

Schematics and other handouts serve for informational purposes only!

trenz electronic	Title: Legal Notices		
	A4	Number: TE0955 EGBE32A	Rev. 02
	Date: 19.01.2026	Copyright: Trenz Electronic GmbH	Page 1 of 28
	Filename: Legal Notices Modules.SchDoc		

REV	Description	
-01	Initial revision	VY
-02	1. Set R156 to assembled and connected to GTYP_AVCC_AUX rail instead of 1V8. Removed patch including R156_NEW. 2. Corrected and updated silkscreen description of Connectors JM5 and JM6 to show also Andromeda connector naming W and X. Removed corresponding schematic notes. 3. Moved serial sticker from bottom to top. Now placed on Ethernet PHY U3. 4. Removed unnecessary zero Ohm R159. 5. Replaced 0402 resistor R157 by 0201 size version. 6. Moved C52 and C74 away from PCB edge/tab. 7. Moved R14, R16, R19, R60, R64, R79, C19,C52 C186 C194, C196, C203 L11 away from ATS holder area. 8. Replaced buffers U7, U10, U16, U25 from 74LVC2G07FZ4-7 to SN74LVC2G07DSF2 9. Added TPM module U37 with corresponding circuits 10. Added option for additional pullups (not fitted: R196, R197) on OM_I2C bus 11. Moved Testpoints: TP7, TP9, TP13, TP16, TP20, TP21, TP27 12. Updated schematic overview page template 13. Added Note that U14, U15 may be removed in future. 14. Added OSPI assembly option (R198, R199) to use MIO12 as dedicated reset instead of ECS#. 15. Change ETH PHY from LAN8830 to LAN8840 to support TSN/PTP	MR

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Title: Revision Changes

A4 | Number: TE0955 EGBE32A | Rev. 02

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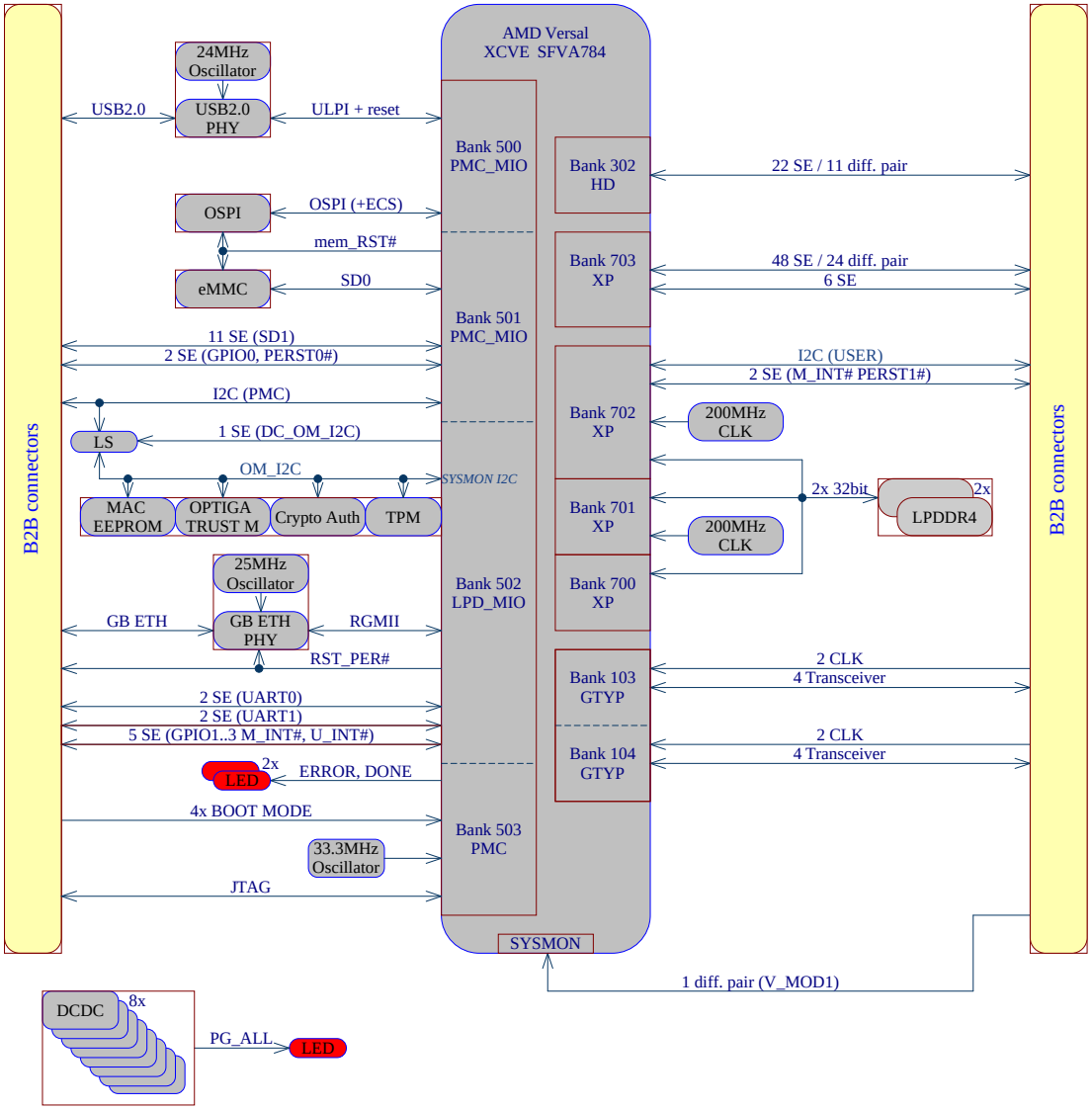
Filename: Revision_Changes.SchDoc

Block Diagram:



Supported Voltage Ranges:

Power Rail	Direction	Range	Tolerance	Description	Note
V_MOD1	IN	5V .. 12V	+/-5%	Module main power supply	-
3V3_AO	OUT	3.3V	+/-3%	Always On rail; Periphery supply	Max additional (low ESR) capacitance: 20µF
1V8	OUT	1.8V	+/-3%	Periphery supply	PMC 500 and LPD 502; Max additional (low ESR) capacitance: 40µF
V_IO_W01	IN	1.0V .. 1.5V	+/-5%	IO supply	XPIO Bank 703
V_IO_X3	IN	1.8V .. 3.3V	+/-3%	IO supply	HD Bank 302
V_IO_CFG	IN	1.8V	+/-5%	IO CFG supply	PMC 501 and 503; 3V3 possible, but not recommended. UART signals have 1.8V voltage levels, independent from V_IO_CFG.
V_BAT	IN	1.35V	+/-10%	Supply for Versal BB RAM and RTC	If not used connect to GND

I2C Addresses:

Device	Bus	Address	Note
Versal PL (or PS via EMIO)	U_I2C (PL)	- -	USER I2C Bus, B2B pin J2B-D33, J2B-D34 to Baseboard
Versal PMC	M_I2C (PMC)	- -	Master
-	M_I2C (PMC)	- -	Module I2C Bus ,B2B pin J2B-C50, J2B-C51 to Baseboard

Following devices are connected OM_I2C which is connected to M_I2C via Levelshifter (U12) and can be dissconnected by PS GPIO

24AA025E48T-I/OT (U17)	OM_I2C	1010011 53H	EEPROM for MAC with user area
SLS32AIA010MLUSON10XTMA2 (U14)	OM_I2C	0110000 30H	Crypto Authentication
ATECC608B-MAHCZ-S (U15)	OM_I2C	110000 60H	OPTIGA TRUST M Cryptographic Co-Processor
ST33KTPM2I3WBZA9 (U37)	OM_I2C	0101110 2EH	STSAFE-TPM 2.0
Versal SYSMON	OM_I2C	software defined	I2C address see reference design

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Title: Overview

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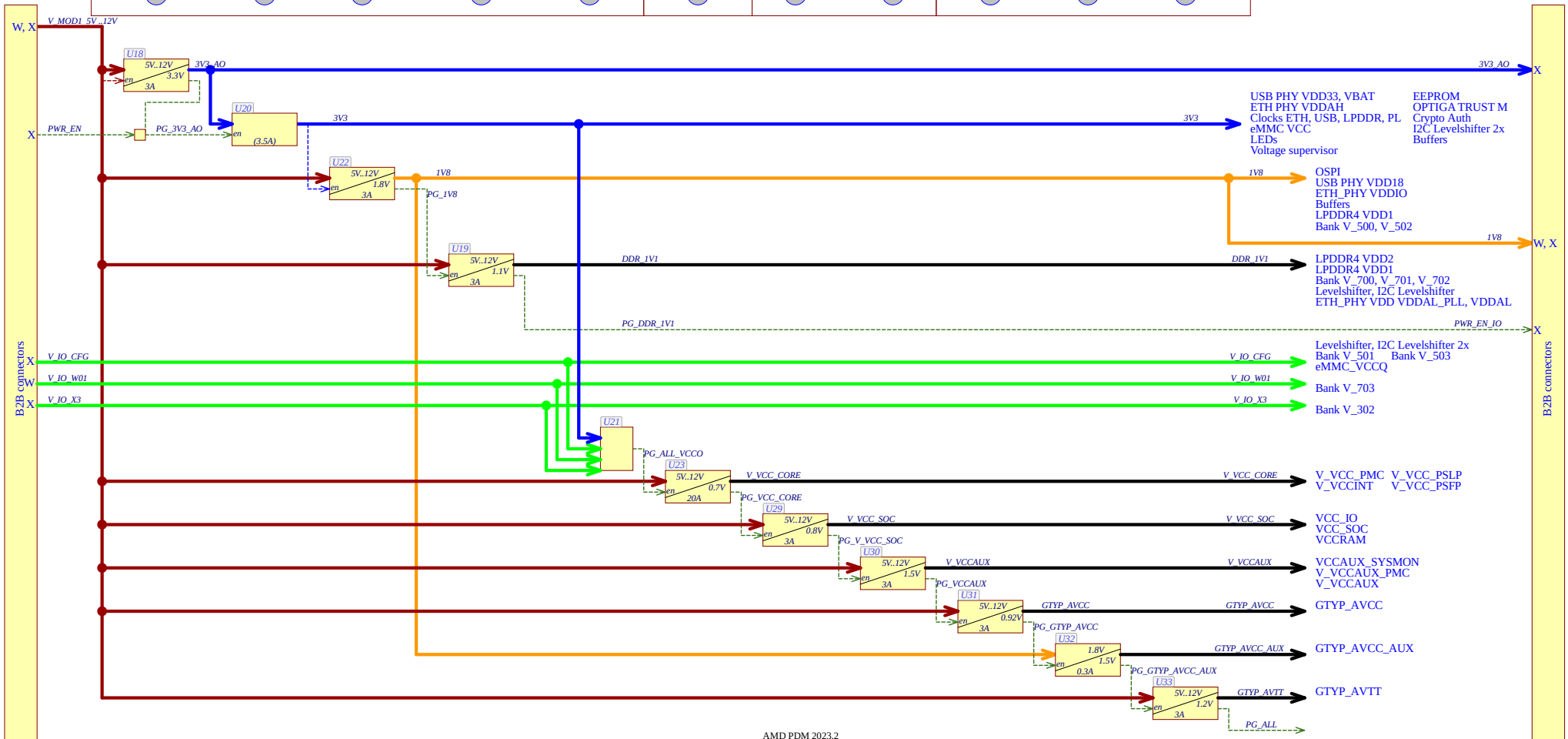
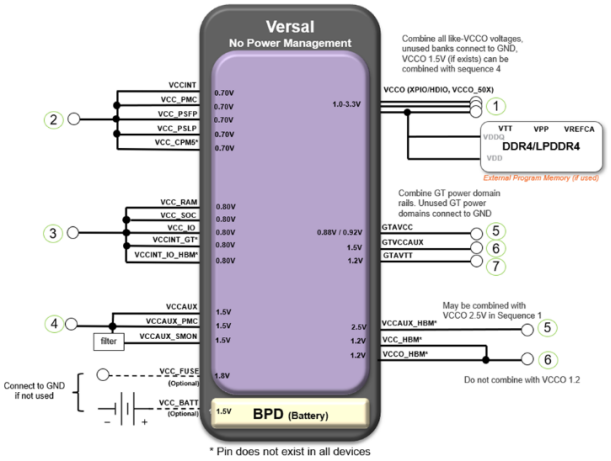
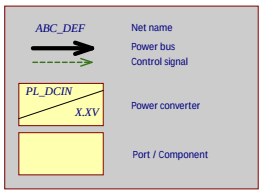


AMD PDM 2023.2

- ETH PHY (U2): 3V3 and 1V8 before 1V1_DDR





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Title: Power Diagram		
A3	Nummer: TE0955 EGBE32A	Rev. 02
Datum: 2025-08-22	Copyright:Trenz Electronic GmbH	Page 4 of 28
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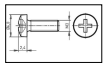
Special notes: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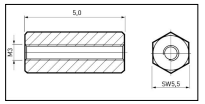
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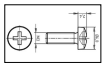
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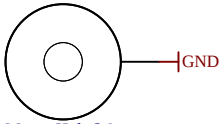

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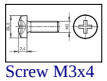
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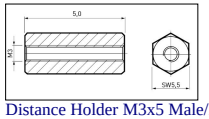
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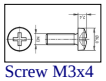
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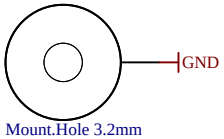

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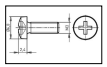
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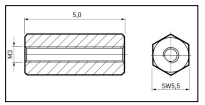
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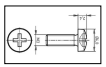
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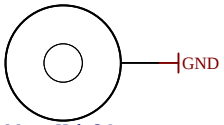

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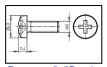
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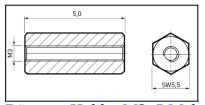
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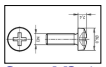
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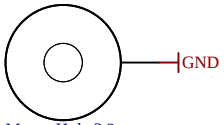
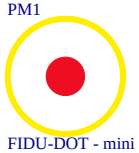


Mount.Hole 3.2mm

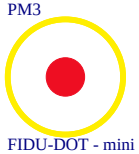
PM1


FIDU-DOT - mini

PM2

FIDU-DOT - mini

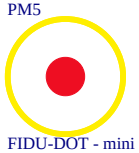
PM3


FIDU-DOT - mini

PM4

FIDU-DOT - mini

PM5


FIDU-DOT - mini

PM6

FIDU-DOT - mini

Serial1

Serial

Serialnumber 6,3 x 6.3mm

LOGO1

CE Logo on Top Overlay

CE-TOPOVERLAY

Design drawn by:	MR
Checked by:	MT
Assembly variant:	EGBE32A
Created by:	MR
Modified by:	MR
Modified at:	2025-02-06

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Title: TE0955

A4	Number: TE0955 EGBE32A	Rev. 02
Date: 19.01.2026	Copyright: Trenz Electronic GmbH	Page 5 of 28
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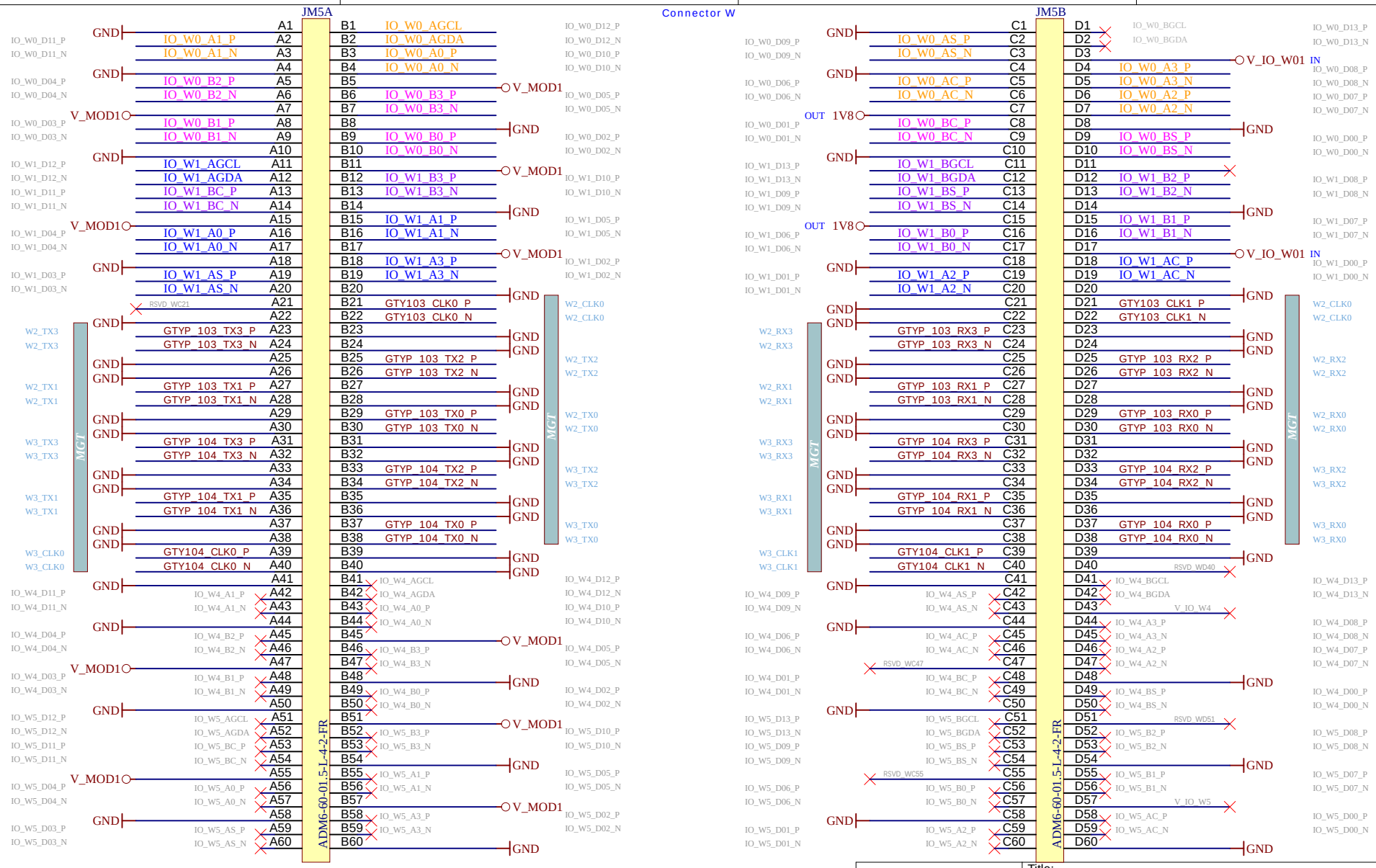

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Date: 19.01.2026	Copyright: Trenz Electronic GmbH		Page 6 of 28
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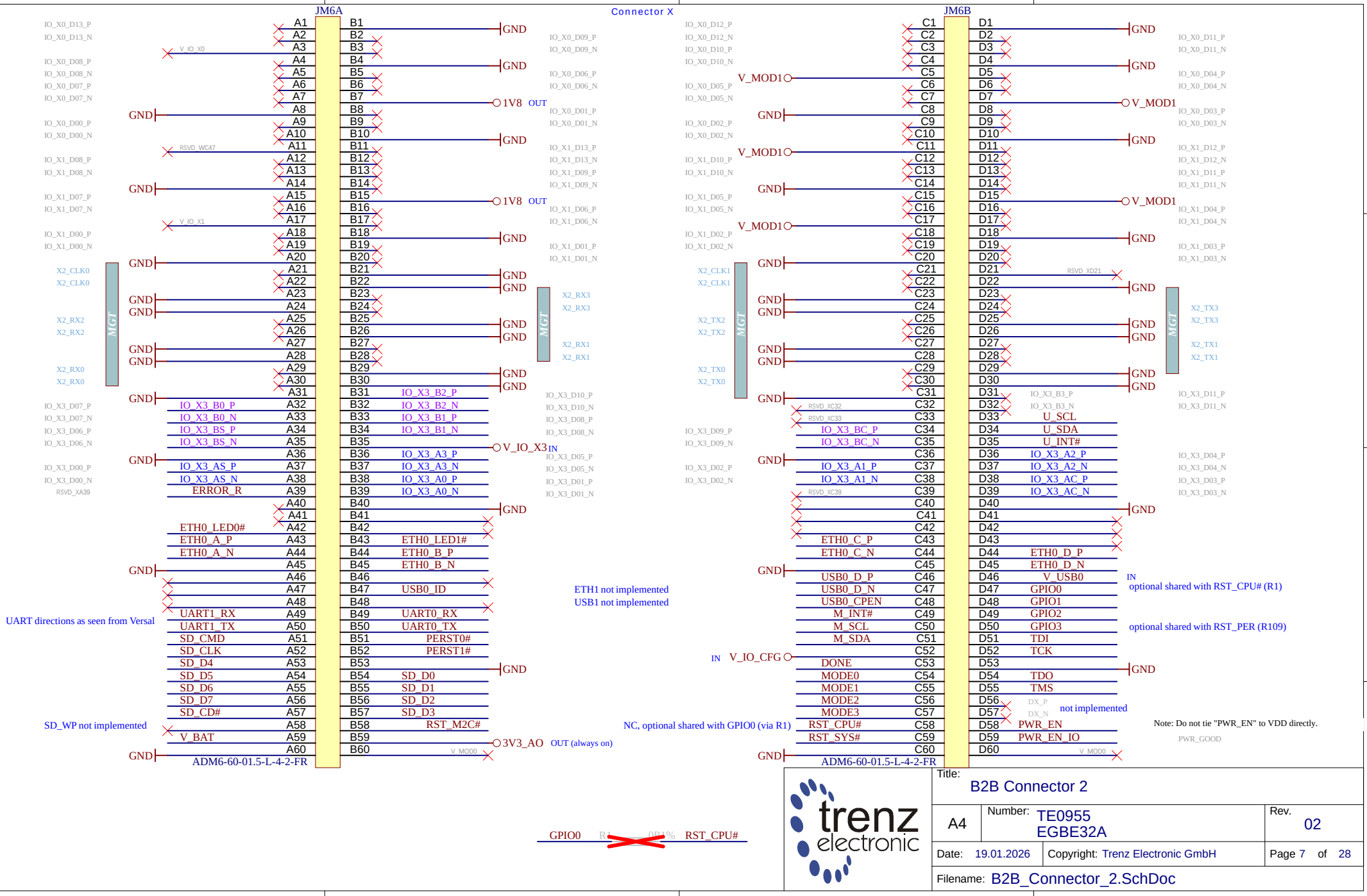

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Rev.	02
Date: 19.01.2026	Copyright: Trenz Electronic GmbH
Page 7 of 28	
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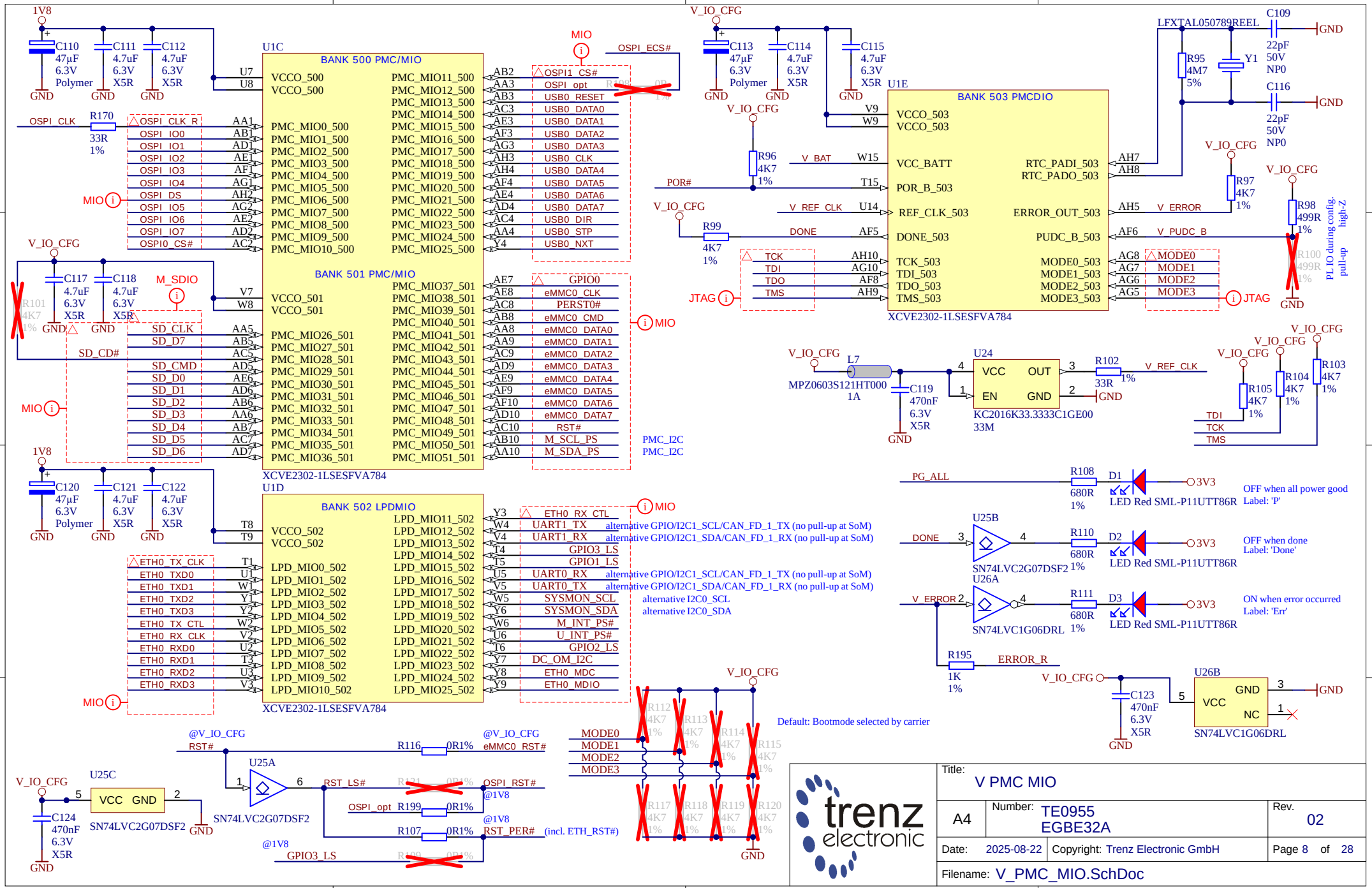
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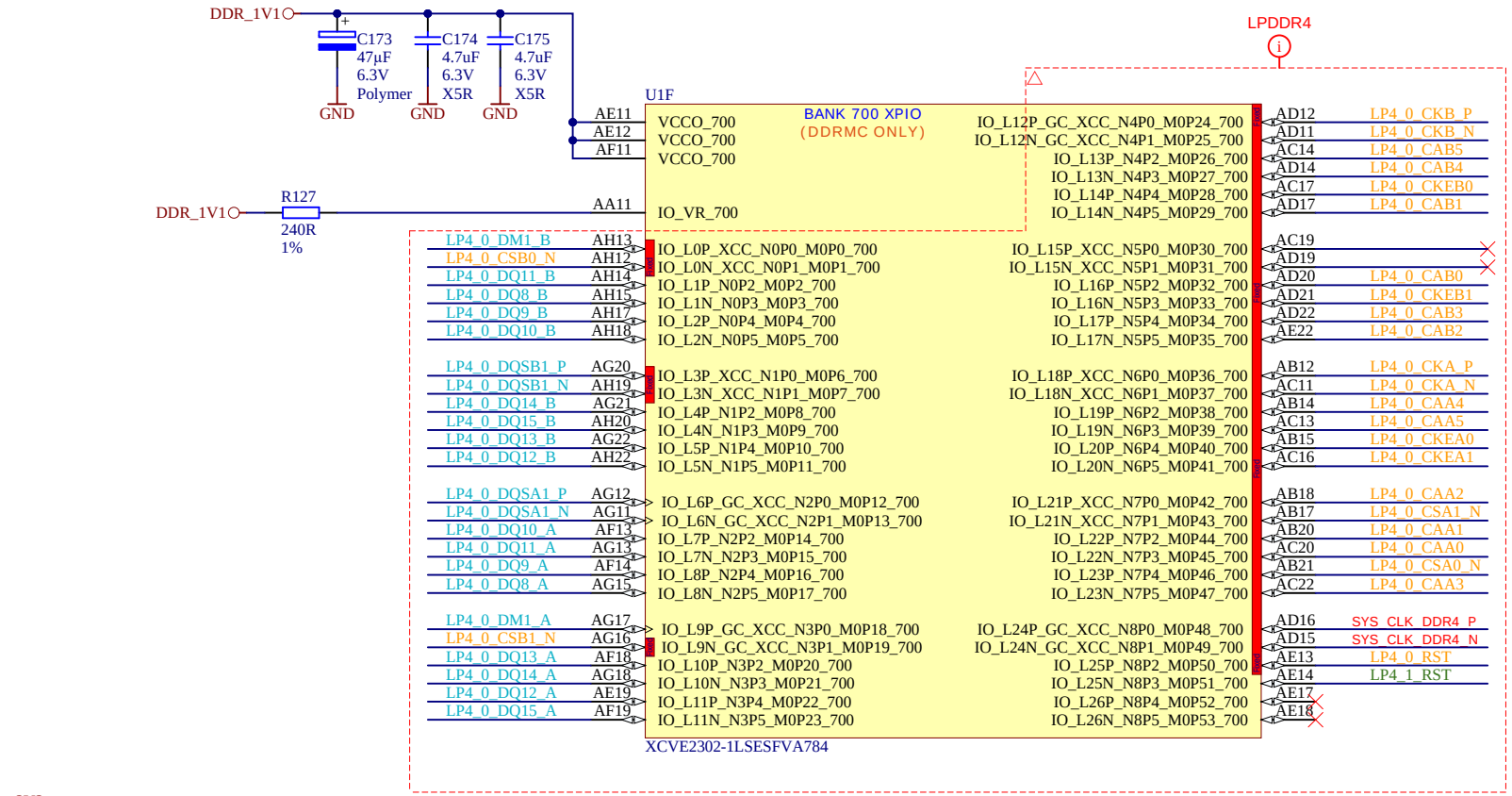
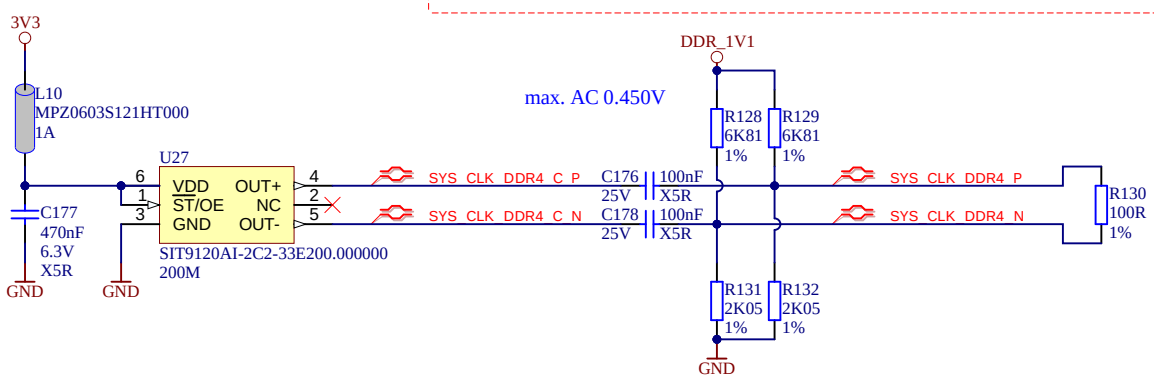
BANK 500 PMC/MIO
BANK 501 PMC/MIO
XCVE2302-1LSESFVA784
U1D
BANK 502 LPDMIO
XCVE2302-1LSESFVA784
U1E
BANK 503 PMCDIO
XCVE2302-1LSESFVA784
1V8
V_IO_CFG
MIO
M_SDIO
JTAG
PMC_I2C
OSPI_ECS#
OSPI_CLK
SD_CD#
POR#
V_BAT
V_REF_CLK
DONE
V_ERROR
V_PUDC_B
MODE0
MODE1
MODE2
MODE3
PL IO during config. high-Z pull-up
MPZ0603S121HT000 1A
KC2016K33.3333C1GE00 33M
LFXTAL050789REEL
PG_ALL
DONE
V_ERROR
ERROR_R
LED Red SML-P11UTT86R
SN74LVC2G07DSF2
SN74LVC1G06DRL
OFF when all power good Label: 'P'
OFF when done Label: 'Done'
ON when error occurred Label: 'Err'
Default: Bootmode selected by carrier
alternative GPIO/I2C1_SCL/CAN_FD_1_TX (no pull-up at SoM)
alternative GPIO/I2C1_SDA/CAN_FD_1_RX (no pull-up at SoM)
alternative I2C0_SCL
alternative I2C0_SDA
@V_IO_CFG RST#
@V_IO_CFG eMMC0_RST#
@1V8 OSPI_RST#
@1V8 RST_PER# (incl. ETH_RST#)
trenz electronic
Title: V PMC MIO
A4
Number: TE0955 EGBE32A
Rev. 02
Date: 2025-08-22
Copyright: Trenz Electronic GmbH
Page 8 of 28
Filename: V_PMC_MIO.SchDoc





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Title: V XPIO 700			
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Date: 2025-08-22	Copyright: Trenz Electronic GmbH		Page 9 of 28
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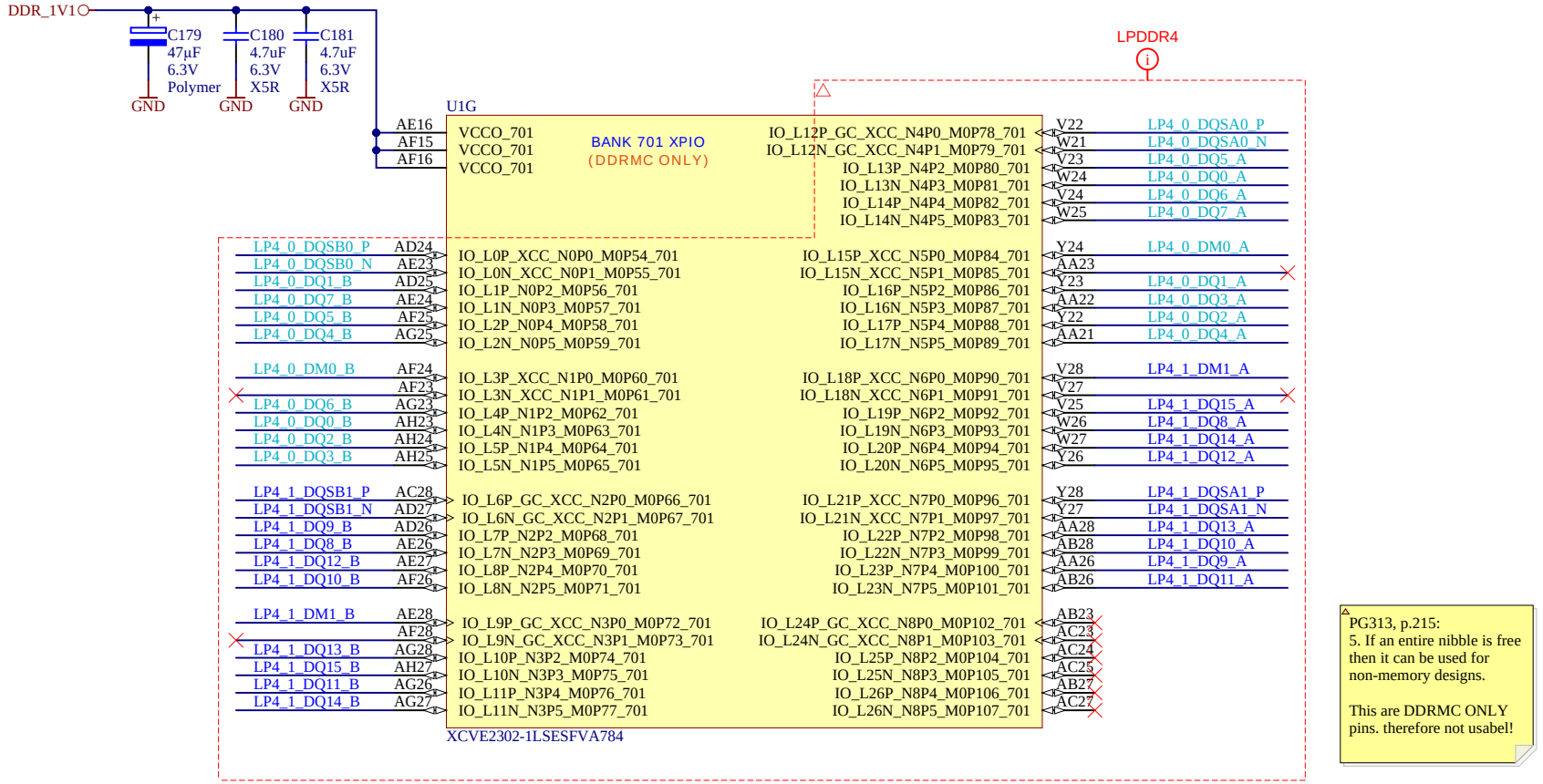

trenz electronic	Title: V XPIO 701		
	A4	Number: TE0955 EGBE32A	Rev. 02
	Date: 2025-08-22	Copyright: Trenz Electronic GmbH	Page 10 of 28
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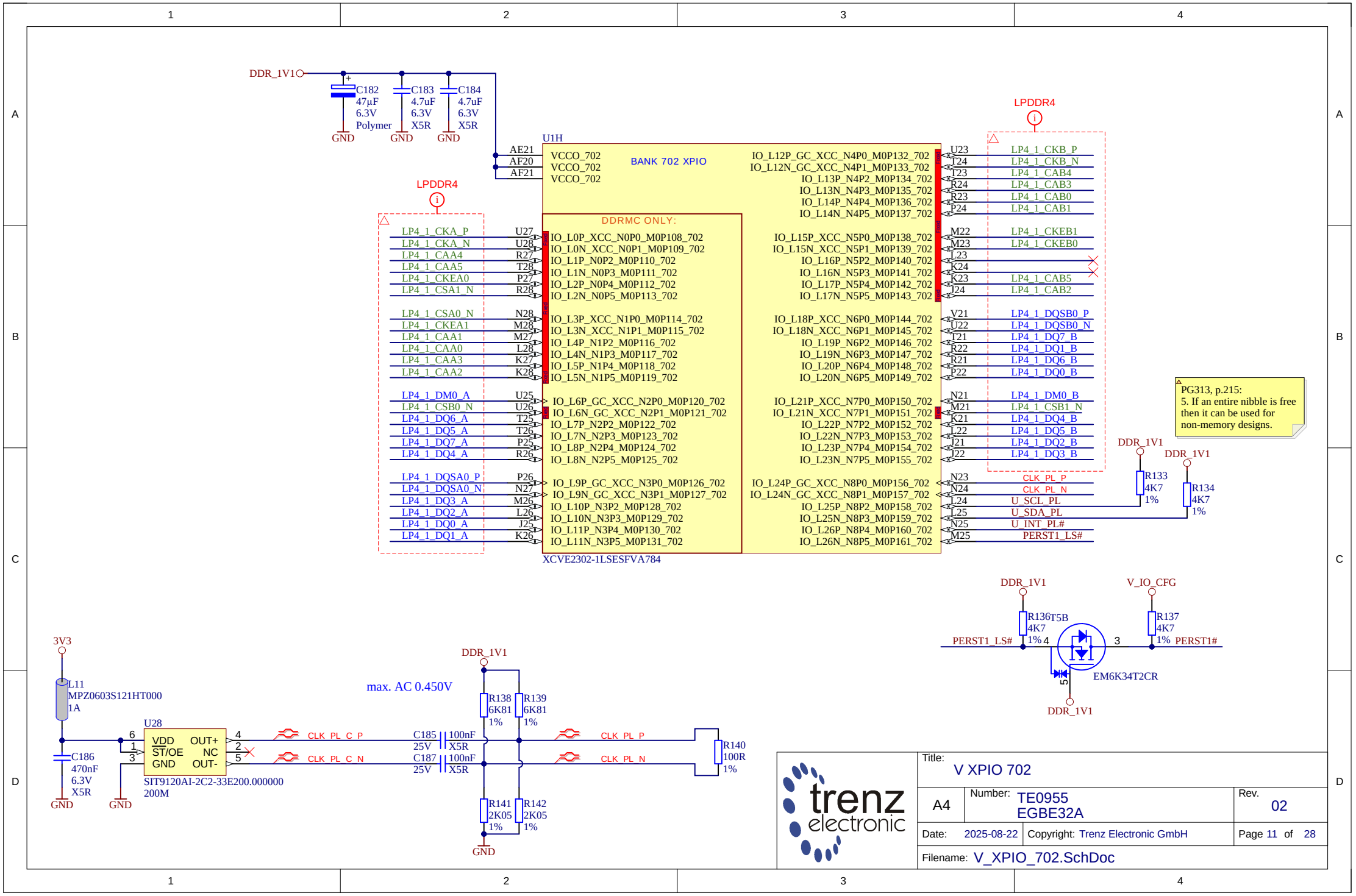
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C182 47µF 6.3V Polymer
C183 4.7uF 6.3V X5R
C184 4.7uF 6.3V X5R
GND
U1H
BANK 702 XPIO
VCCO_702
AE21
AF20
AF21
LPDDR4
DDRMC ONLY:
LP4_1_CKA_P U27
LP4_1_CKA_N U28
LP4_1_CAA4 R27
LP4_1_CAA5 T28
LP4_1_CKEA0 P27
LP4_1_CSA1_N R28
LP4_1_CSA0_N N28
LP4_1_CKEA1 M28
LP4_1_CAA1 M27
LP4_1_CAA0 L28
LP4_1_CAA3 K27
LP4_1_CAA2 K28
LP4_1_DM0_A U25
LP4_1_CSB0_N U26
LP4_1_DQ6_A T25
LP4_1_DQ5_A T26
LP4_1_DQ7_A P25
LP4_1_DQ4_A R26
LP4_1_DQSA0_P P26
LP4_1_DQSA0_N N27
LP4_1_DQ3_A M26
LP4_1_DQ2_A L26
LP4_1_DQ0_A J25
LP4_1_DQ1_A K26
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IO_L0N_XCC_N0P1_M0P109_702
IO_L1P_N0P2_M0P110_702
IO_L1N_N0P3_M0P111_702
IO_L2P_N0P4_M0P112_702
IO_L2N_N0P5_M0P113_702
IO_L3P_XCC_N1P0_M0P114_702
IO_L3N_XCC_N1P1_M0P115_702
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IO_L4N_N1P3_M0P117_702
IO_L5P_N1P4_M0P118_702
IO_L5N_N1P5_M0P119_702
IO_L6P_GC_XCC_N2P0_M0P120_702
IO_L6N_GC_XCC_N2P1_M0P121_702
IO_L7P_N2P2_M0P122_702
IO_L7N_N2P3_M0P123_702
IO_L8P_N2P4_M0P124_702
IO_L8N_N2P5_M0P125_702
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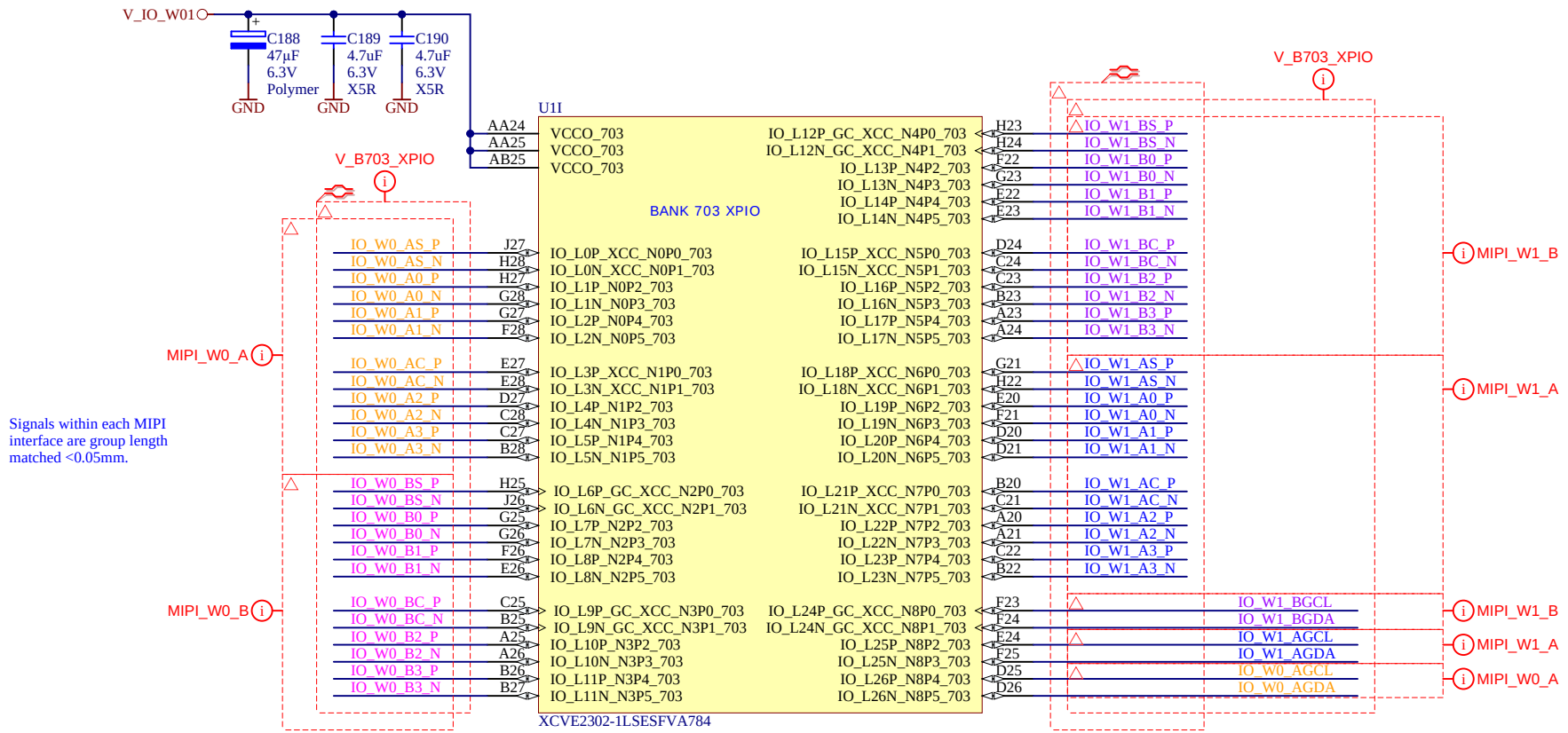
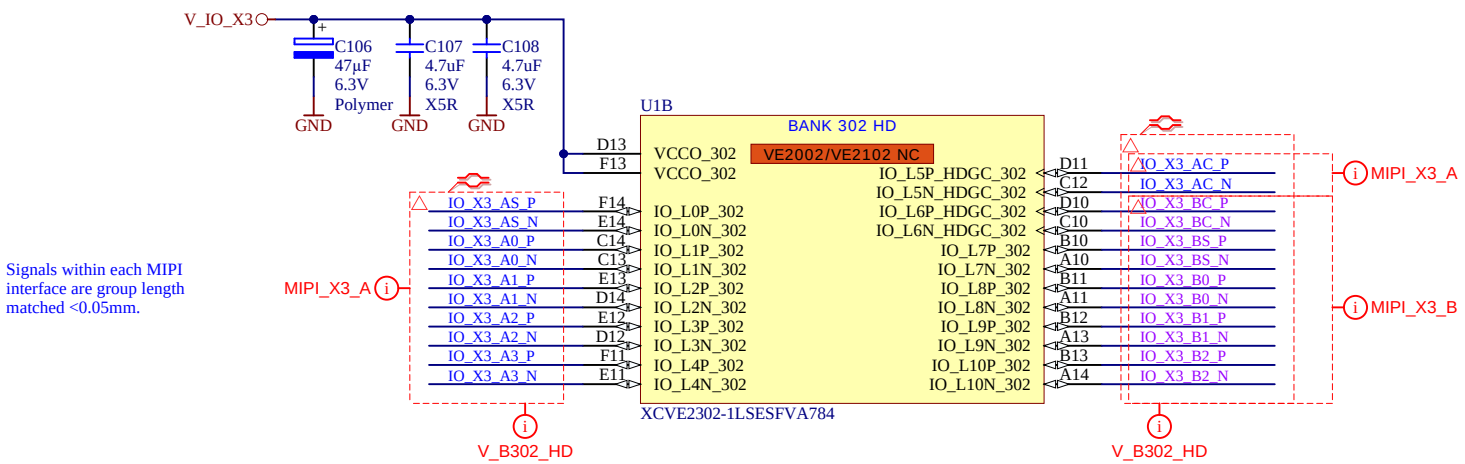
IO_L10N_N3P3_M0P129_702
IO_L11P_N3P4_M0P130_702
IO_L11N_N3P5_M0P131_702
IO_L12P_GC_XCC_N4P0_M0P132_702
IO_L12N_GC_XCC_N4P1_M0P133_702
IO_L13P_N4P2_M0P134_702
IO_L13N_N4P3_M0P135_702
IO_L14P_N4P4_M0P136_702
IO_L14N_N4P5_M0P137_702
IO_L15P_XCC_N5P0_M0P138_702
IO_L15N_XCC_N5P1_M0P139_702
IO_L16P_N5P2_M0P140_702
IO_L16N_N5P3_M0P141_702
IO_L17P_N5P4_M0P142_702
IO_L17N_N5P5_M0P143_702
IO_L18P_XCC_N6P0_M0P144_702
IO_L18N_XCC_N6P1_M0P145_702
IO_L19P_N6P2_M0P146_702
IO_L19N_N6P3_M0P147_702
IO_L20P_N6P4_M0P148_702
IO_L20N_N6P5_M0P149_702
IO_L21P_XCC_N7P0_M0P150_702
IO_L21N_XCC_N7P1_M0P151_702
IO_L22P_N7P2_M0P152_702
IO_L22N_N7P3_M0P153_702
IO_L23P_N7P4_M0P154_702
IO_L23N_N7P5_M0P155_702
IO_L24P_GC_XCC_N8P0_M0P156_702
IO_L24N_GC_XCC_N8P1_M0P157_702
IO_L25P_N8P2_M0P158_702
IO_L25N_N8P3_M0P159_702
IO_L26P_N8P4_M0P160_702
IO_L26N_N8P5_M0P161_702
U23 LP4_1_CKB_P
T24 LP4_1_CKB_N
T23 LP4_1_CAB4
R24 LP4_1_CAB3
R23 LP4_1_CAB0
P24 LP4_1_CAB1
M22 LP4_1_CKEB1
M23 LP4_1_CKEB0
L23
K24
K23 LP4_1_CAB5
J24 LP4_1_CAB2
V21 LP4_1_DQSB0_P
U22 LP4_1_DQSB0_N
T21 LP4_1_DQ7_B
R22 LP4_1_DQ1_B
R21 LP4_1_DQ6_B
P22 LP4_1_DQ0_B
N21 LP4_1_DM0_B
M21 LP4_1_CSB1_N
K21 LP4_1_DQ4_B
L22 LP4_1_DQ5_B
J21 LP4_1_DQ2_B
J22 LP4_1_DQ3_B
N23 CLK_PL_P
N24 CLK_PL_N
L24 U_SCL_PL
L25 U_SDA_PL
N25 U_INT_PL#
M25 PERST1_LS#
XCVE2302-1LSESFVA784
PG313, p.215:
5. If an entire nibble is free then it can be used for non-memory designs.
R133 4K7 1%
R134 4K7 1%
3V3
L11 MPZ0603S121HT000 1A
C186 470nF 6.3V X5R
U28
VDD ST/OE GND OUT+ NC OUT-
SIT9120AI-2C2-33E200.000000 200M
max. AC 0.450V
CLK_PL_C_P
CLK_PL_C_N
C185 100nF 25V X5R
C187 100nF 25V X5R
R138 6K81 1%
R139 6K81 1%
R140 100R 1%
R141 2K05 1%
R142 2K05 1%
CLK_PL_P
CLK_PL_N
V_IO_CFG
R136 4K7 1%
R137 4K7 1%
PERST1_LS#
PERST1#
T5B
EM6K34T2CR
trenz electronic
Title: V XPIO 702
A4
Number: TE0955 EGBE32A
Rev. 02
Date: 2025-08-22
Copyright: Trenz Electronic GmbH
Page 11 of 28
Filename: V_XPIO_702.SchDoc

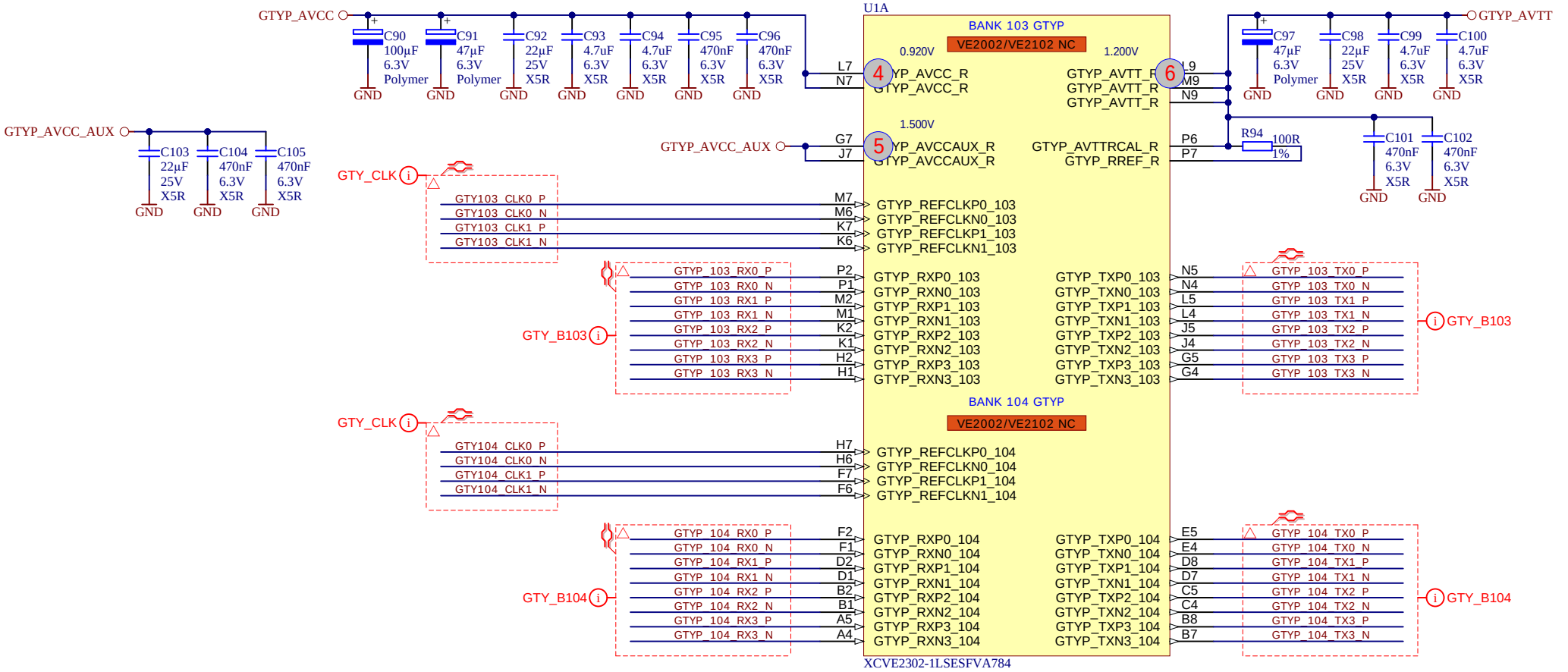


trenz electronic	Title: V XPIO 703		
	A4	Number: TE0955 EGBE32A	Rev. 02
	Date: 2025-08-22	Copyright: Trenz Electronic GmbH	Page 12 of 28
	Filename: V_XPIO_703.SchDoc		

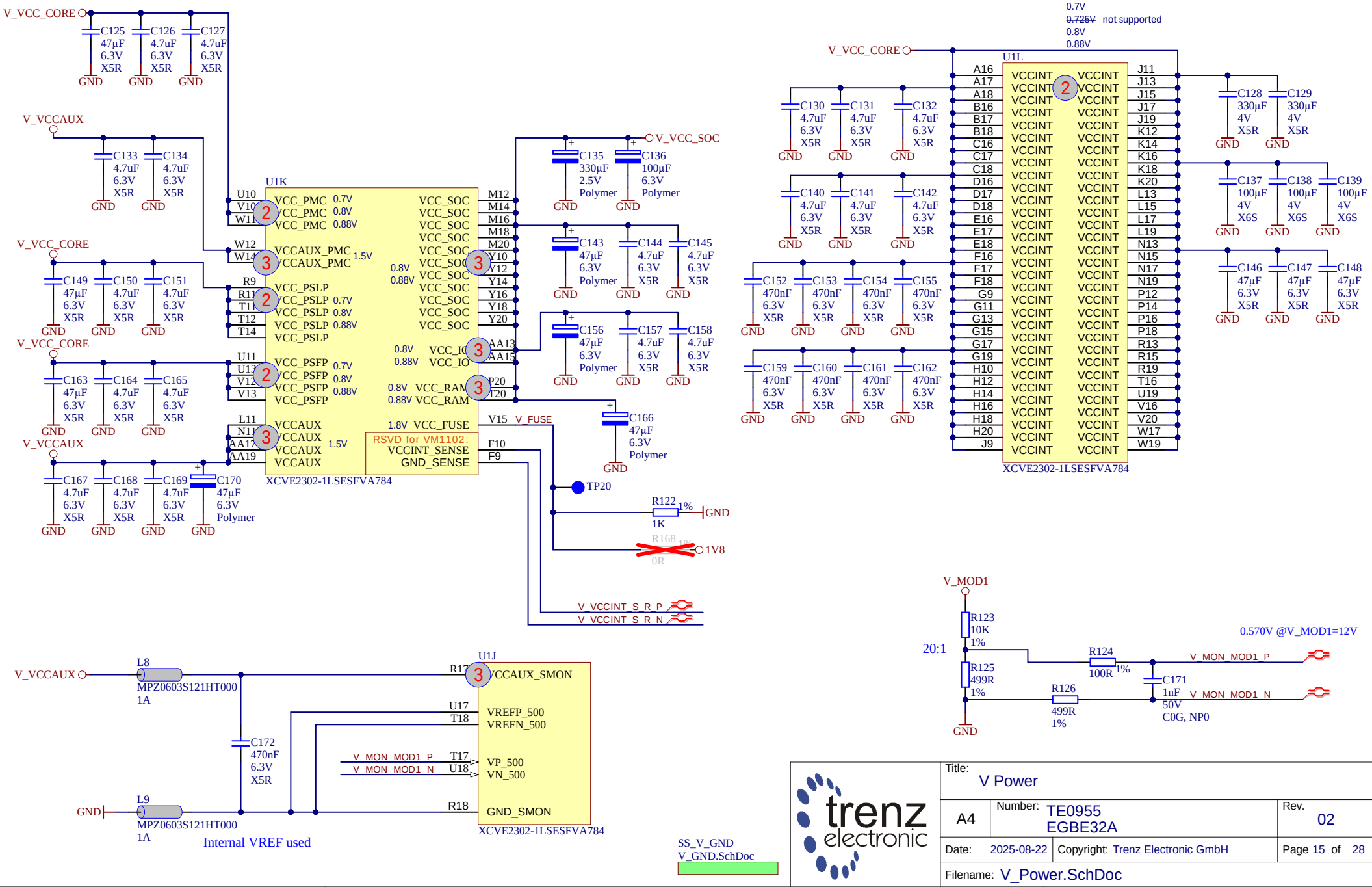


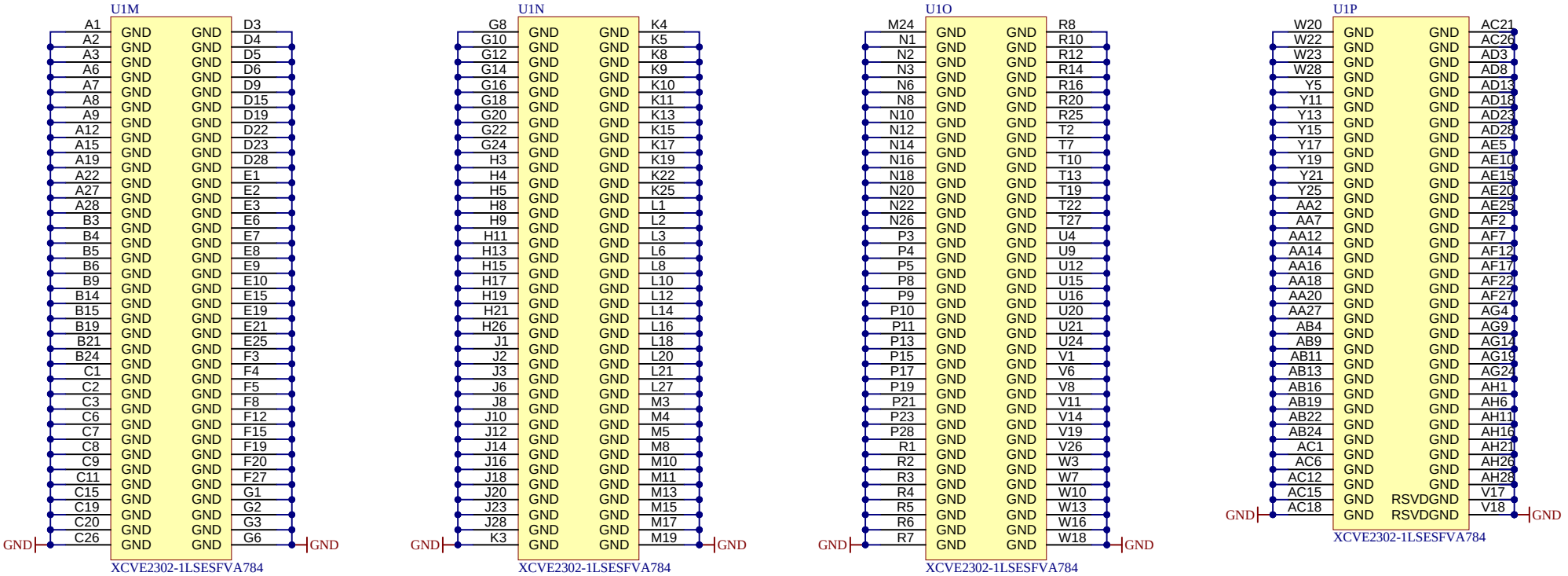
Signals within each MIPI interface are group length matched <0.05mm.

trenz electronic	Title: V HD		
	A4	Number: TE0955 EGBE32A	Rev. 02
	Date: 19.01.2026	Copyright: Trenz Electronic GmbH	Page 13 of 28
	Filename: V_HD.SchDoc		

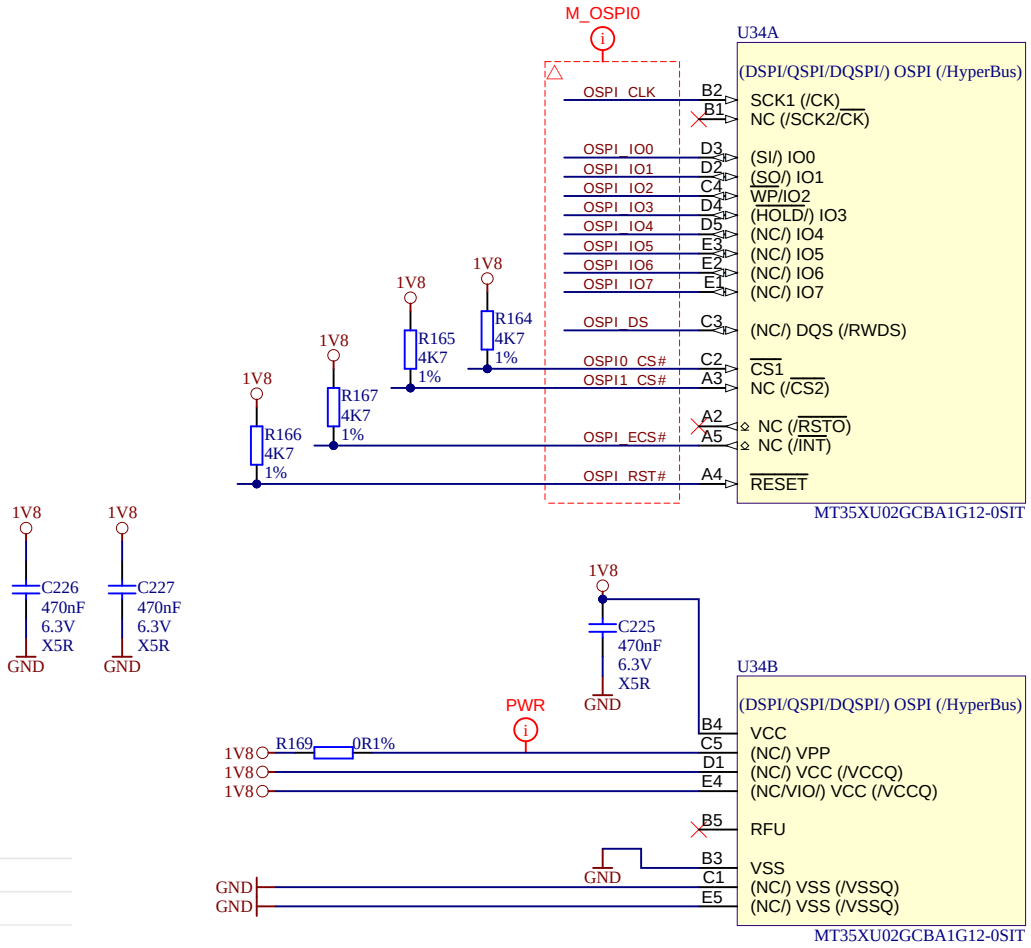


Title: V GTYP			
A4	Number: TE0955 EGBE32A		Rev. 02
Date: 2025-08-22	Copyright: Trenz Electronic GmbH		Page 14 of 28
Filename: V_GTYP.SchDoc			


V_VCC_CORE
C125 47µF 6.3V X5R
C126 4.7uF 6.3V X5R
C127 4.7uF 6.3V X5R
GND
V_VCCAUX
C133 4.7uF 6.3V X5R
C134 4.7uF 6.3V X5R
U1K
XCVE2302-1LSESFVA784
VCC_PMC 0.7V
VCC_PMC 0.8V
VCC_PMC 0.88V
VCCAUX_PMC 1.5V
VCC_PSLP
VCC_PSFP
VCCAUX 1.5V
VCC_SOC 0.8V 0.88V
VCC_IO 0.8V 0.88V
VCC_RAM 0.8V 0.88V
1.8V VCC_FUSE
RSVD for VM1102:
VCCINT_SENSE
GND_SENSE
V_VCC_SOC
C135 330µF 2.5V Polymer
C136 100µF 6.3V Polymer
C143 47µF 6.3V Polymer
C144 4.7uF 6.3V X5R
C145 4.7uF 6.3V X5R
C156 47µF 6.3V Polymer
C157 4.7uF 6.3V X5R
C158 4.7uF 6.3V X5R
C166 47µF 6.3V Polymer
C149 47µF 6.3V X5R
C150 4.7uF 6.3V X5R
C151 4.7uF 6.3V X5R
C163 47µF 6.3V X5R
C164 4.7uF 6.3V X5R
C165 4.7uF 6.3V X5R
C167 4.7uF 6.3V X5R
C168 4.7uF 6.3V X5R
C169 4.7uF 6.3V X5R
C170 47µF 6.3V Polymer
V_FUSE
TP20
R122 1% 1K
R168 0R
1V8
V_VCCINT_S_R_P
V_VCCINT_S_R_N
0.7V
0.725V not supported
0.8V
0.88V
U1L
XCVE2302-1LSESFVA784
VCCINT
C130 4.7uF 6.3V X5R
C131 4.7uF 6.3V X5R
C132 4.7uF 6.3V X5R
C140 4.7uF 6.3V X5R
C141 4.7uF 6.3V X5R
C142 4.7uF 6.3V X5R
C152 470nF 6.3V X5R
C153 470nF 6.3V X5R
C154 470nF 6.3V X5R
C155 470nF 6.3V X5R
C159 470nF 6.3V X5R
C160 470nF 6.3V X5R
C161 470nF 6.3V X5R
C162 470nF 6.3V X5R
C128 330µF 4V X5R
C129 330µF 4V X5R
C137 100µF 4V X6S
C138 100µF 4V X6S
C139 100µF 4V X6S
C146 47µF 6.3V X5R
C147 47µF 6.3V X5R
C148 47µF 6.3V X5R
V_MOD1
R123 10K 1%
20:1
R125 499R 1%
R124 100R 1%
R126 499R 1%
C171 1nF 50V C0G, NP0
V_MON_MOD1_P
V_MON_MOD1_N
0.570V @V_MOD1=12V
L8 MPZ0603S121HT000 1A
L9 MPZ0603S121HT000 1A
C172 470nF 6.3V X5R
U1J
VCCAUX_SMON
VREFP_500
VREFN_500
VP_500
VN_500
GND_SMON
Internal VREF used
SS_V_GND
V_GND.SchDoc
trenz electronic
Title: V Power
A4
Number: TE0955 EGBE32A
Rev. 02
Date: 2025-08-22
Copyright: Trenz Electronic GmbH
Page 15 of 28
Filename: V_Power.SchDoc



Title: V GND			
A4	Number: TE0955 EGBE32A		Rev. 02
Date: 2025-08-22	Copyright: Trenz Electronic GmbH		Page 16 of 28
Filename: V_GND.SchDoc			



OSPI

	PMC Bank 0
MIO 0	OSPI: ospi_clk
MIO 1	OSPI: ospi_io[0]
MIO 2	OSPI: ospi_io[1]
MIO 3	OSPI: ospi_io[2]
MIO 4	OSPI: ospi_io[3]
MIO 5	OSPI: ospi_io[4]
MIO 6	OSPI: ospi_ds
MIO 7	OSPI: ospi_io[5]
MIO 8	OSPI: ospi_io[6]
MIO 9	OSPI: ospi_io[7]
MIO 10	OSPI: ospi0_cs_b
MIO 11	OSPI: ospi1_cs_b
MIO 12	GPIO 0: gpio_0_pin[12]

for dedicated OSPI_RST# or optional OSPI_ECS#, see R121, R198, R199

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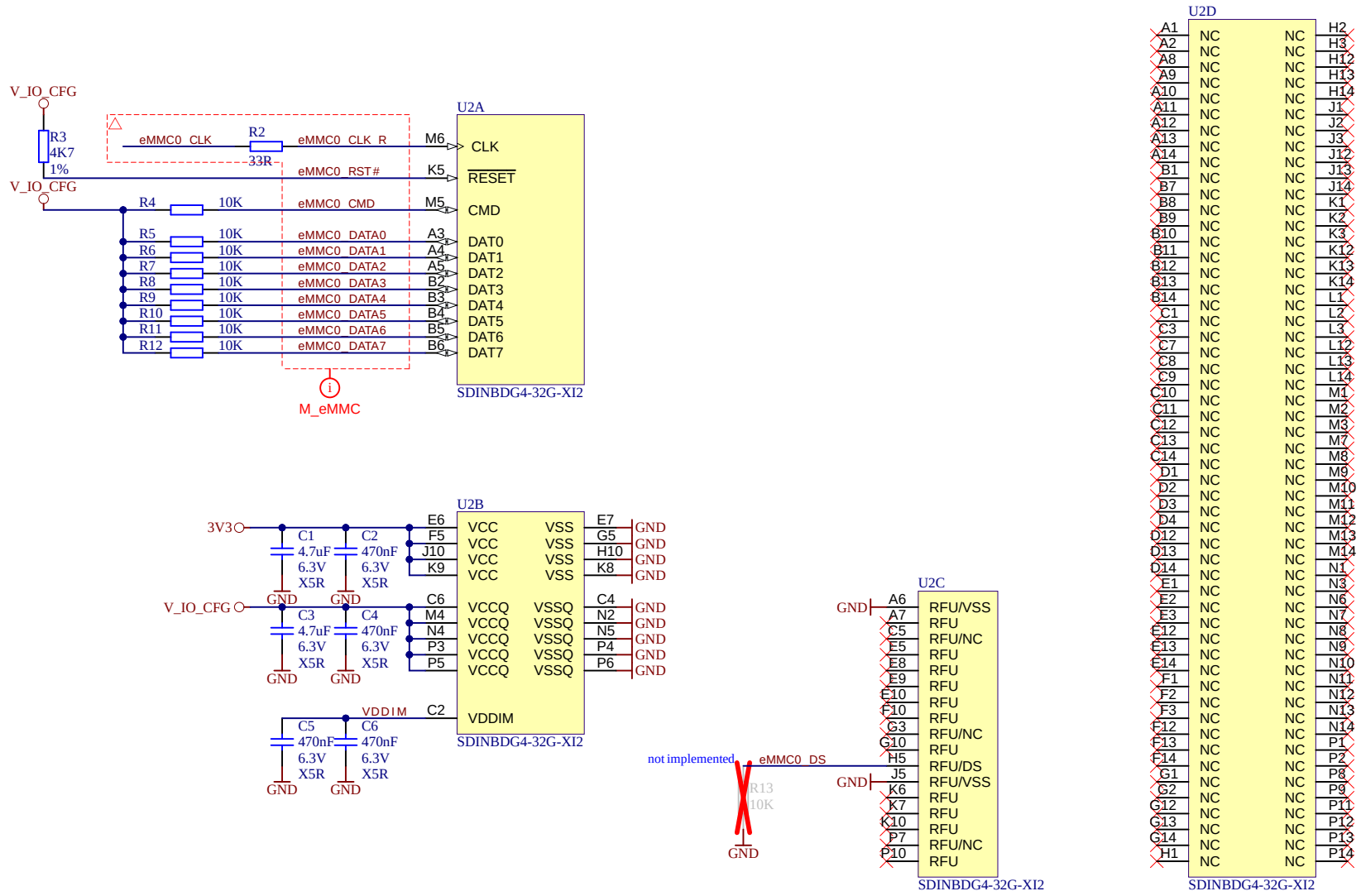
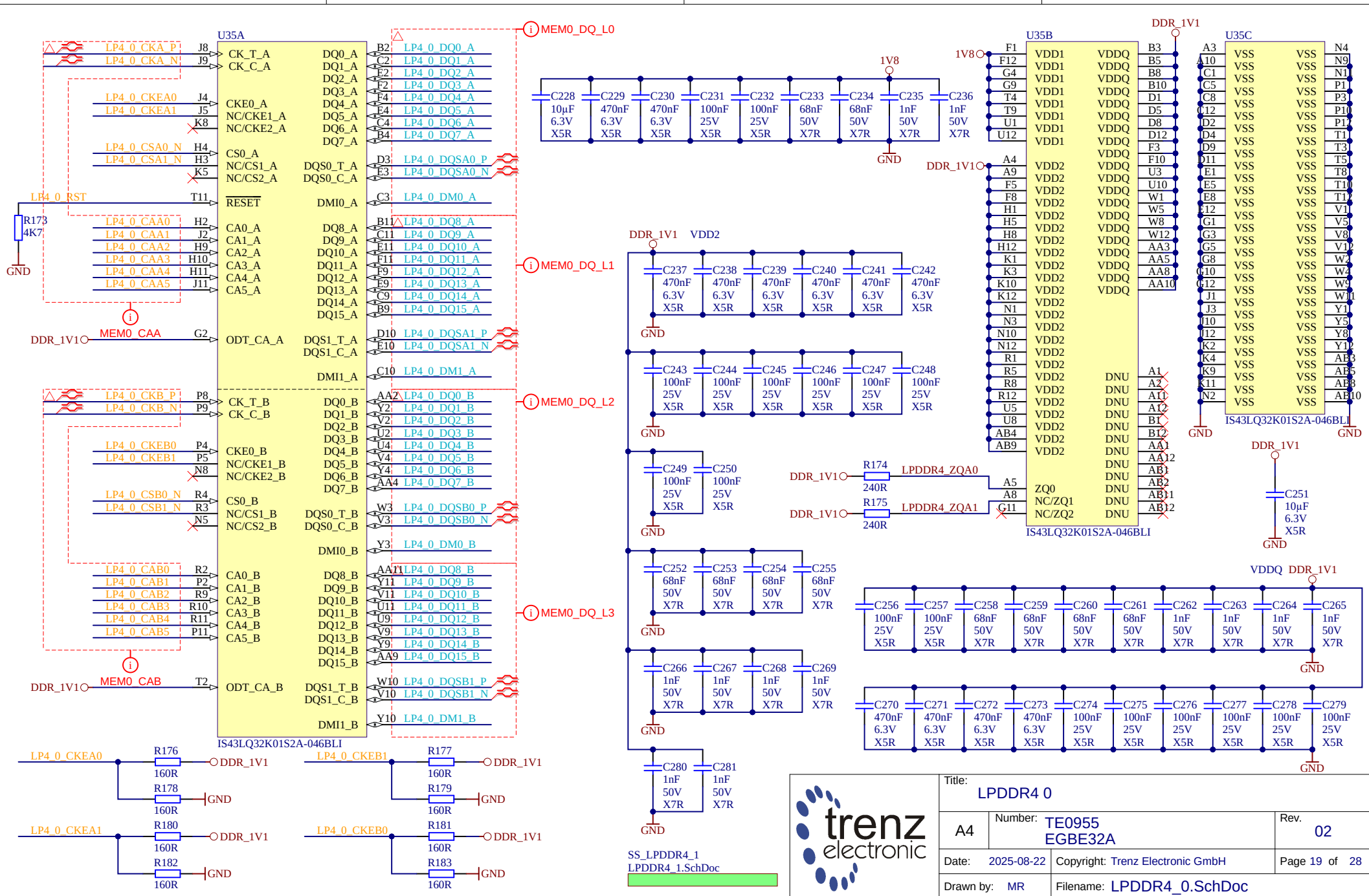
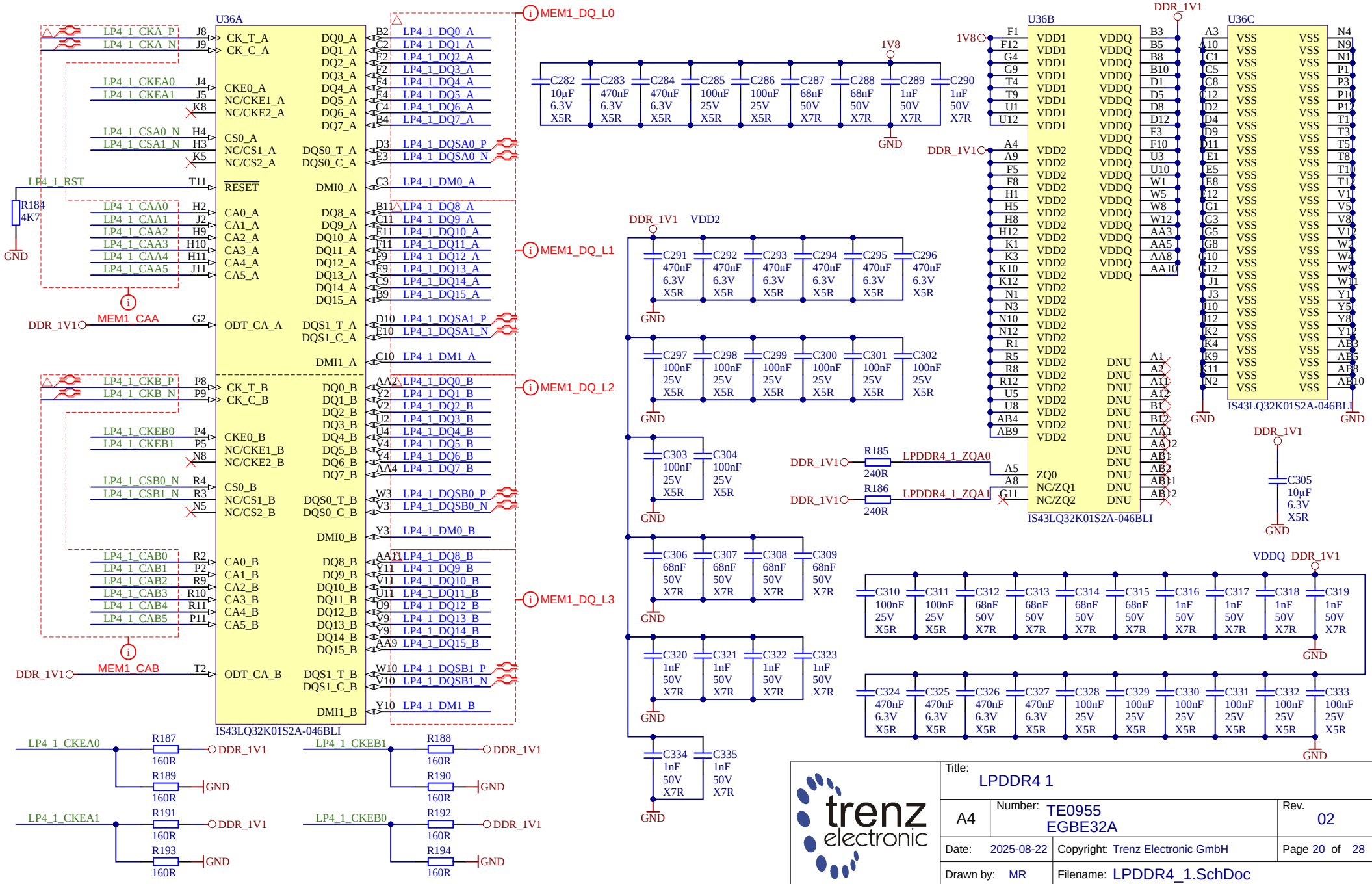
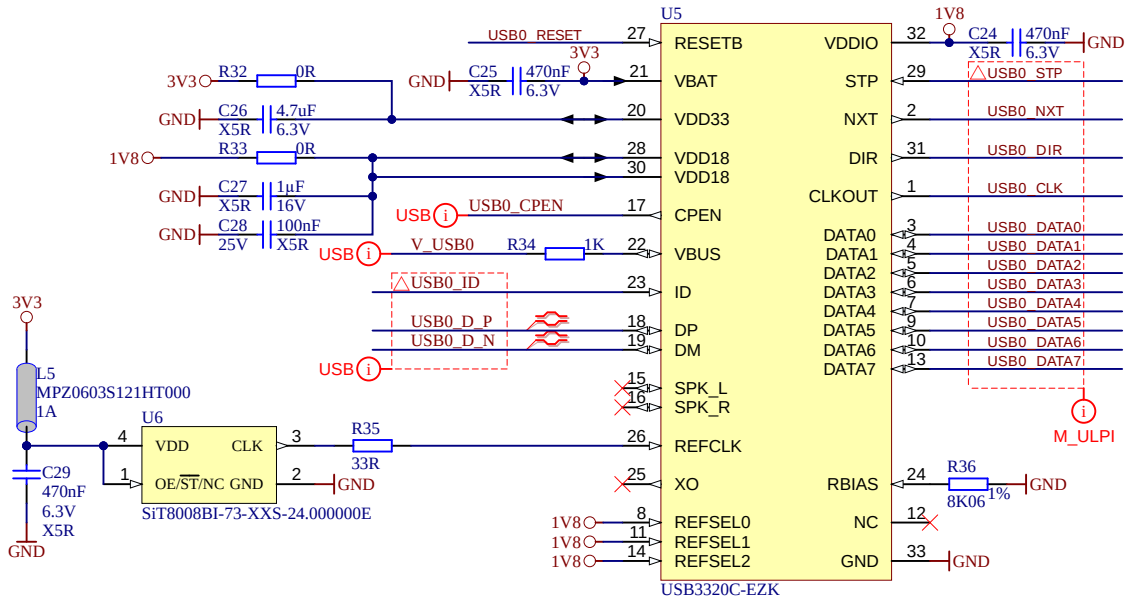
Title: OSPI		
A4	Number: TE0955 EGBE32A	Rev. 02
Date: 2025-08-22	Copyright: Trenz Electronic GmbH	Page 17 of 28
Filename: OSPI.SchDoc		



Title: eMMC		
A4	Number: TE0955 EGBE32A	Rev. 02
Date: 19.01.2026	Copyright: Trenz Electronic GmbH	Page 18 of 28
Filename: eMMC.SchDoc		

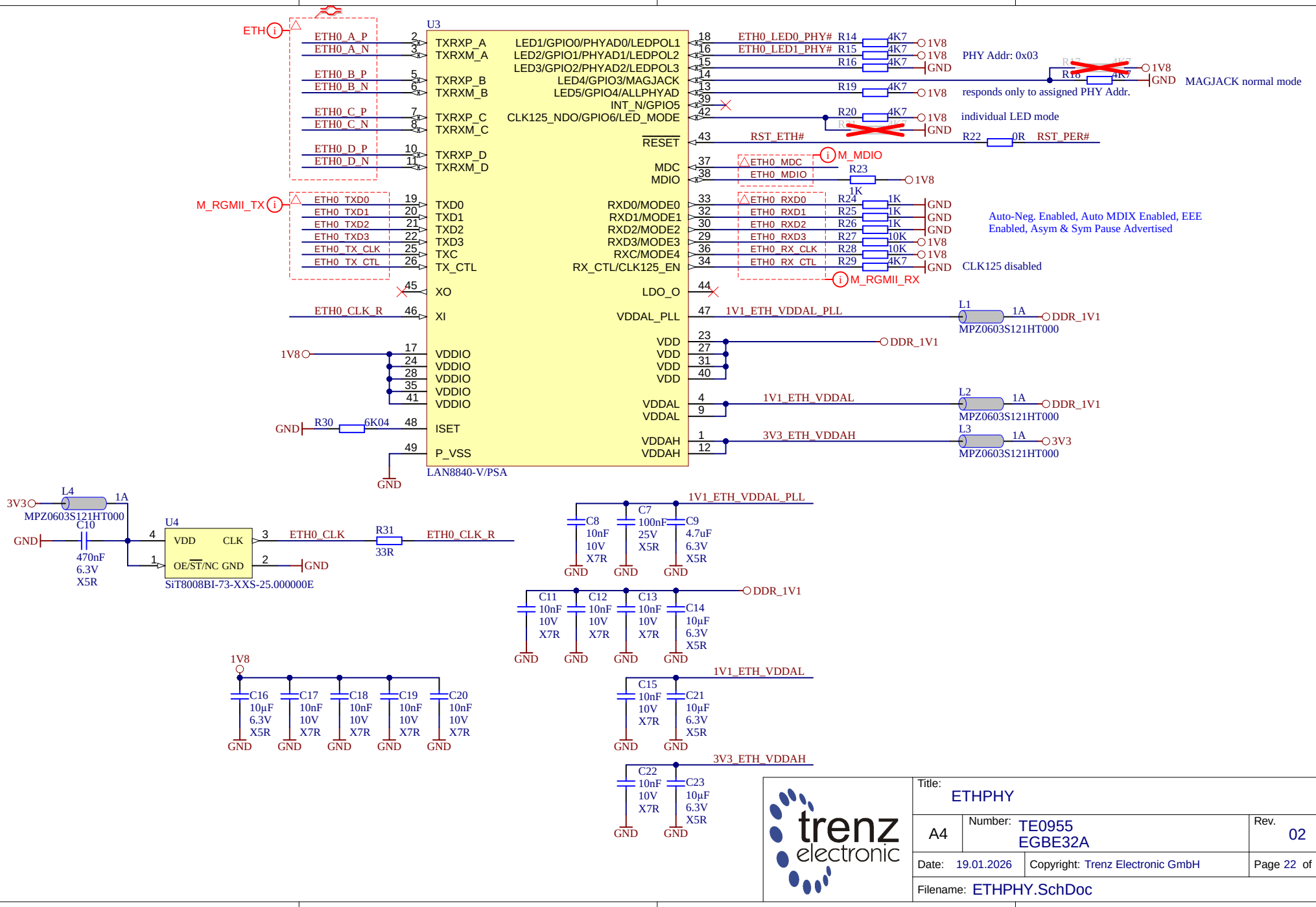

U35A
IS43LQ32K01S2A-046BLI
MEM0_DQ_L0
MEM0_DQ_L1
MEM0_DQ_L2
MEM0_DQ_L3
MEM0_CAA
MEM0_CAB
LP4_0_RST
R173
4K7
GND
DDR_1V1
U35B
U35C
VDD2
VDDQ
1V8
C228 10µF 6.3V X5R
C229 470nF 6.3V X5R
C230 470nF 6.3V X5R
C231 100nF 25V X5R
C232 100nF 25V X5R
C233 68nF 50V X7R
C234 68nF 50V X7R
C235 1nF 50V X7R
C236 1nF 50V X7R
C237 470nF 6.3V X5R
C238 470nF 6.3V X5R
C239 470nF 6.3V X5R
C240 470nF 6.3V X5R
C241 470nF 6.3V X5R
C242 470nF 6.3V X5R
C243 100nF 25V X5R
C244 100nF 25V X5R
C245 100nF 25V X5R
C246 100nF 25V X5R
C247 100nF 25V X5R
C248 100nF 25V X5R
C249 100nF 25V X5R
C250 100nF 25V X5R
C251 10µF 6.3V X5R
C252 68nF 50V X7R
C253 68nF 50V X7R
C254 68nF 50V X7R
C255 68nF 50V X7R
C256 100nF 25V X5R
C257 100nF 25V X5R
C258 68nF 50V X7R
C259 68nF 50V X7R
C260 68nF 50V X7R
C261 68nF 50V X7R
C262 1nF 50V X7R
C263 1nF 50V X7R
C264 1nF 50V X7R
C265 1nF 50V X7R
C266 1nF 50V X7R
C267 1nF 50V X7R
C268 1nF 50V X7R
C269 1nF 50V X7R
C270 470nF 6.3V X5R
C271 470nF 6.3V X5R
C272 470nF 6.3V X5R
C273 470nF 6.3V X5R
C274 100nF 25V X5R
C275 100nF 25V X5R
C276 100nF 25V X5R
C277 100nF 25V X5R
C278 100nF 25V X5R
C279 100nF 25V X5R
C280 1nF 50V X7R
C281 1nF 50V X7R
R174 240R
R175 240R
LPDDR4_ZQA0
LPDDR4_ZQA1
R176 160R
R177 160R
R178 160R
R179 160R
R180 160R
R181 160R
R182 160R
R183 160R
SS_LPDDR4_1
LPDDR4_1.SchDoc
trenz electronic
Title: LPDDR4 0
A4
Number: TE0955 EGBE32A
Rev. 02
Date: 2025-08-22
Copyright: Trenz Electronic GmbH
Page 19 of 28
Drawn by: MR
Filename: LPDDR4_0.SchDoc


U36A
U36B
U36C
IS43LQ32K01S2A-046BLI
MEM1_DQ_L0
MEM1_DQ_L1
MEM1_DQ_L2
MEM1_DQ_L3
MEM1_CAA
MEM1_CAB
LP4_1_RST
R184 4K7
DDR_1V1
VDD2
1V8
GND
LPDDR4_1_ZQA0
LPDDR4_1_ZQA1
R185 240R
R186 240R
R187 160R
R188 160R
R189 160R
R190 160R
R191 160R
R192 160R
R193 160R
R194 160R
C305 10µF 6.3V X5R
VDDQ
Title: LPDDR4 1
A4
Number: TE0955 EGBE32A
Rev. 02
Date: 2025-08-22
Copyright: Trenz Electronic GmbH
Page 20 of 28
Drawn by: MR
Filename: LPDDR4_1.SchDoc
trenz electronic



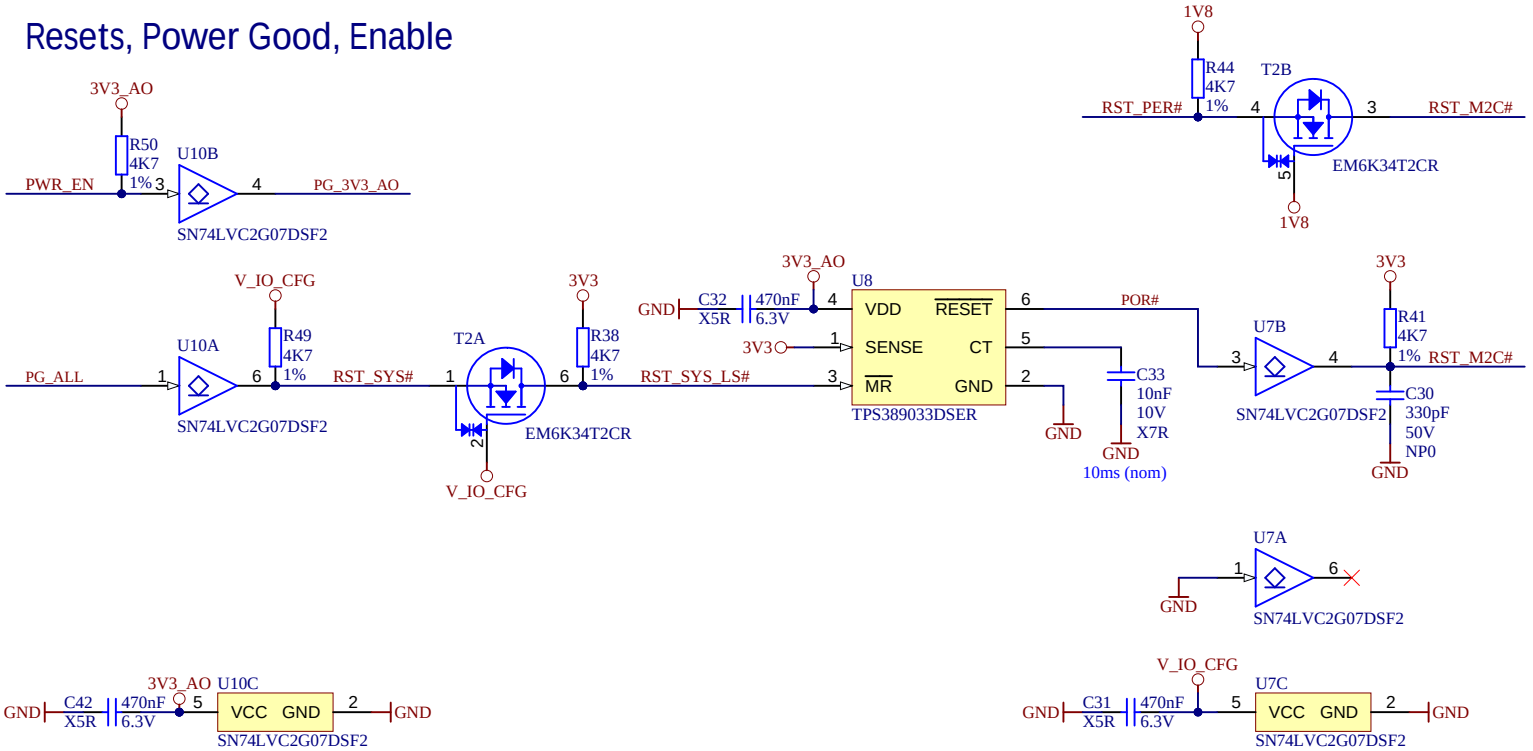
	R32	R33	C26	C28
USB3320	0R	0R	4.7 µF	0.1 µF
USB3340	DNP	DNP	1 µF	1 µF

trenz electronic	Title: USBPHY		
	A4	Number: TE0955 EGBE32A	Rev. 02
	Date: 19.01.2026	Copyright: Trenz Electronic GmbH	Page 21 of 28
	Filename: USBPHY.SchDoc		

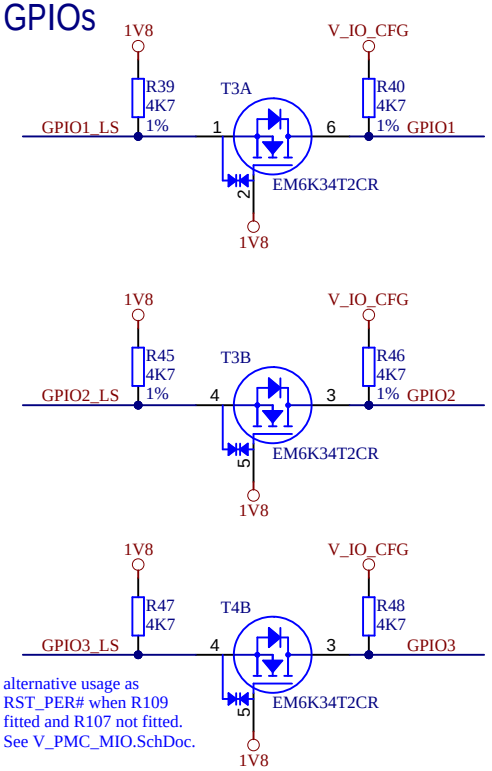


Title: ETHPHY			
A4	Number: TE0955 EGBE32A		Rev. 02
Date: 19.01.2026	Copyright: Trenz Electronic GmbH		Page 22 of 28
Filename: ETHPHY.SchDoc			

Resets, Power Good, Enable

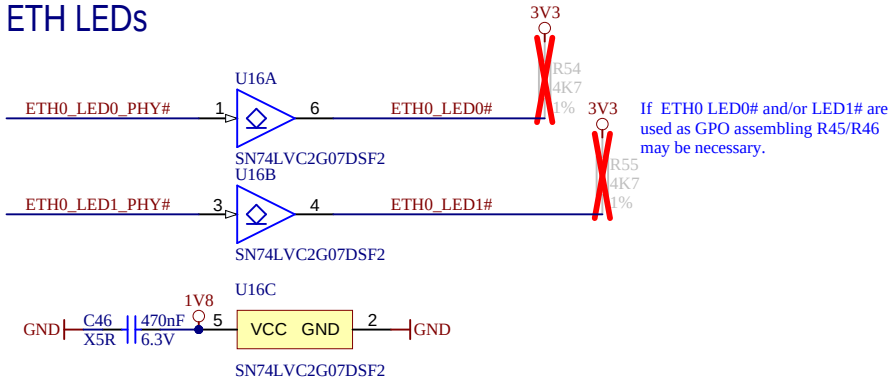


GPIOs



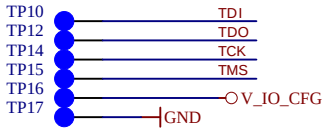
alternative usage as RST_PER# when R109 fitted and R107 not fitted. See V_PMC_MIO.SchDoc.

ETH LEDs



If ETH0 LED0# and/or LED1# are used as GPO assembling R45/R46 may be necessary.

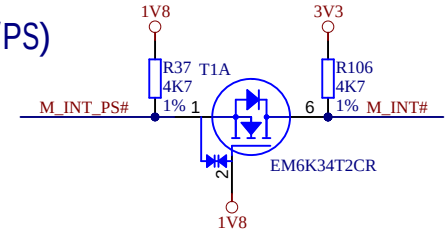
JTAG Testpoints

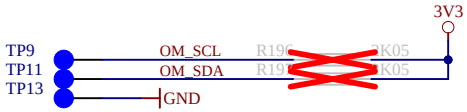


trenz electronic

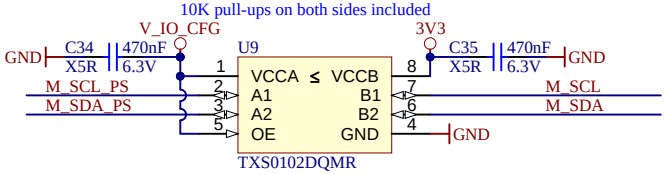
Title: MISC 0		
A4	Number: TE0955 EGBE32A	Rev. 02
Date: 19.01.2026	Copyright: Trenz Electronic GmbH	Page 23 of 28
Filename: MISC_0.SchDoc		

I2C (PMC/PS)

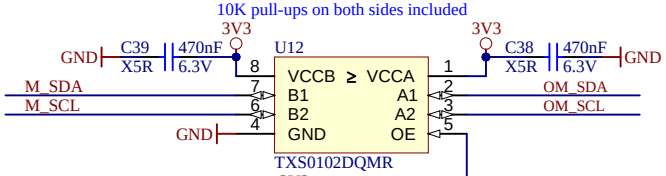




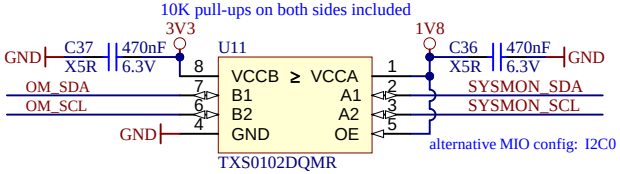
10K pull-ups on both sides included

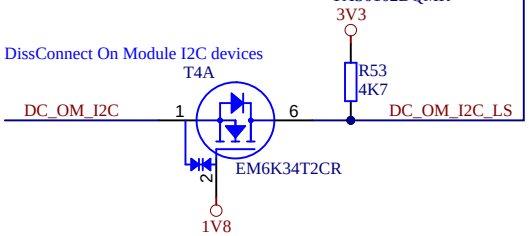


10K pull-ups on both sides included

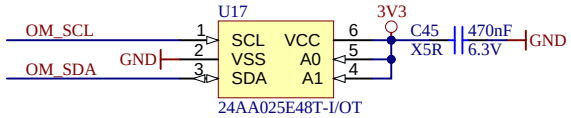


10K pull-ups on both sides included

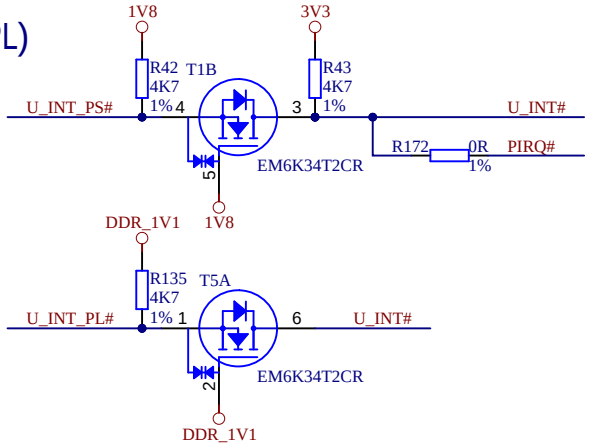


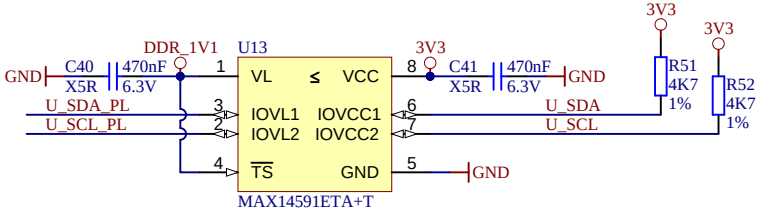


I2C Address: 0x53

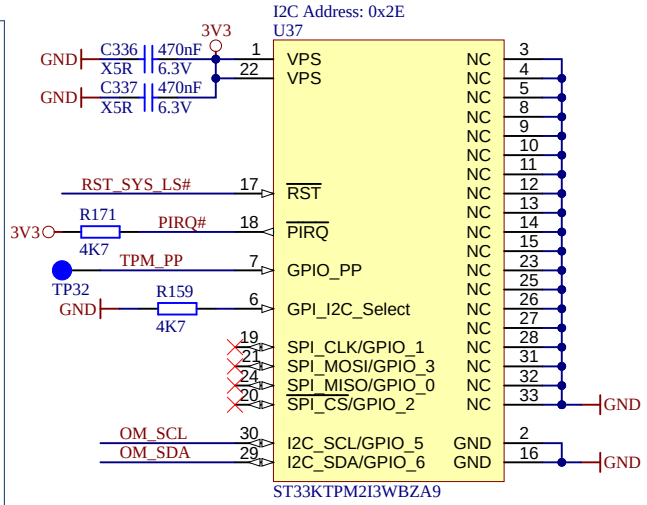


User I2C (PL)

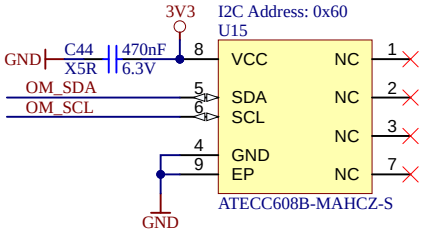




I2C Address: 0x2E

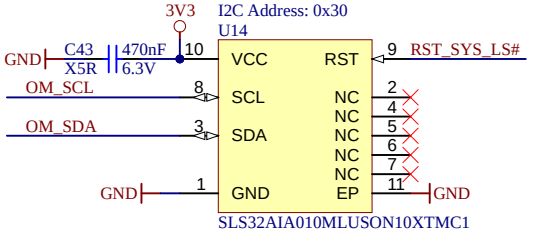


I2C Address: 0x60



U14, U15 are only for backward compatibility and may be removed in future.

I2C Address: 0x30



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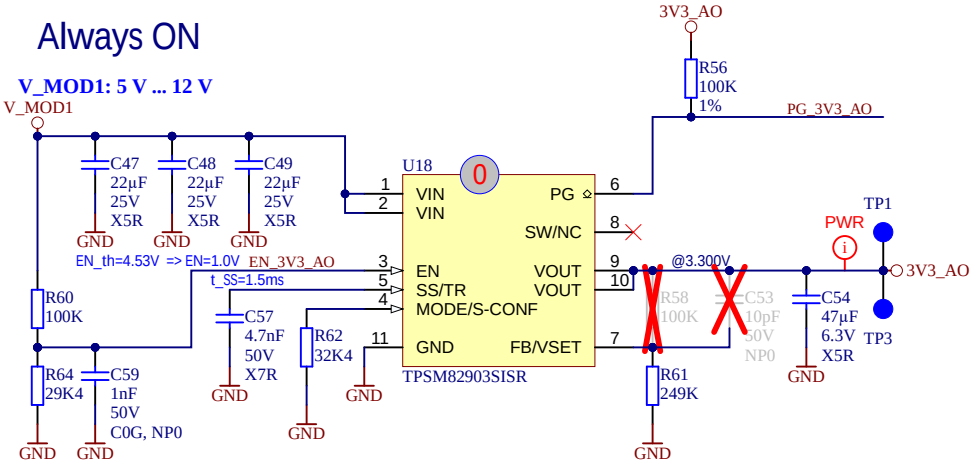
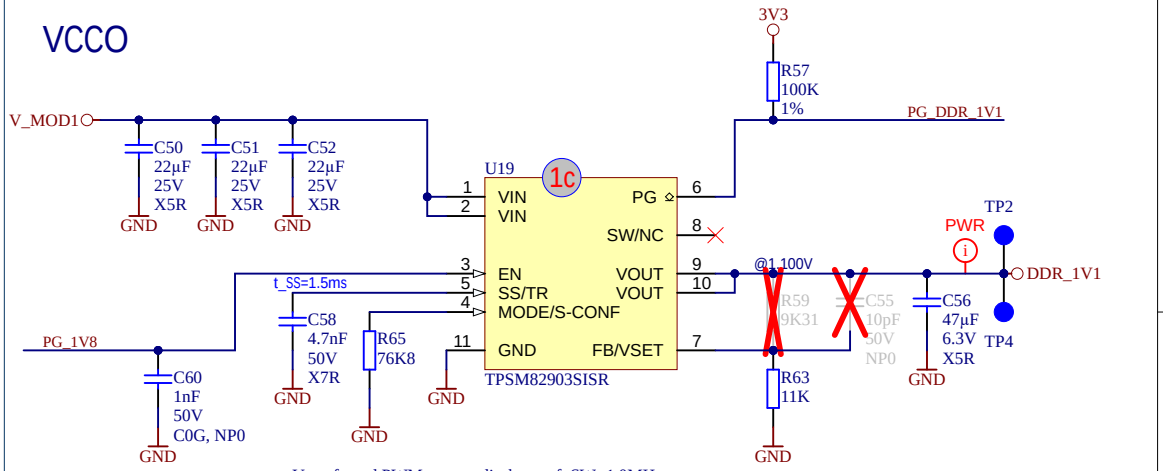
Always ON

V_MOD1: 5 V ... 12 V



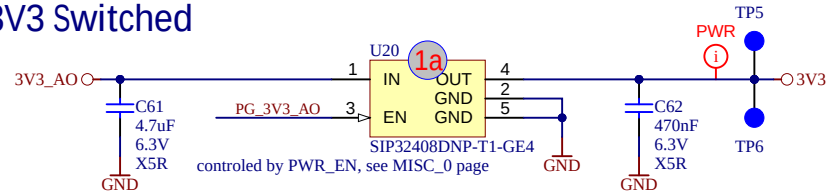
Vset; forced PWM; output discharge; f_SW=2.5MHz

VCCO

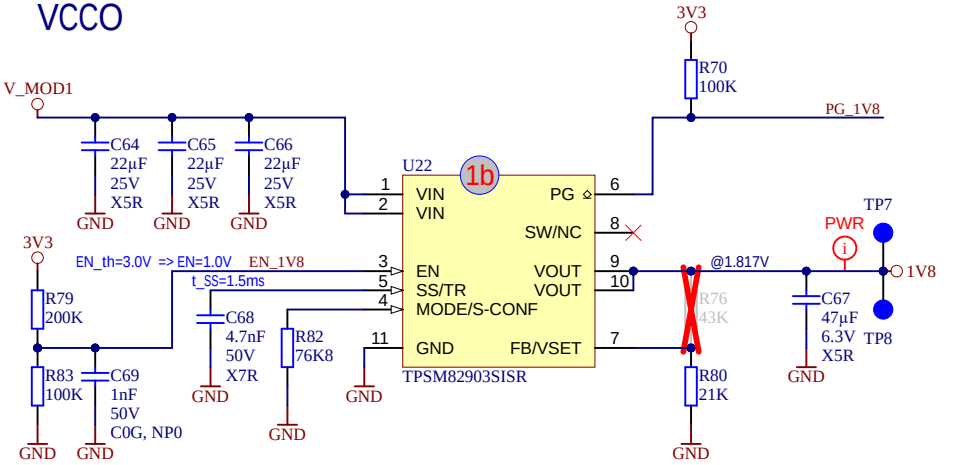


Vset; forced PWM; output discharge; f_SW=1.0MHz

3V3 Switched

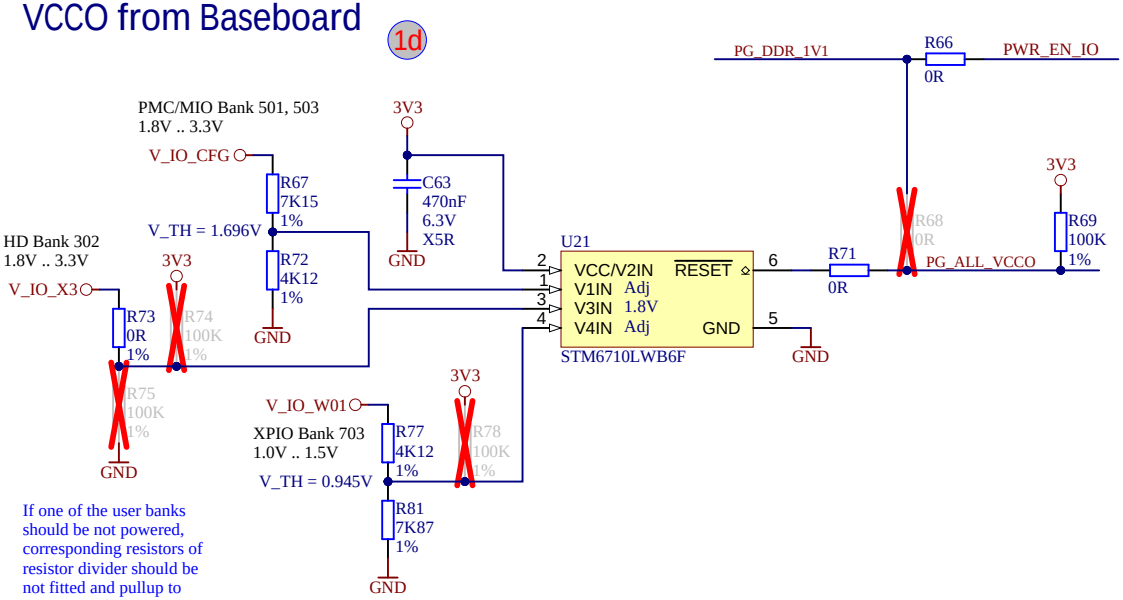


VCCO



Vset; forced PWM; output discharge; f_SW=1.0MHz

VCCO from Baseboard



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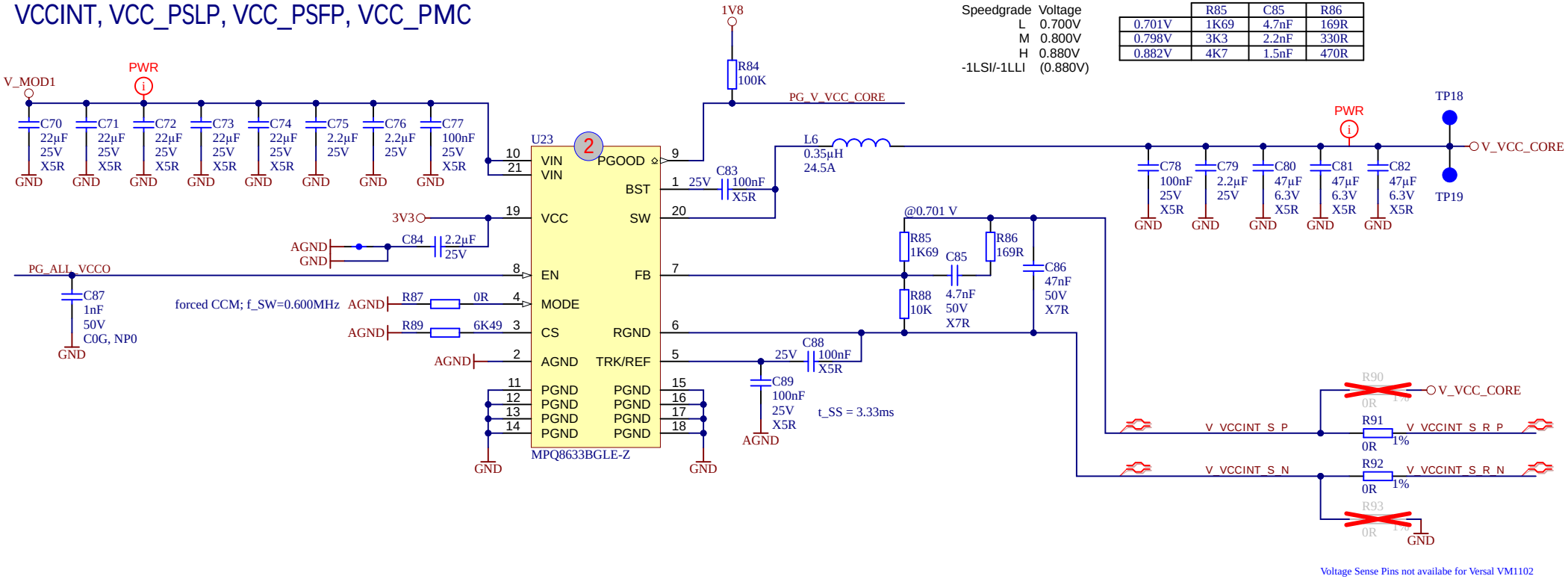
VCCINT, VCC_PSLP, VCC_PMC



Speedgrade	Voltage
L	0.700V
M	0.800V
H	0.880V
-1LSI/-1LLI	(0.880V)

	R85	C85	R86
0.701V	1K69	4.7nF	169R
0.798V	3K3	2.2nF	330R
0.882V	4K7	1.5nF	470R

Voltage Sense Pins not availabe for Versal VM1102

trenz electronic	Title: Power 2		
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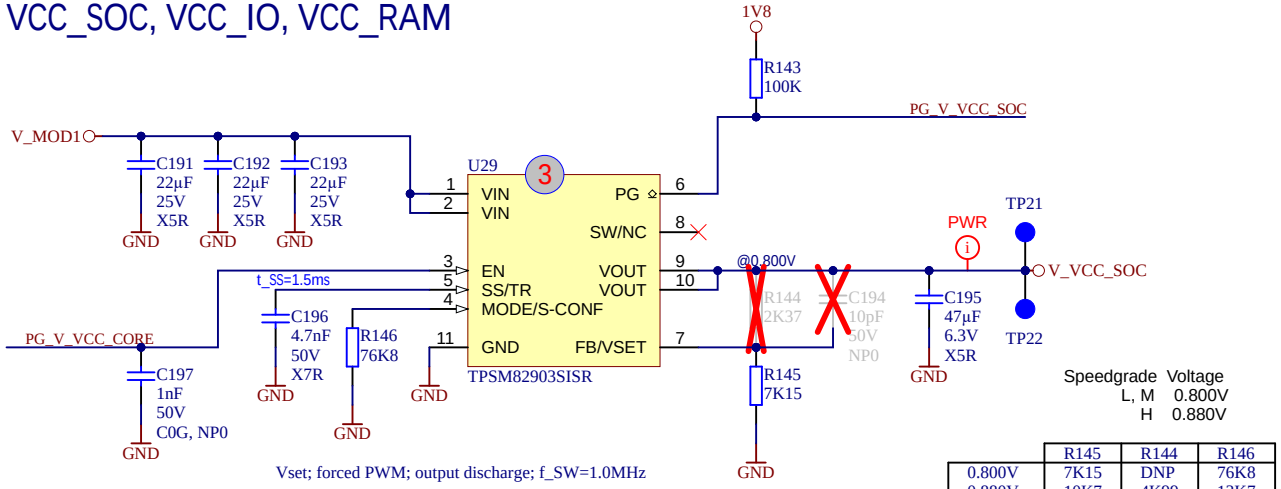
VCC_SOC, VCC_IO, VCC_RAM

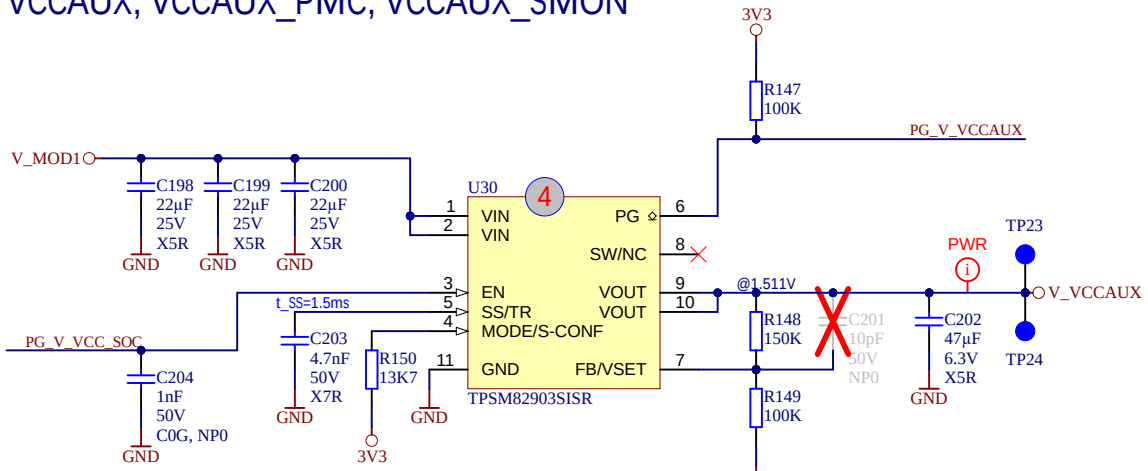


Vset; forced PWM; output discharge; f_SW=1.0MHz

Speedgrade Voltage
L, M 0.800V
H 0.880V

	R145	R144	R146
0.800V	7K15	DNP	76K8
0.880V	10K7	4K99	13K7

VCCAUX, VCCAUX_PMC, VCCAUX_SMON



ext. FB; forced PWM; output discharge; f_SW=1.0MHz

VE2002, VE2102 have no transceiver

Title: Power 3			
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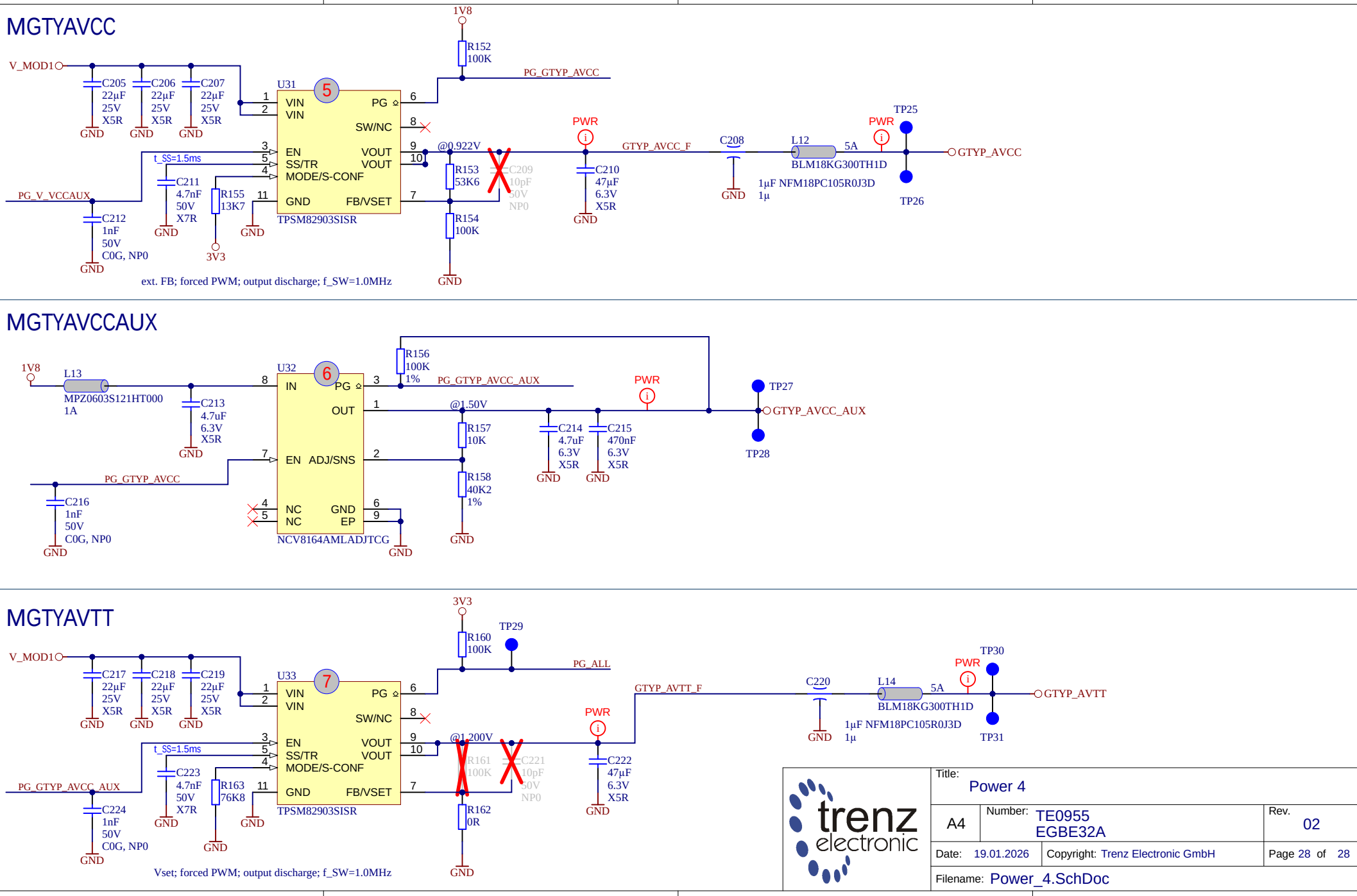
MGTYAVCC
V_MOD1
C205 22µF 25V X5R
C206 22µF 25V X5R
C207 22µF 25V X5R
GND
U31
TPSM82903SISR
VIN
VIN
EN
SS/TR
MODE/S-CONF
GND
PG
SW/NC
VOUT
VOUT
FB/VSET
1V8
R152 100K
PG_GTYP_AVCC
@0.922V
R153 53K6
C209 10pF 50V NP0
R154 100K
t_SS=1.5ms
C211 4.7nF 50V X7R
R155 13K7
3V3
PG_V_VCCAUX
C212 1nF 50V C0G, NP0
ext. FB; forced PWM; output discharge; f_SW=1.0MHz
PWR
GTYP_AVCC_F
C210 47µF 6.3V X5R
C208
1µF NFM18PC105R0J3D 1µ
L12 BLM18KG300TH1D 5A
TP25
TP26
GTYP_AVCC
MGTYAVCCAUX
1V8
L13 MPZ0603S121HT000 1A
C213 4.7uF 6.3V X5R
U32
NCV8164AMLADJTCG
IN
PG
OUT
EN
ADJ/SNS
NC
NC
GND
EP
R156 100K 1%
PG_GTYP_AVCC_AUX
@1.50V
R157 10K
R158 40K2 1%
C214 4.7uF 6.3V X5R
C215 470nF 6.3V X5R
TP27
TP28
GTYP_AVCC_AUX
PG_GTYP_AVCC
C216 1nF 50V C0G, NP0
MGTYAVTT
3V3
TP29
R160 100K
PG_ALL
U33
TPSM82903SISR
@1.200V
R161 100K
C221 10pF 50V NP0
R162 0R
C217 22µF 25V X5R
C218 22µF 25V X5R
C219 22µF 25V X5R
t_SS=1.5ms
C223 4.7nF 50V X7R
R163 76K8
PG_GTYP_AVCC_AUX
C224 1nF 50V C0G, NP0
Vset; forced PWM; output discharge; f_SW=1.0MHz
GTYP_AVTT_F
C222 47µF 6.3V X5R
C220
1µF NFM18PC105R0J3D 1µ
L14 BLM18KG300TH1D 5A
TP30
TP31
GTYP_AVTT
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A4
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