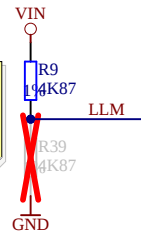
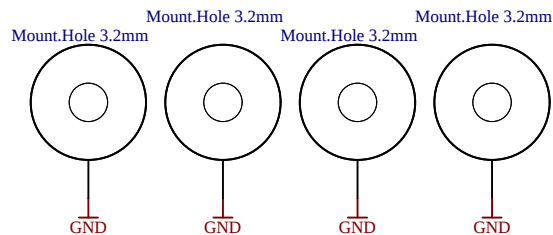
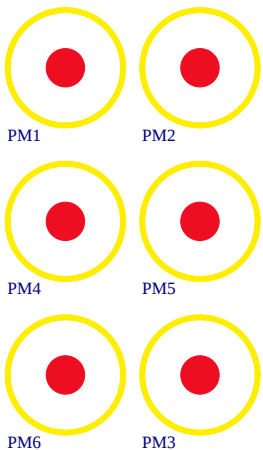
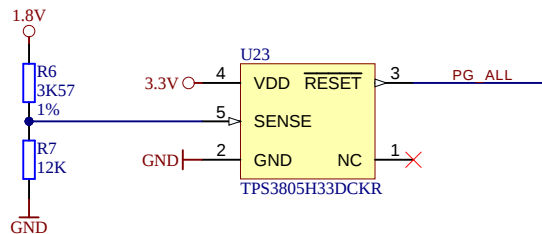
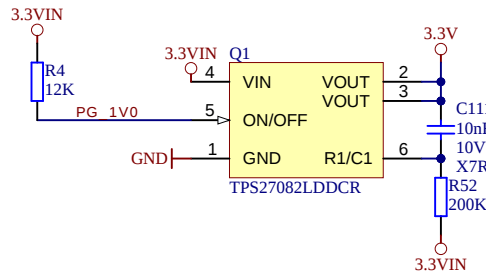


U_B2B_Connector	U_PLL
B2B_Connector.SchDoc	PLL.SchDoc
U_B14_B16	U_POWER
B14_B16.SchDoc	POWER.SchDoc
U_B15	
B15.SchDoc	
U_B34	
B34.SchDoc	
U_B35-RAM	
B35-RAM.SchDoc	
U_DDR3-MEM	
DDR3-MEM.SchDoc	
U_ETHERNET	
ETHERNET.SchDoc	
U_FPGA_CFG	
FPGA_CFG.SchDoc	
U_FPGA_PWR_MISC	
FPGA_PWR_MISC.SchDoc	

LOGO1
TE Logo PRINT Layer
LOGO PRINT

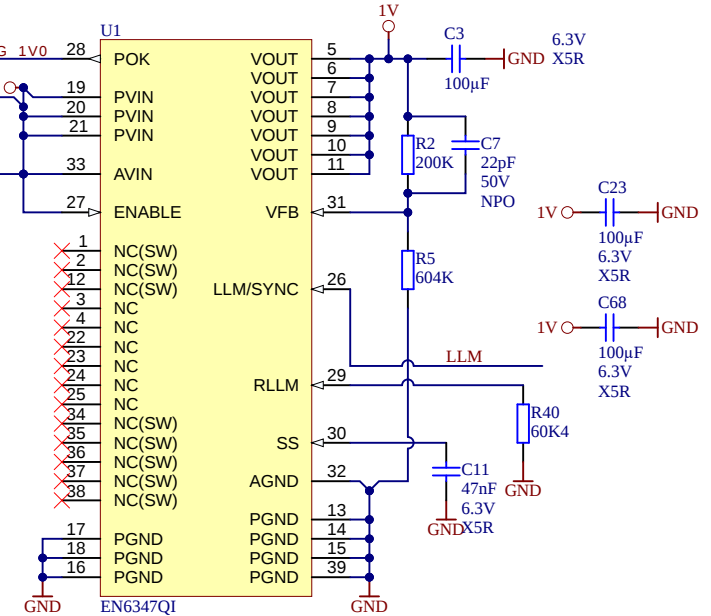
Select which resistor to mount with assembly variant!
Pullup switch DCDC to light load mode



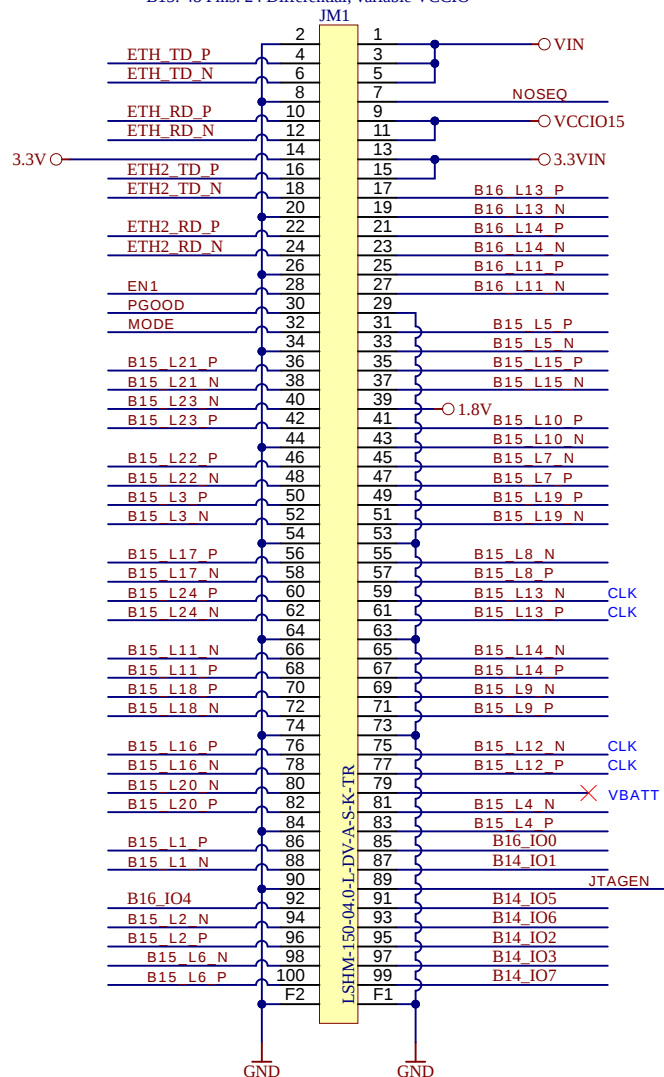
Top of Board



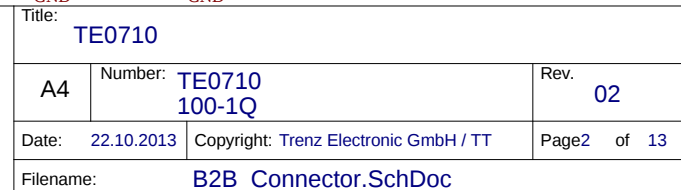
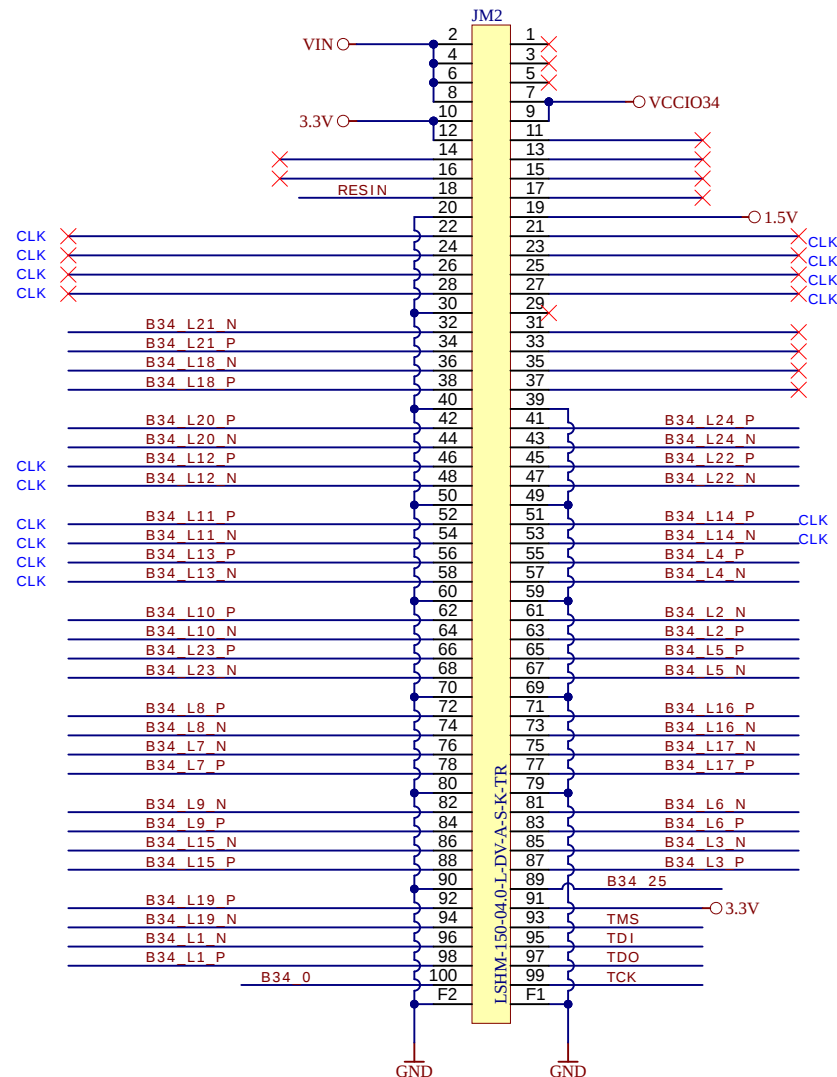
Title: TE0710		
A4	Number: TE0710 100-1Q	Rev. 02
Date: 2014-03-07	Copyright: Trenz Electronic GmbH / TT	Page1 of 13
Filename: TE0710.SchDoc		

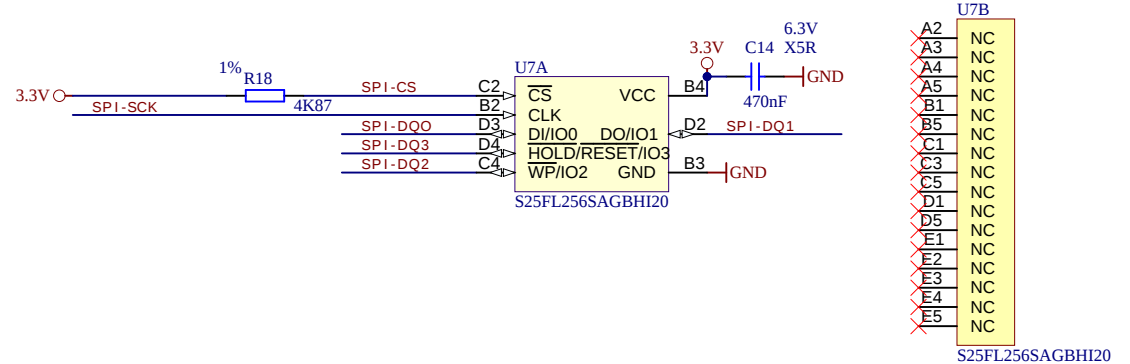
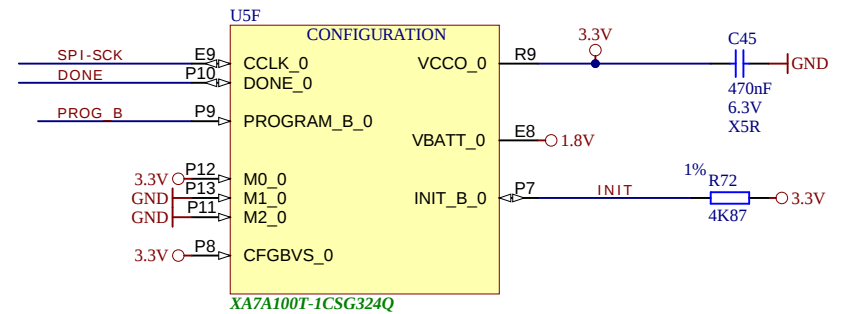
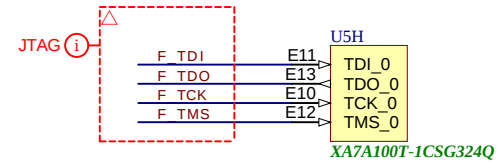


Assembly variant	100-1Q
Created by	TT
Modified by	VY
Modified at	2019-07-25
SVN Revision	8563

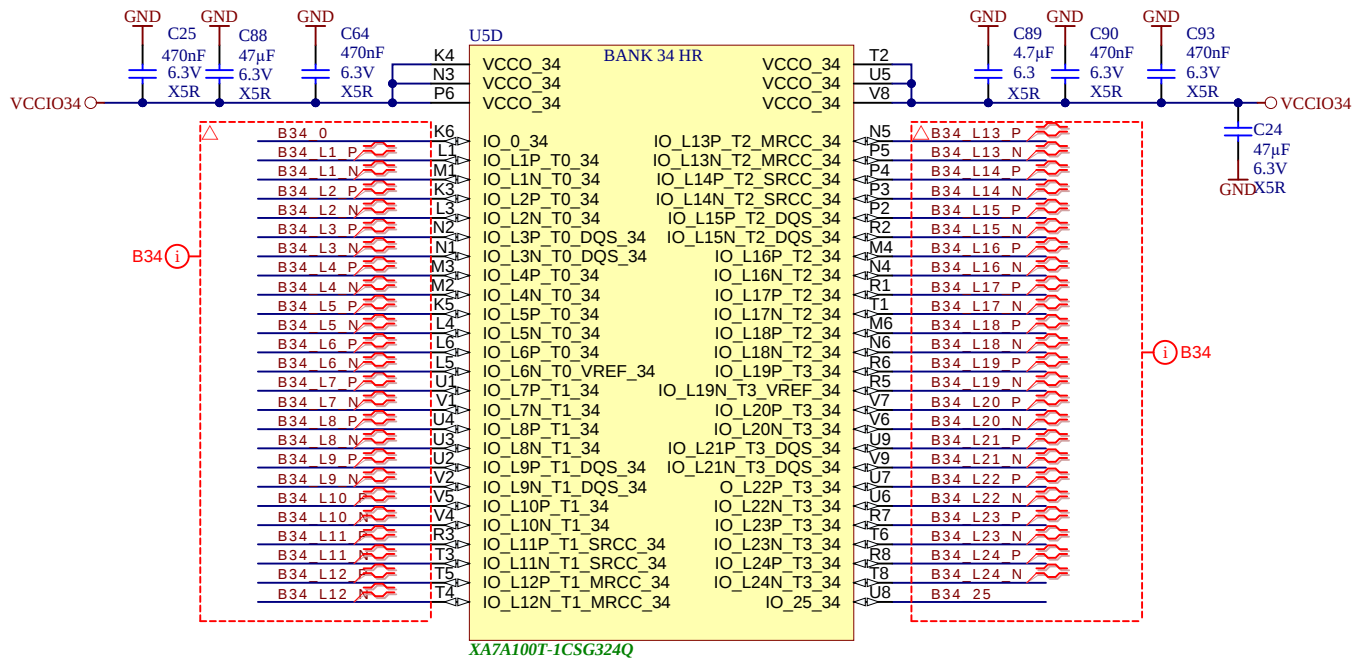


B34: 50 Pins: 24 Differential; variable VCCIO
JTAG or IO (4 pins): 3.3V Levels

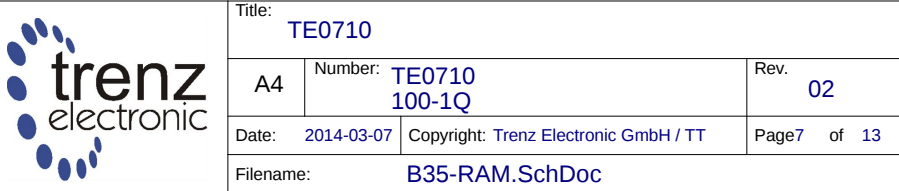


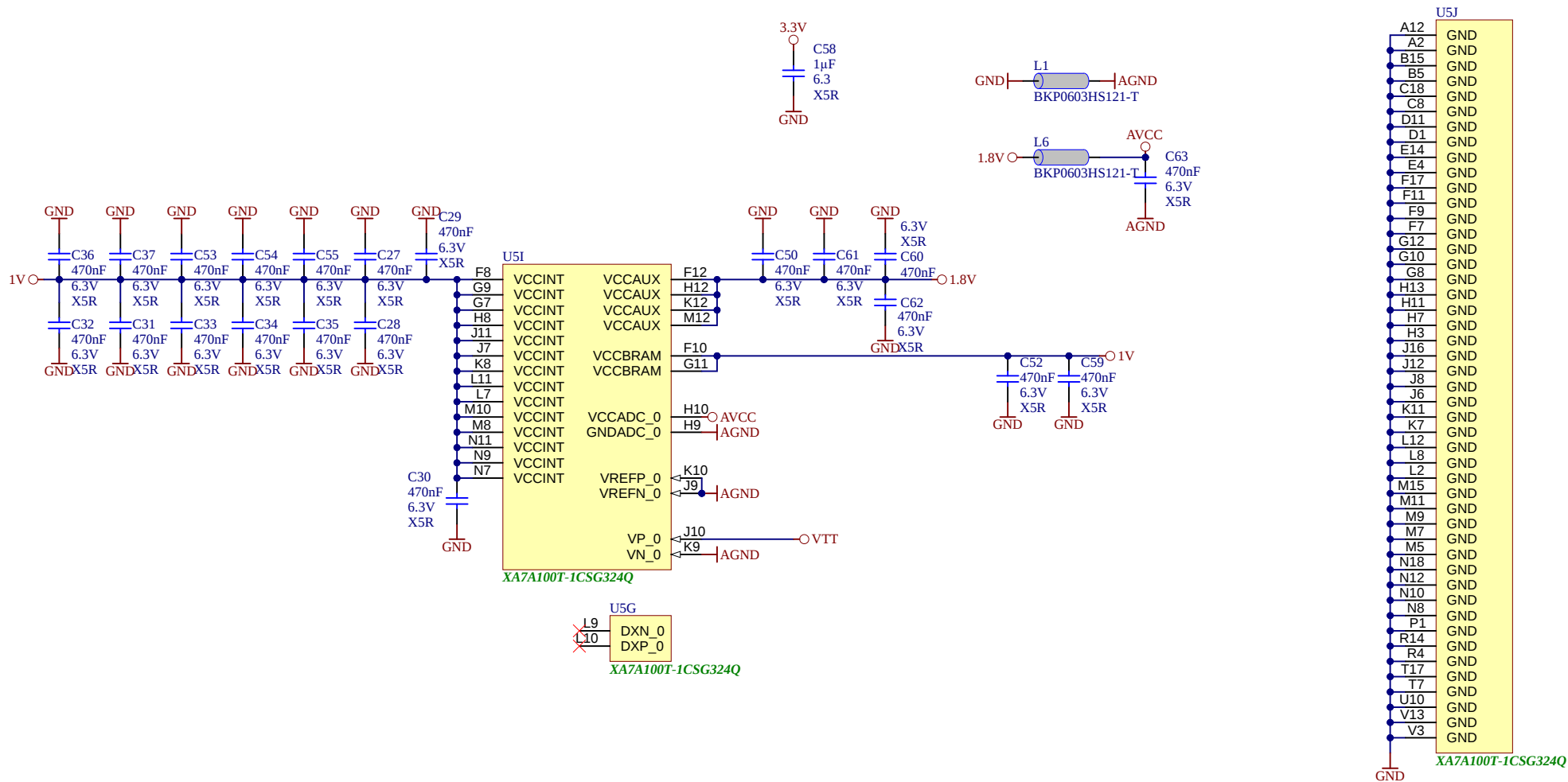


		Title: TE0710	
		Number: TE0710 100-1Q	Rev. 02
Date: 2014-03-07	Copyright: Trenz Electronic GmbH / TT		Page3 of 13
Filename: FPGA_CFG.SchDoc			



	Title: TE0710		
	A4	Number: TE0710 100-1Q	Rev. 02
	Date: 2014-03-07	Copyright: Trenz Electronic GmbH / TT	Page6 of 13
	Filename: B34.SchDoc		





		Title: TE0710	
		A4	Number: TE0710 100-1Q
Date: 2014-03-07	Copyright: Trenz Electronic GmbH / TT		Rev. 02
Filename: FPGA_PWR_MISC.SchDoc		Page 8 of 13	

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A

A

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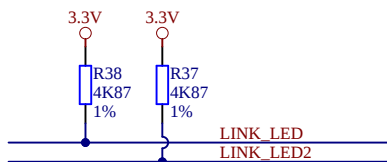
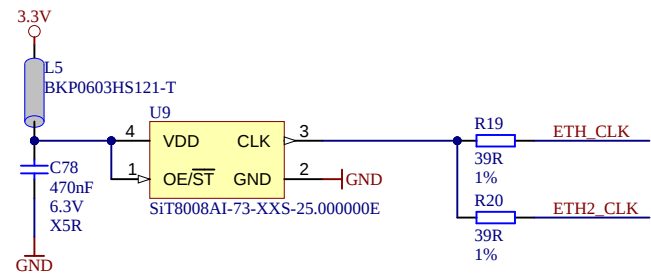
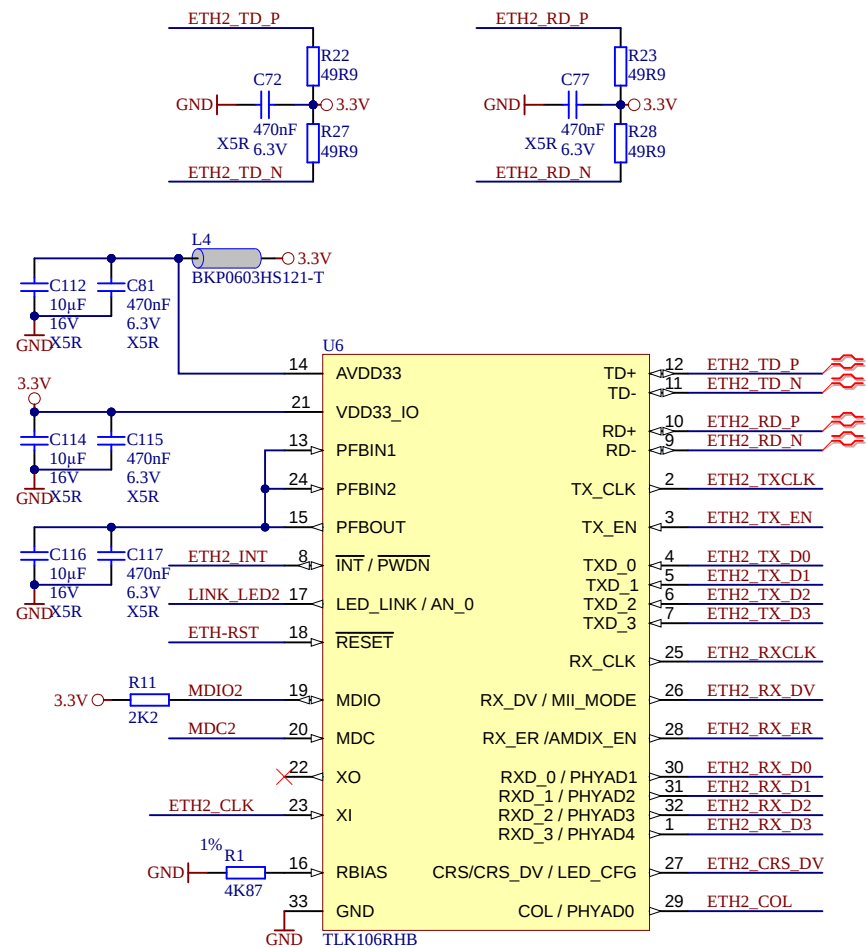
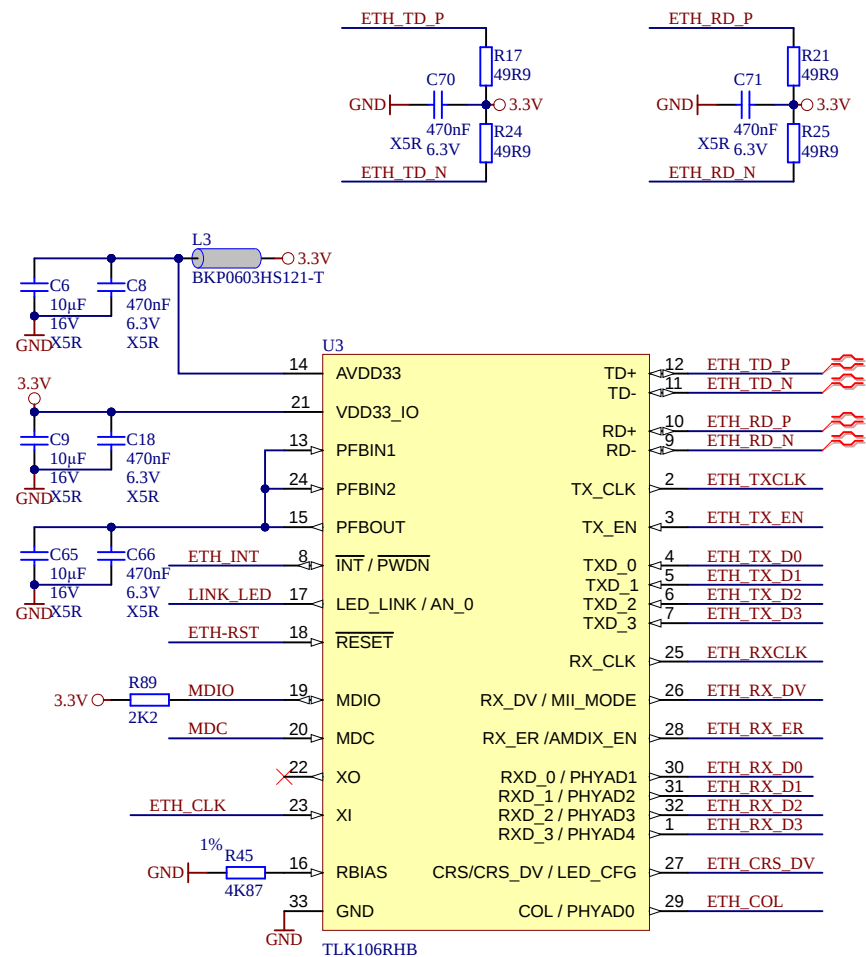
B

C

C

D

D



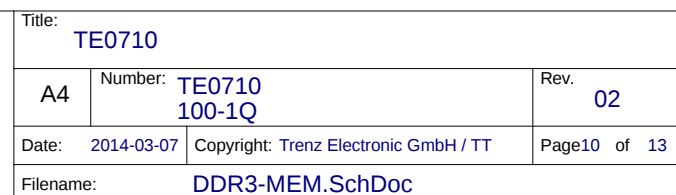
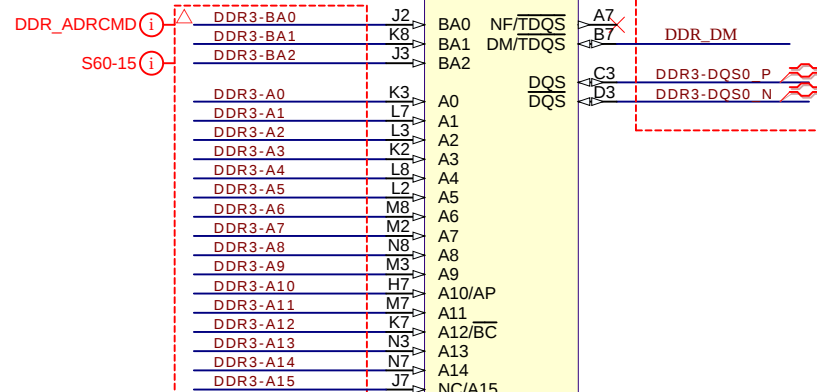
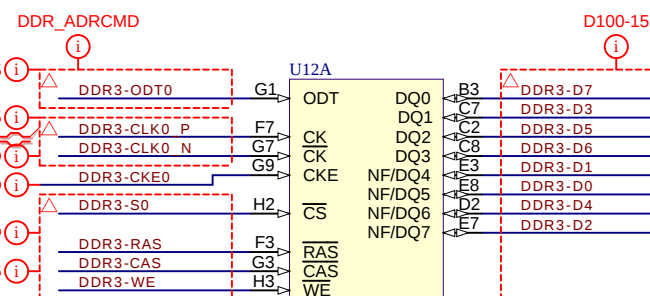
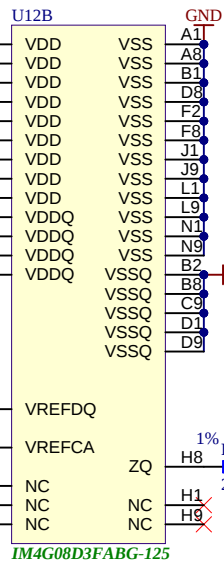
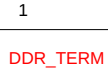
Title: TE0710			
A4	Number: TE0710 100-1Q	Rev. 02	
Date: 22.10.2013	Copyright: Trenz Electronic GmbH / TT		Page 9 of 13
Filename: ETHERNET.SchDoc			

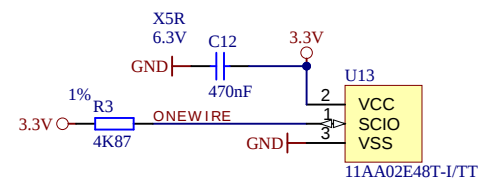
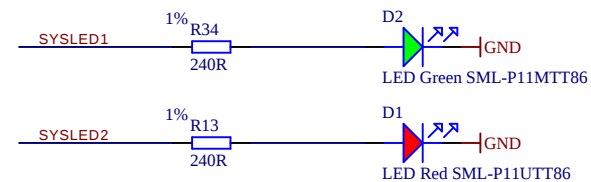
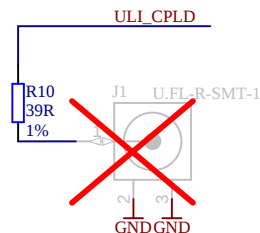
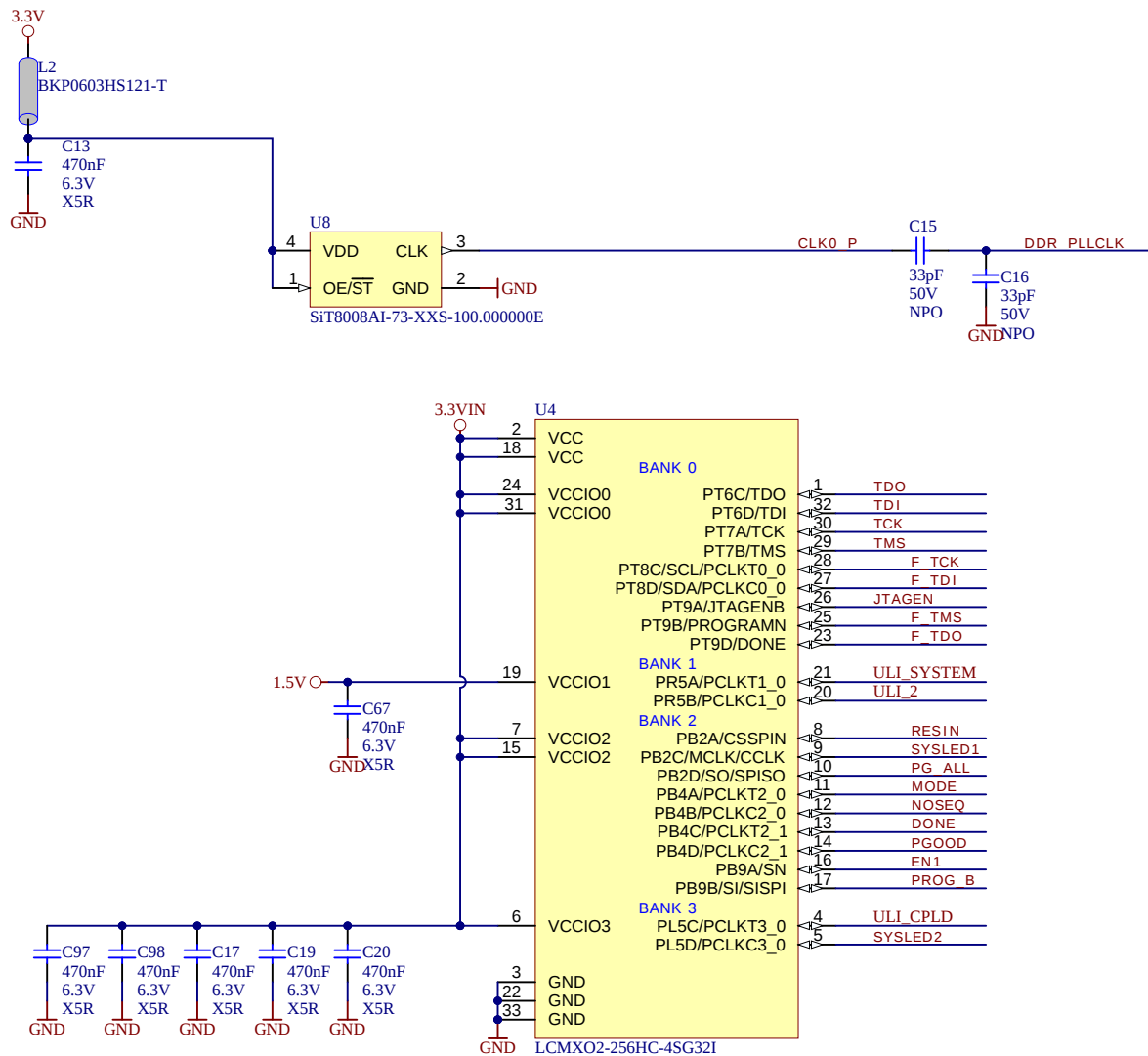
1

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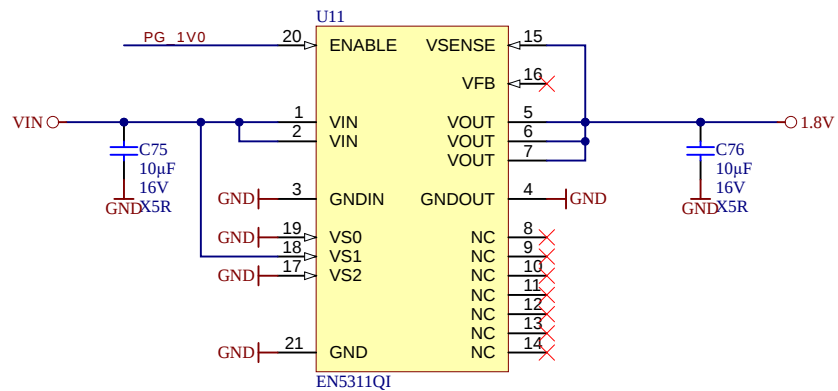
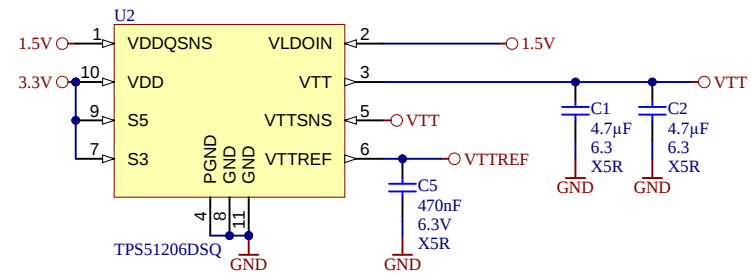
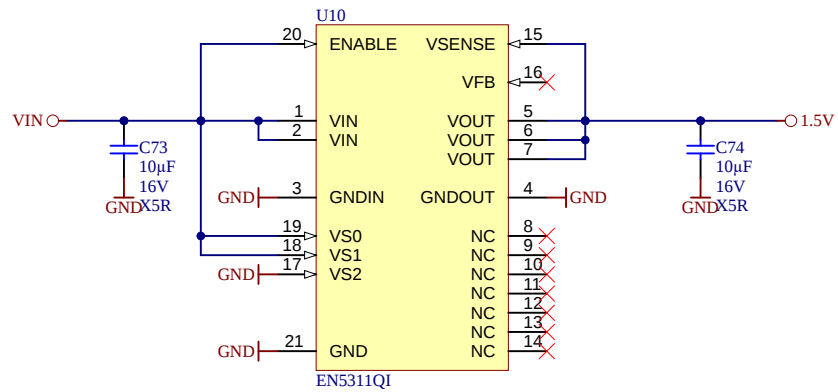
3

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		Title: TE0710	
		Number: TE0710 100-1Q	Rev. 02
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Filename: PLL.SchDoc			



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	A4	Number: TE0710 100-1Q	Rev. 02
	Date: 2014-03-07	Copyright: Trenz Electronic GmbH	Page 12 of 13
	Filename: POWER.SchDoc		

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Revision changes REV02.a (15.03.19):

1) Schematic: nets B14_IO0 and B14_IO4 renamed to B16_IO0 and B16_IO4 accordingly
2) Schematic: changed value of R12 and R15 to 240R

Revision changes REV02.b (19.07.19):

3) VY: Replaced capacitors C39, C40, C43, C111 from 10nF 6.3V X5R to 10nF 10V X7R

B

B

C

C

D

D



Title:TEC0049

A4

Number:TE0710100-1Q

Rev.02

Date:2017-10-09

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Filename:RevisionChanges.SchDoc

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