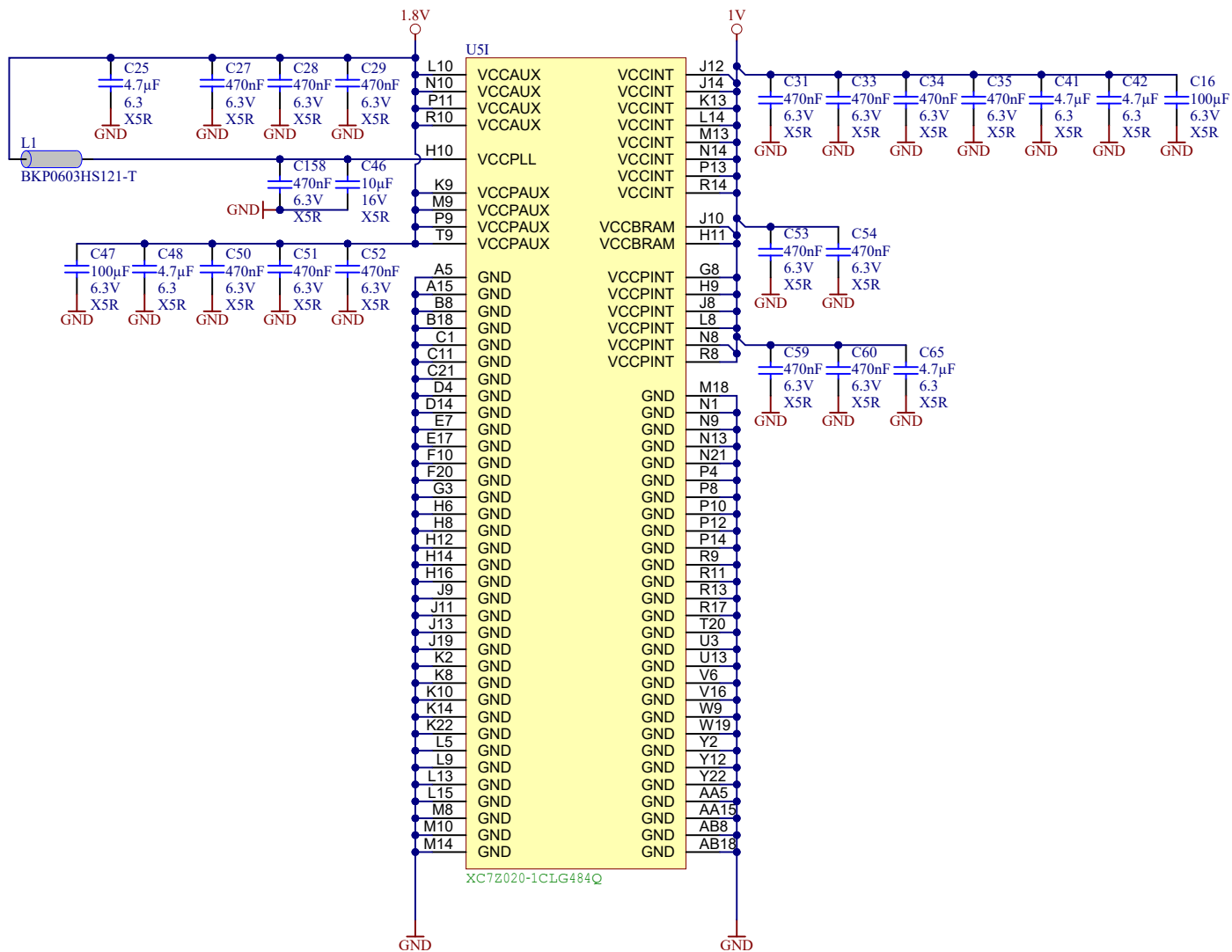
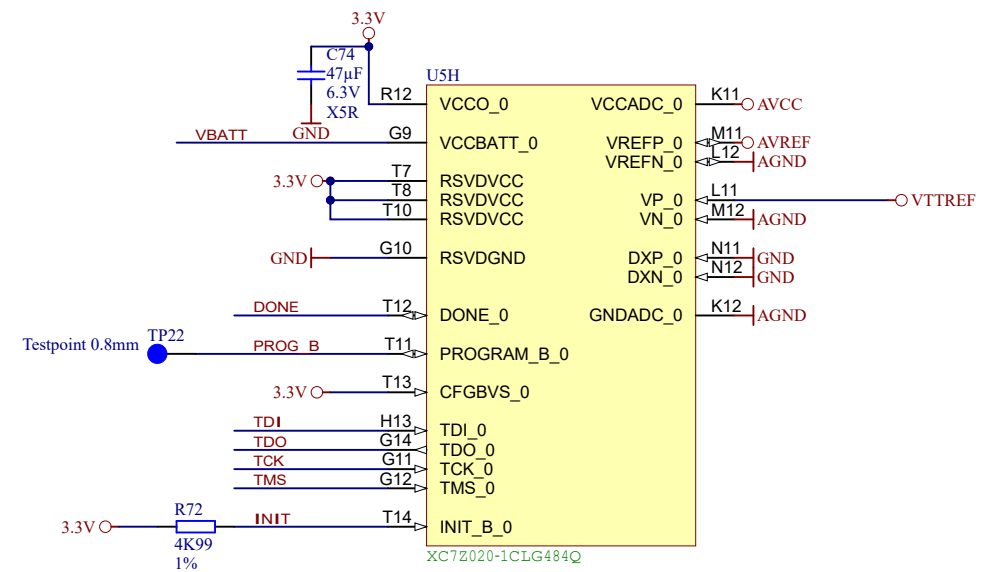
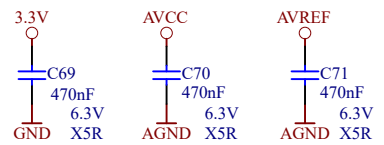
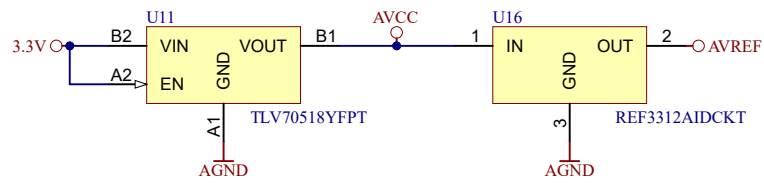
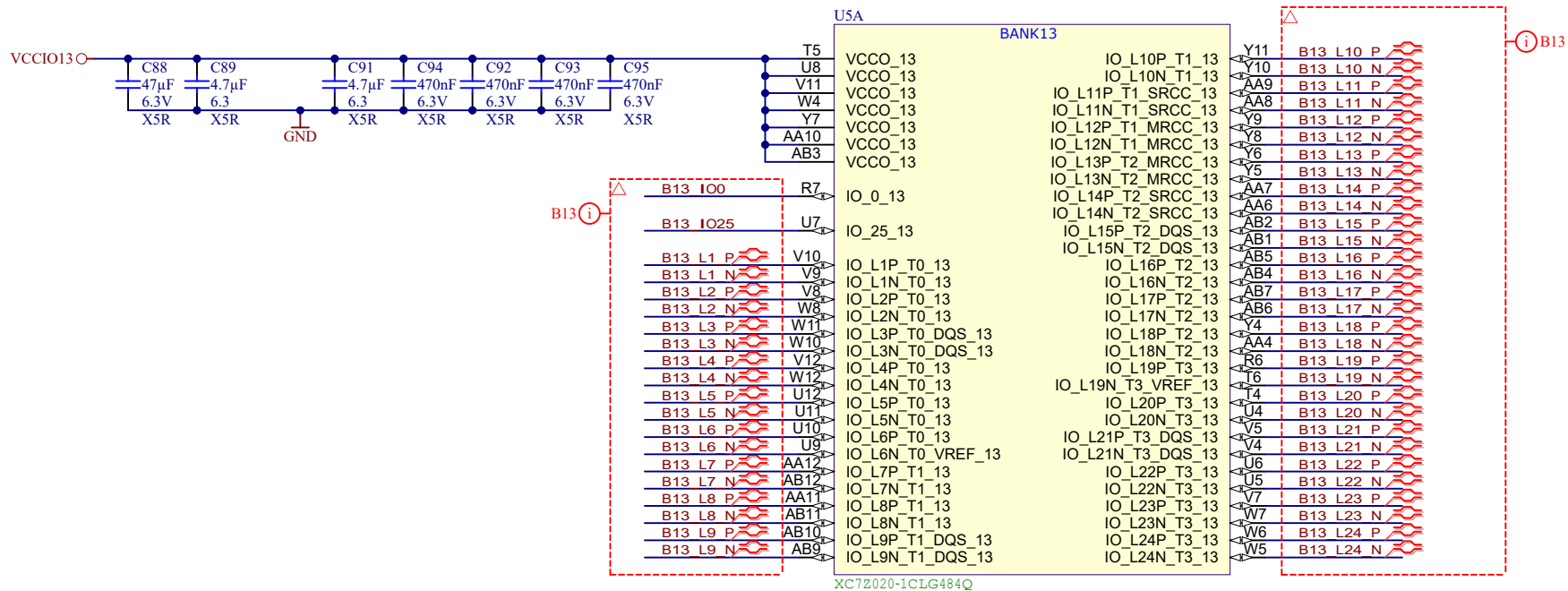




	Title: GigaZee - FPGA Power		
	A4	Number: TE0720 61Q43FA	Rev. 03
	Date: 2015-10-12	Copyright: 2013 Trenz Electronic GmbH	Page2 of 15
	Filename: FPGA-PWR.SchDoc		

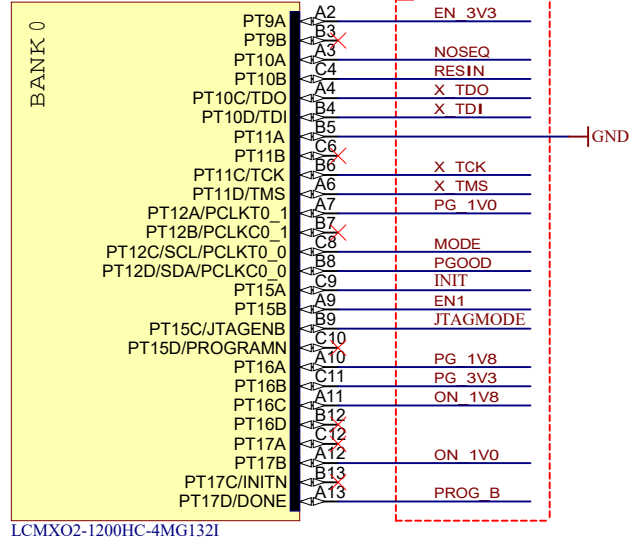


		Title: GigaZee - B0	
		A4	Number: TE0720 61Q43FA
Date: 2015-10-12		Copyright: 2013 Trenz Electronic GmbH	
Filename: FPGA-MISC.SchDoc		Page3 of 15	

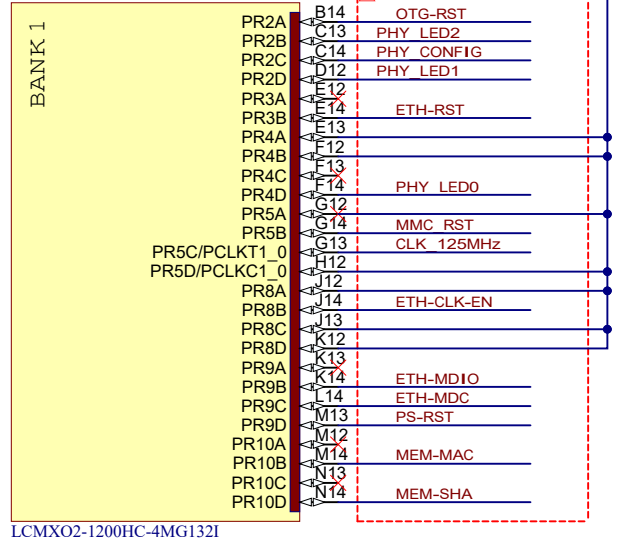


		Title: GigaZee - B13	
		A4	Number: TE0720 61Q43FA
Date: 2015-10-12	Copyright: 2013 Trenz Electronic GmbH		Rev. 03
Filename: B13.SchDoc		Page5 of 15	

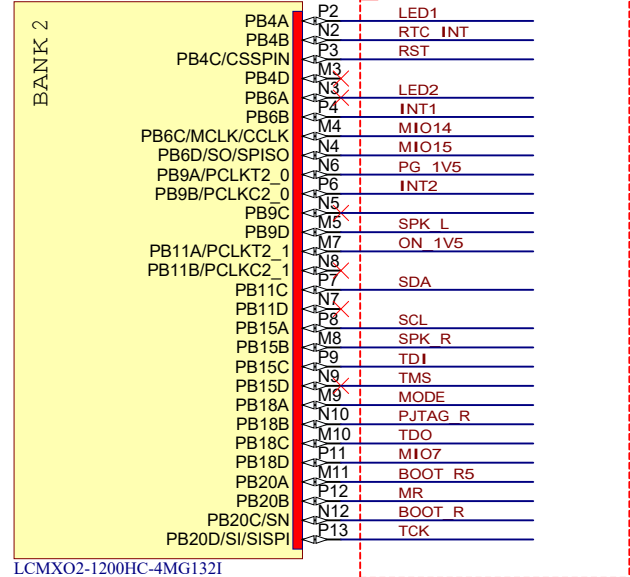
U19A



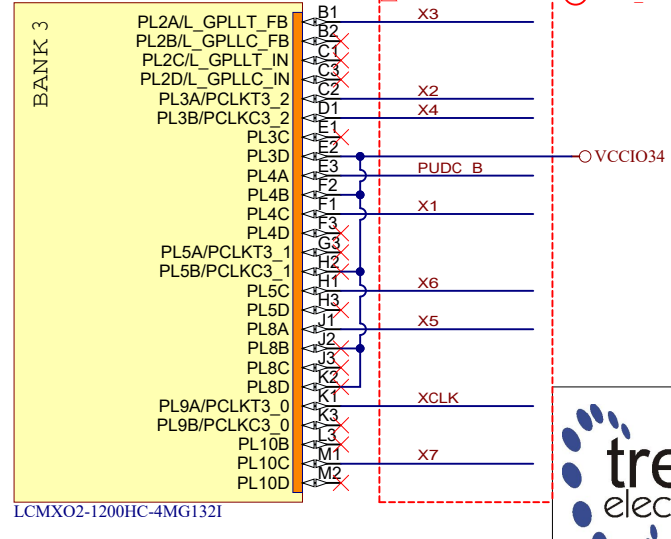
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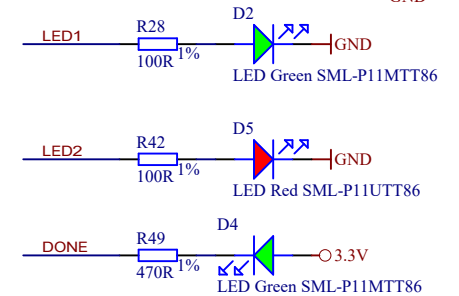
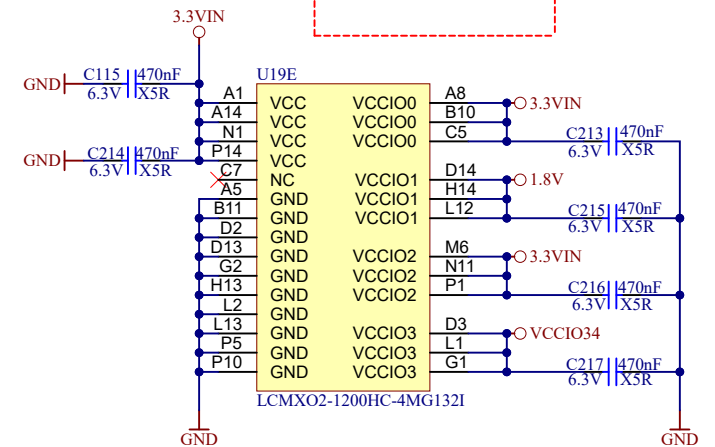
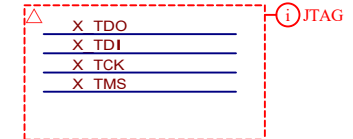
U19C



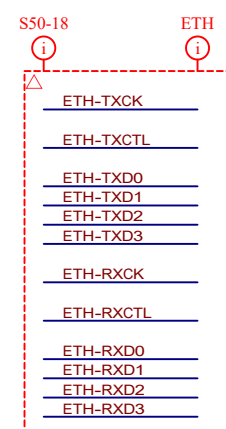
U19D

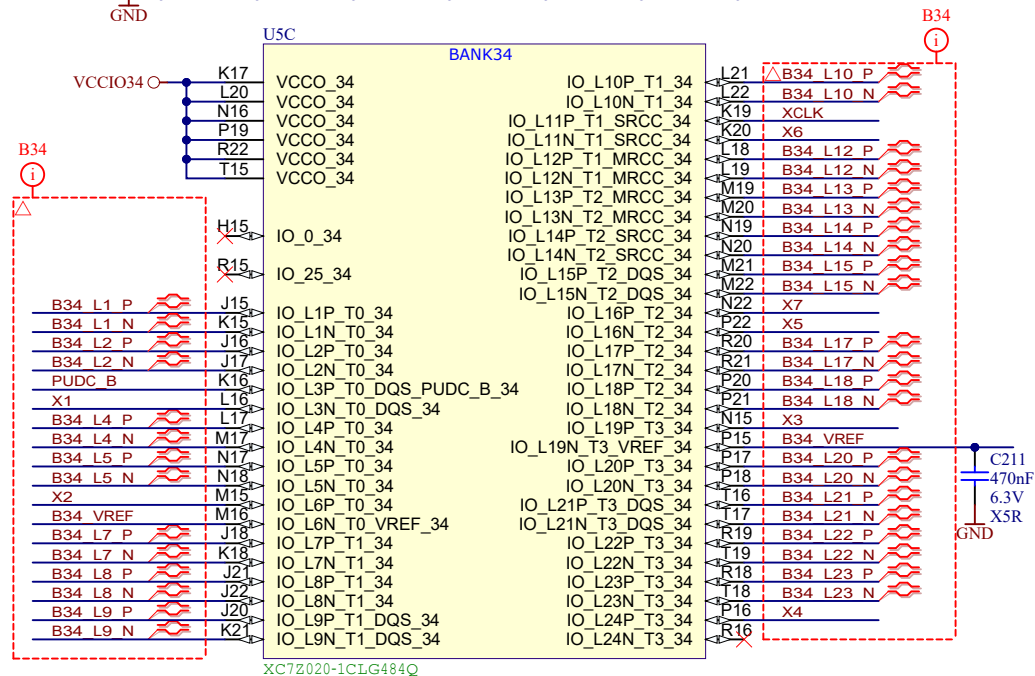
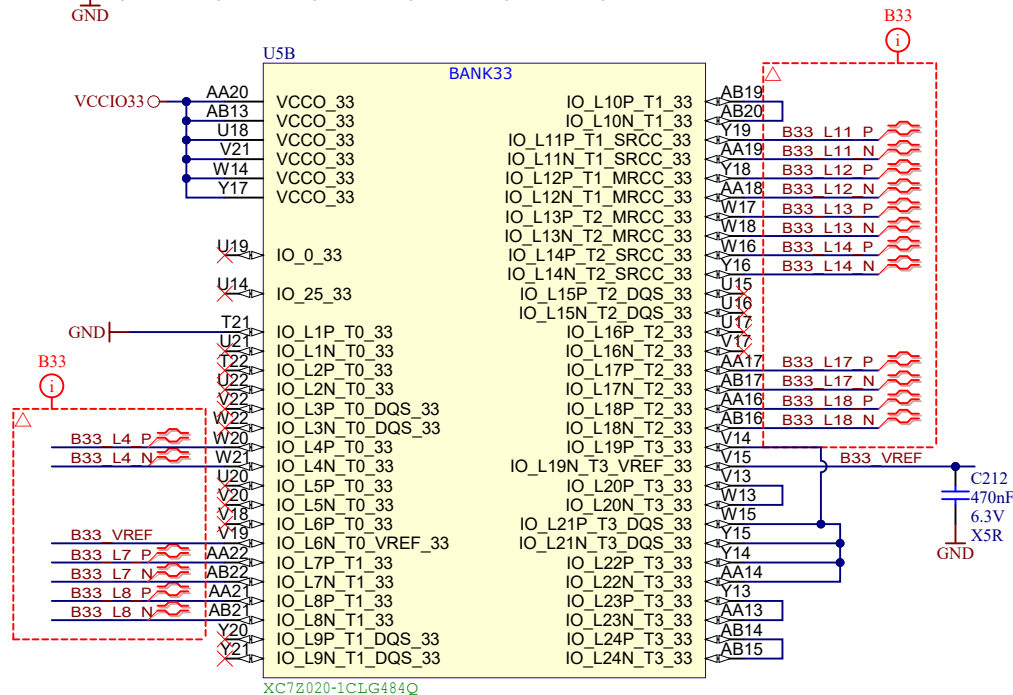
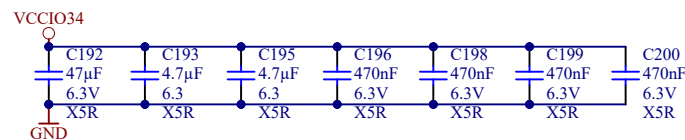
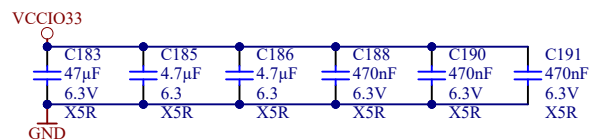


CPLD_3V3

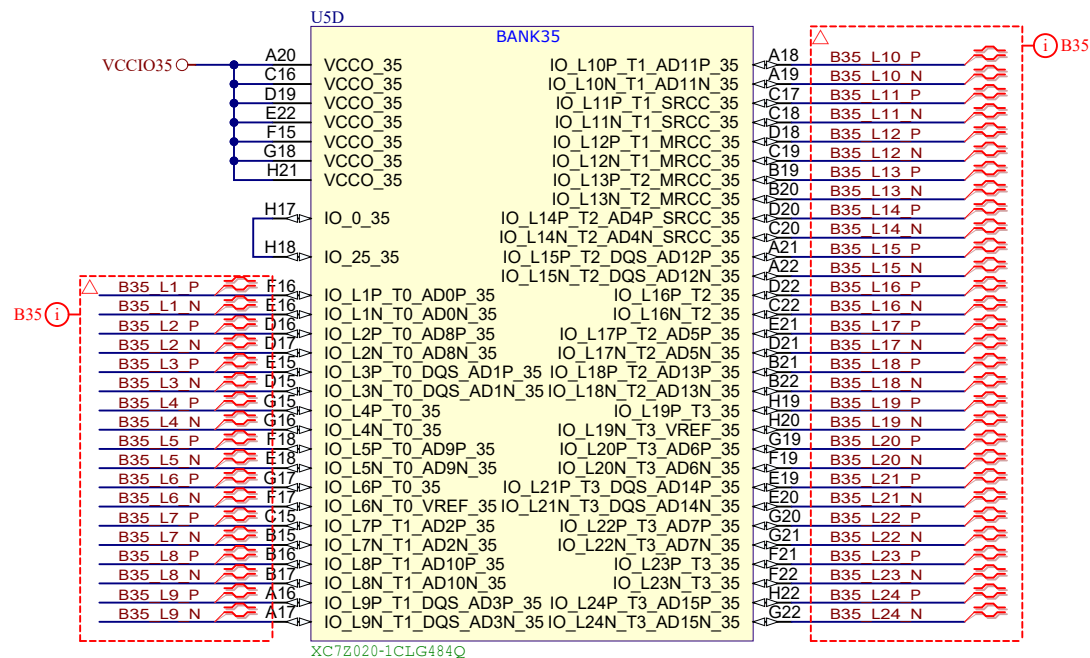
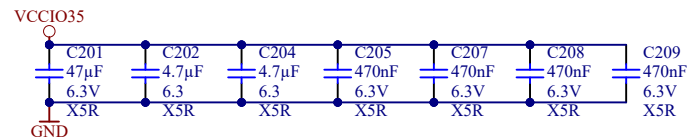


Title: GigaZee - System Controller		
A4	Number: TE0720 61Q43FA	Rev. 03
Date: 2015-10-12	Copyright: 2013 Trenz Electronic GmbH	Page6 of 15
Filename: SystemController.SchDoc		





Title: GigaZee - B33 - B34			
A4	Number: TE0720 61Q43FA		Rev. 03
Date: 2015-10-12	Copyright: 2013 Trenz Electronic GmbH		Page8 of 15
Filename: B33-B34.SchDoc			



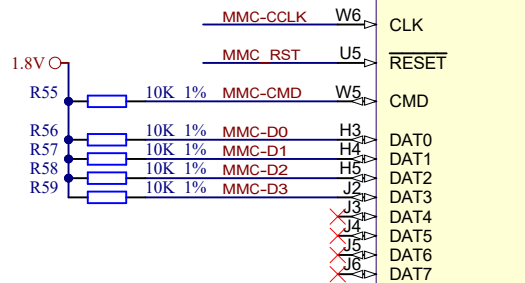
Title: GigaZee - B35			
A4	Number: TE0720 61Q43FA	Rev. 03	
Date: 2015-10-12	Copyright: 2013 Trenz Electronic GmbH		Page9 of 15
Filename: B35.SchDoc			

U15D

A4	NC	NC	R2
A6	NC	NC	R3
A9	NC	NC	R12
A11	NC	NC	R13
B2	NC	NC	R14
B13	NC	NC	T1
D1	NC	NC	T2
D14	NC	NC	T3
H1	NC	NC	T12
H2	NC	NC	T13
H8	NC	NC	T14
H9	NC	NC	U1
H10	NC	NC	U2
H11	NC	NC	U3
H12	NC	NC	U12
H13	NC	NC	U13
H14	NC	NC	U14
J1	NC	NC	V1
J7	NC	NC	V2
J8	NC	NC	V3
J9	NC	NC	V12
J10	NC	NC	V13
J11	NC	NC	V14
J12	NC	NC	W1
J13	NC	NC	W2
J14	NC	NC	W3
K1	NC	NC	W7
K3	NC	NC	W8
K7	NC	NC	W9
K8	NC	NC	W10
K9	NC	NC	W11
K10	NC	NC	W12
K11	NC	NC	W13
K12	NC	NC	W14
K13	NC	NC	Y1
K14	NC	NC	Y3
L1	NC	NC	Y6
L2	NC	NC	Y7
L3	NC	NC	Y8
L4	NC	NC	Y9
L12	NC	NC	Y10
L13	NC	NC	Y11
L14	NC	NC	Y12
M1	NC	NC	Y13
M2	NC	NC	Y14
M3	NC	NC	AA1
M12	NC	NC	AA2
M13	NC	NC	AA8
M14	NC	NC	AA9
N1	NC	NC	AA11
N2	NC	NC	AA12
N3	NC	NC	AA13
N12	NC	NC	AA14
N13	NC	NC	AE1
N14	NC	NC	AE14
P1	NC	NC	AG2
P2	NC	NC	AG13
P12	NC	NC	AH4
P13	NC	NC	AH6
P14	NC	NC	AH9
R1	NC	NC	AH11

IS21ES08G-JCL1

U15A



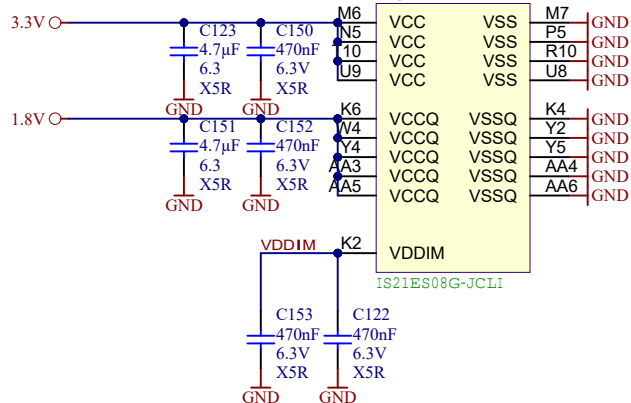
IS21ES08G-JCL1

U15C

H6	RFU
H7	RFU
K5	RFU
M5	RFU
M8	RFU
M9	RFU
M10	RFU
N10	RFU
P3	RFU
P10	RFU
R5	RFU
T5	RFU
U6	RFU
U7	RFU
U10	RFU
AA7	RFU
AA10	RFU

IS21ES08G-JCL1

U15B



IS21ES08G-JCL1



Title: GigaZee - eNAND

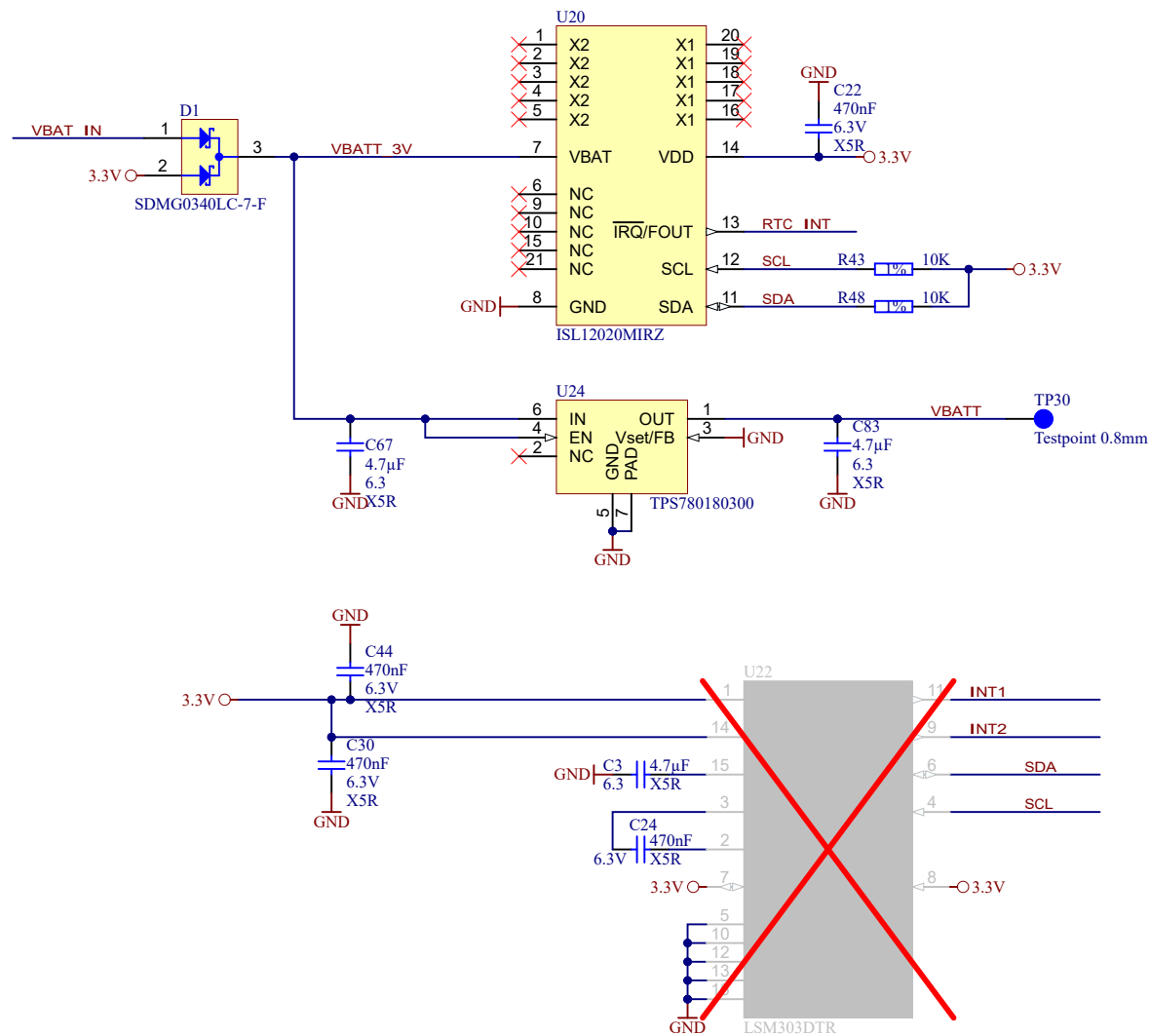
A4 Number: TE0720
61Q43FA

Rev. 03

Date: 2015-10-12 Copyright: 2013 Trenz Electronic GmbH

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Filename: eMMC.SchDoc



Title: GigaZee - RTC, MEMS, SHA, MAC		
A4	Number: TE0720 61Q43FA	Rev. 03
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Filename: SENSOR-RTC.SchDoc		Page 12 of 15

1

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USG

BANK502

E2 VCCO_DDR 502
F5 VCCO_DDR 502
H1 VCCO_DDR 502
J4 VCCO_DDR 502
K7 VCCO_DDR 502
M3 VCCO_DDR 502
N6 VCCO_DDR 502
R2 VCCO_DDR 502
V1 VCCO_DDR 502

H7 PS_DDR_VREF0_502
P7 PS_DDR_VREF1_502

PS_DDR_A0_502
PS_DDR_A1_502
PS_DDR_A2_502
PS_DDR_A3_502
PS_DDR_A4_502
PS_DDR_A5_502
PS_DDR_A6_502
PS_DDR_A7_502
PS_DDR_A8_502
PS_DDR_A9_502
PS_DDR_A10_502
PS_DDR_A11_502
PS_DDR_A12_502
PS_DDR_A13_502
PS_DDR_A14_502

PS_DDR_BA0_502
PS_DDR_BA1_502
PS_DDR_BA2_502

PS_DDR_DM0_502
PS_DDR_DM1_502
PS_DDR_DM2_502
PS_DDR_DM3_502

PS_DDR_CK0_502
PS_DDR_CK1_502

PS_DDR_VRP_502
PS_DDR_VRN_502

PS_DDR_ODT_502

PS_DDR_CS_B_502

PS_DDR_CKE_502

PS_DDR_WE_B_502

PS_DDR_CAS_B_502

PS_DDR_RAS_B_502

PS_DDR_DRST_B_502

XC7Z020-1CLG484Q

M4 DDR3-A0
M5 DDR3-A1
K4 DDR3-A2
L4 DDR3-A3
K6 DDR3-A4
K5 DDR3-A5
J7 DDR3-A6
J6 DDR3-A7
J5 DDR3-A8
H5 DDR3-A9
J3 DDR3-A10
G5 DDR3-A11
H4 DDR3-A12
F4 DDR3-A13
G4 DDR3-A14

L7 DDR3-BA0
L6 DDR3-BA1
M6 DDR3-BA2

B1 DDR3-DM0
H3 DDR3-DM1
P1 DDR3-DM2
AA2 DDR3-DM3

N4 DDR3-CLK0_P
N5 DDR3-CLK0_N

N7 DDR3-VRP
M7 DDR3-VRN

P5 DDR3-ODT0

P6 DDR3-S0

V3 DDR3-CKE0

R4 DDR3-WE

P3 DDR3-CAS

R5 DDR3-RAS

F3 DDR3-RESET

1.5V

C126 100µF 6.3V X5R
C175 4.7µF 6.3V X5R
C176 470nF 6.3V X5R
C177 470nF 6.3V X5R
C179 470nF 6.3V X5R
C182 470nF 6.3V X5R

DDR3-ODT0
DDR3-S0
DDR3-CKE0
DDR3-RESET

CONTROL

DDR3-A0
DDR3-A1
DDR3-A2
DDR3-A3
DDR3-A4
DDR3-A5
DDR3-A6
DDR3-A7
DDR3-A8
DDR3-A9
DDR3-A10
DDR3-A11
DDR3-A12
DDR3-A13
DDR3-A14

DDR3-BA0
DDR3-BA1
DDR3-BA2
DDR3-WE
DDR3-CAS
DDR3-RAS

① ADDR_CMD

LANE0

LANE1

LANE2

LANE3

DDR3-D0
DDR3-D1
DDR3-D2
DDR3-D3
DDR3-D4
DDR3-D5
DDR3-D6
DDR3-D7
DDR3-DM0
DDR3-QS0_P
DDR3-QS0_N

DDR3-D8
DDR3-D9
DDR3-D10
DDR3-D11
DDR3-D12
DDR3-D13
DDR3-D14
DDR3-D15
DDR3-DM1
DDR3-QS1_P
DDR3-QS1_N

DDR3-D16
DDR3-D17
DDR3-D18
DDR3-D19
DDR3-D20
DDR3-D21
DDR3-D22
DDR3-D23
DDR3-DM2
DDR3-QS2_P
DDR3-QS2_N

DDR3-D24
DDR3-D25
DDR3-D26
DDR3-D27
DDR3-D28
DDR3-D29
DDR3-D30
DDR3-D31
DDR3-DM3
DDR3-QS3_P
DDR3-QS3_N



Title:

GigaZee - B502

A4

Number:

TE0720
61Q43FA

Rev.

03

Date:

2015-10-12

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Filename:

PS-DDR.SchDoc

1

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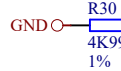
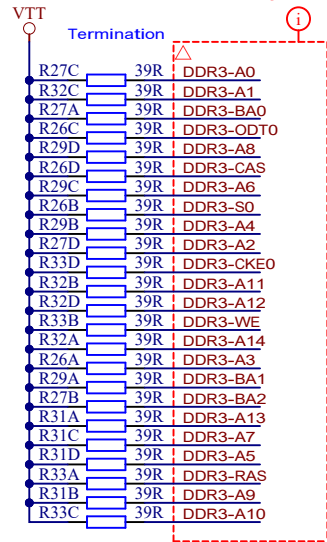
DDR3-Terminator

A

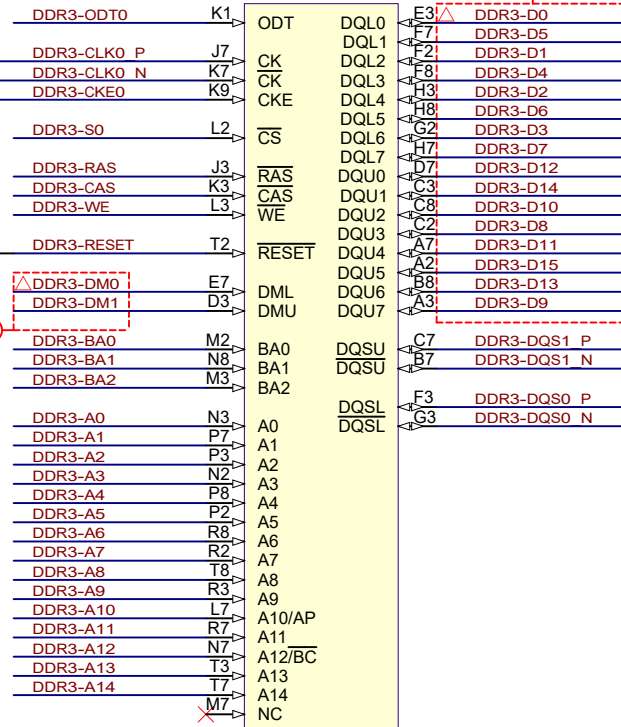
B

A

B



S40-15



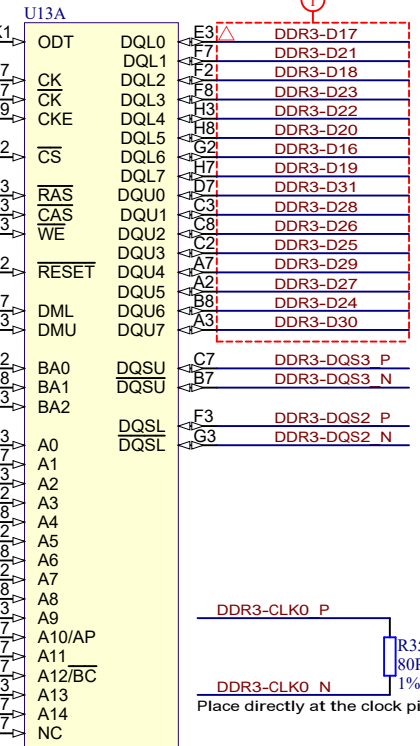
S40-15

S40-15

S40-15

S40-15

S40-15



DDR3-CLK0 P

DDR3-CLK0 N

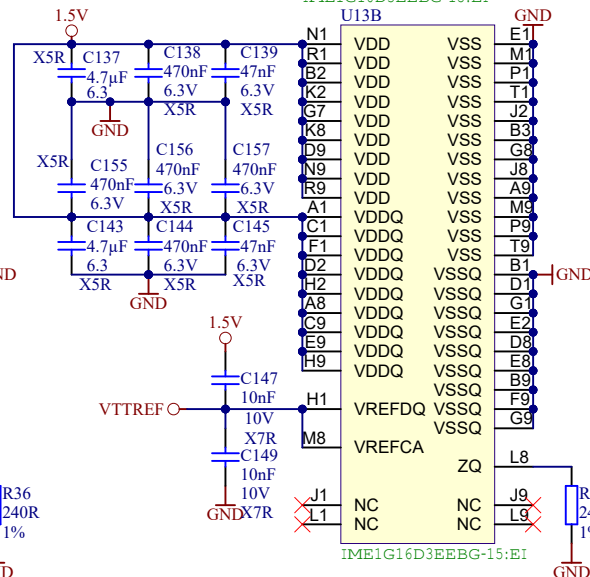
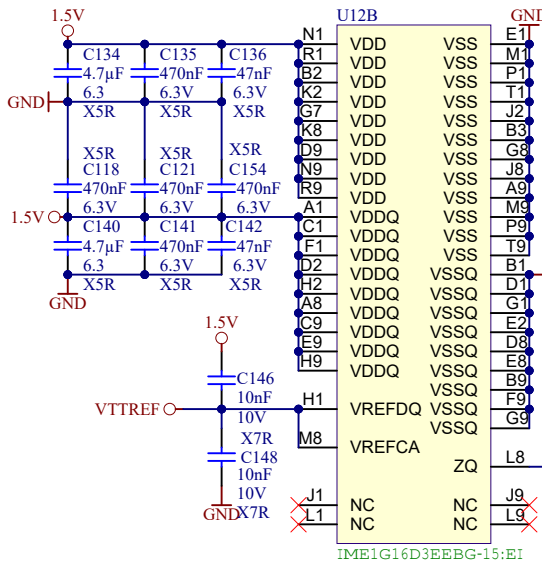
Place directly at the clock pins

C

D

C

D



Title: GigaZee - DDR3		
A4	Number: TE0720 61Q43FA	Rev. 03
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Filename: DDR3-RAM.SchDoc		

1

2

3

4

D80-15

DDR3-DQS0 P

DDR3-DQS0 N

DDR3-DQS1 P

DDR3-DQS1 N

DDR3-CLK0 P

DDR3-CLK0 N

DDR3-DQS2 P

DDR3-DQS2 N

DDR3-DQS3 P

DDR3-DQS3 N

