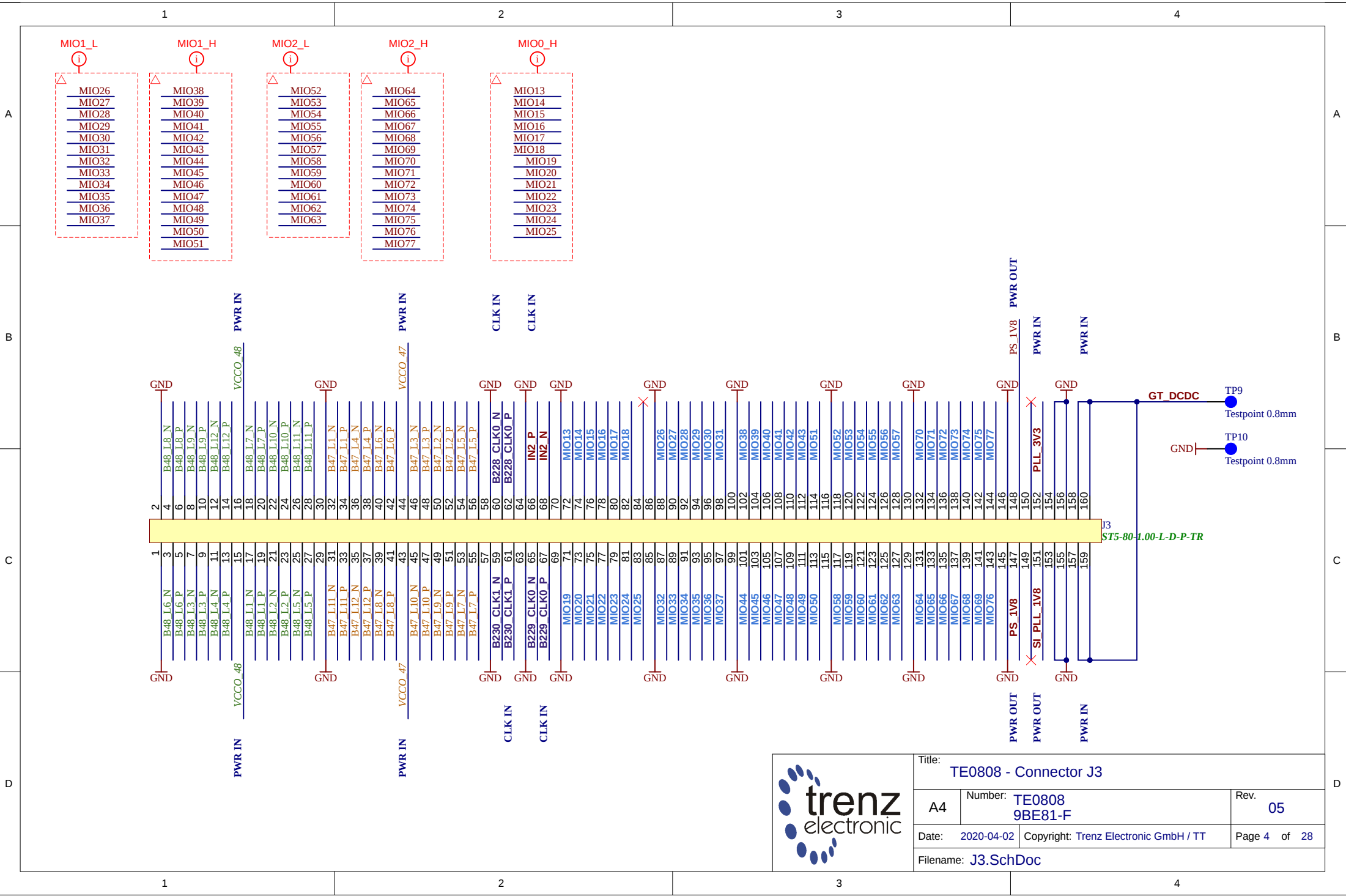




Title: TE0808 - Connector J3		
A4	Number: TE0808 9BE81-F	Rev. 05
Date: 2020-04-02	Copyright: Trenz Electronic GmbH / TT	Page 4 of 28
Filename: J3.SchDoc		

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A

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C

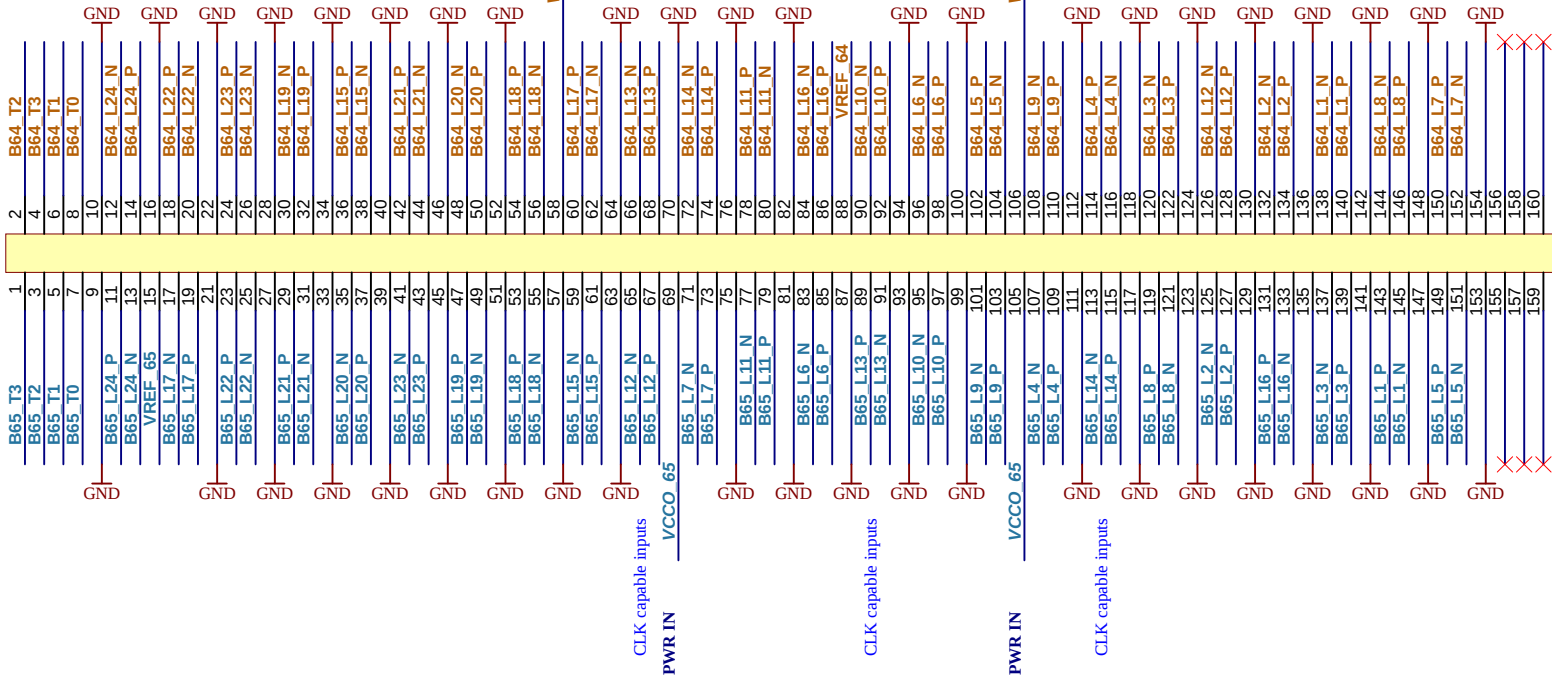
D

A

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D



J4
ST5-80-1.00-L-D-P-TR

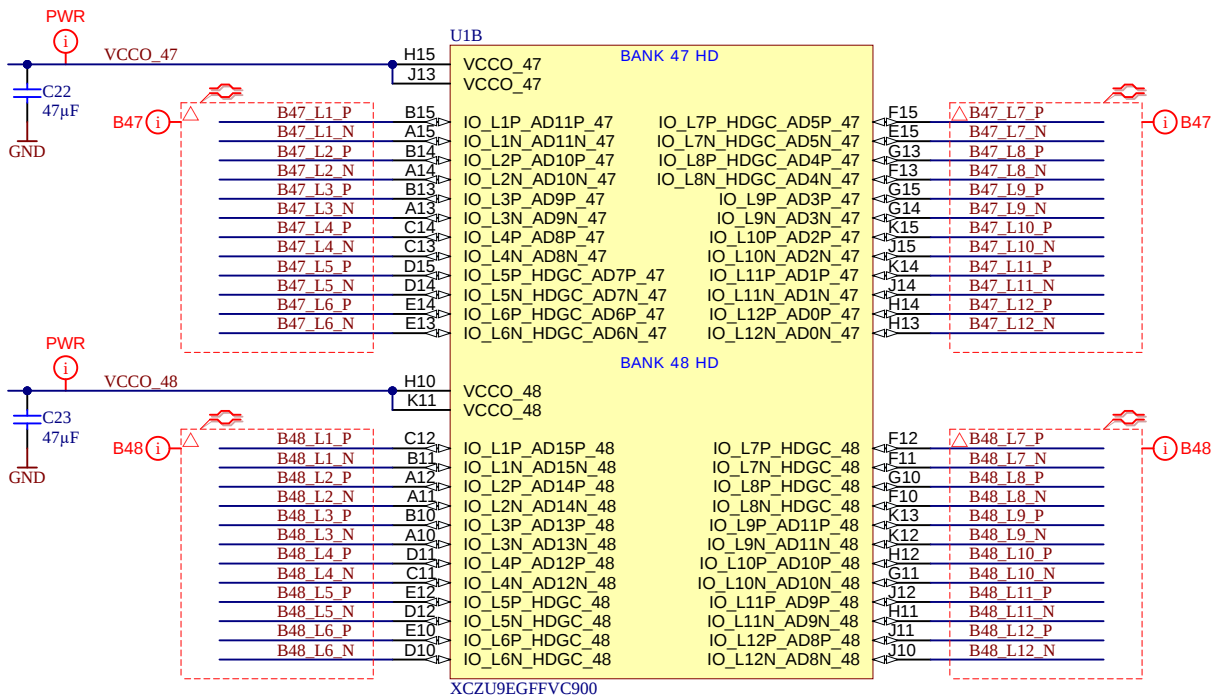
Title: TE0808 - Connector J4		
A4	Number: TE0808 9BE81-F	Rev. 05
Date: 2020-04-02	Copyright: Trenz Electronic GmbH / TT	Page 5 of 28
Filename: J4.SchDoc		

1

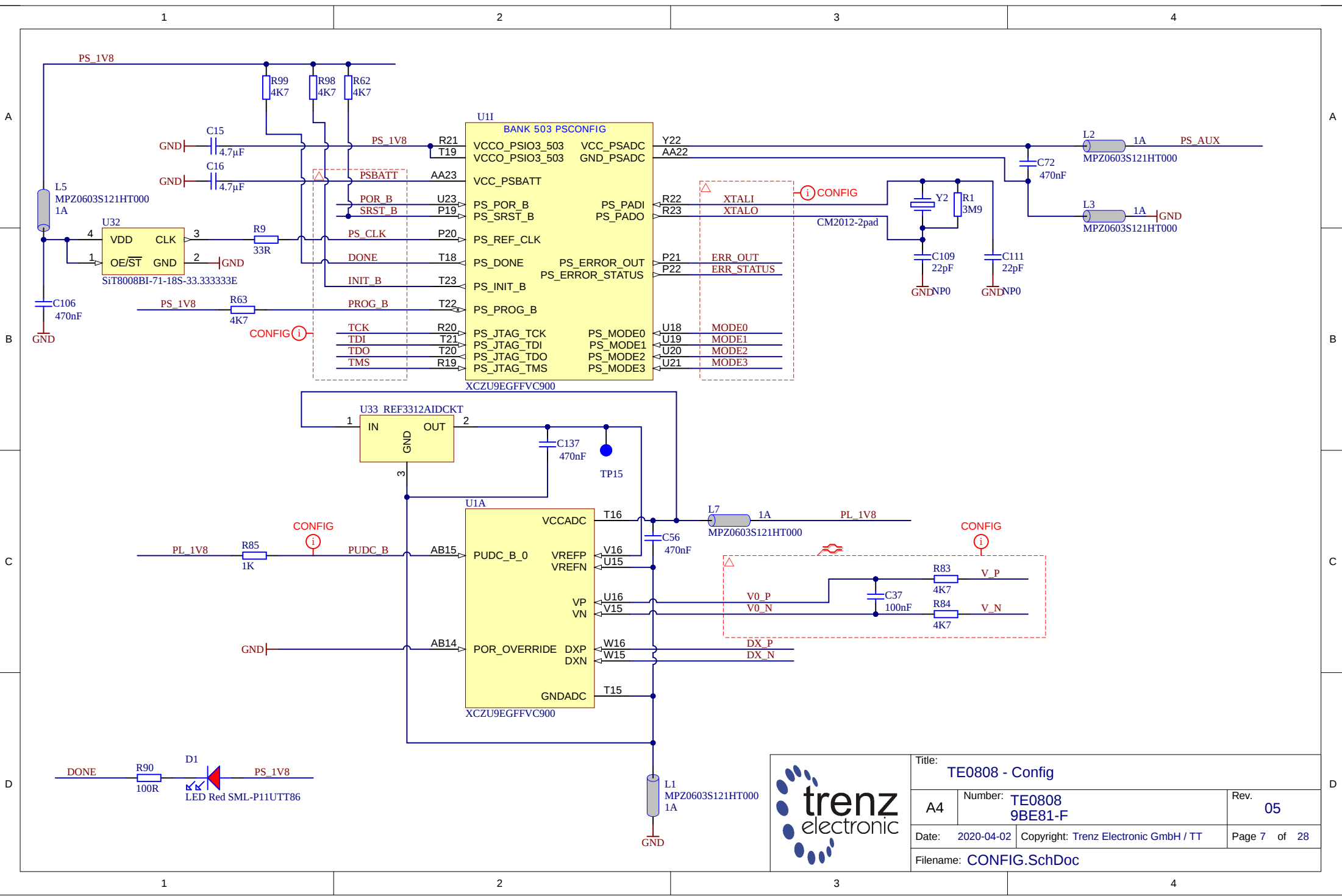
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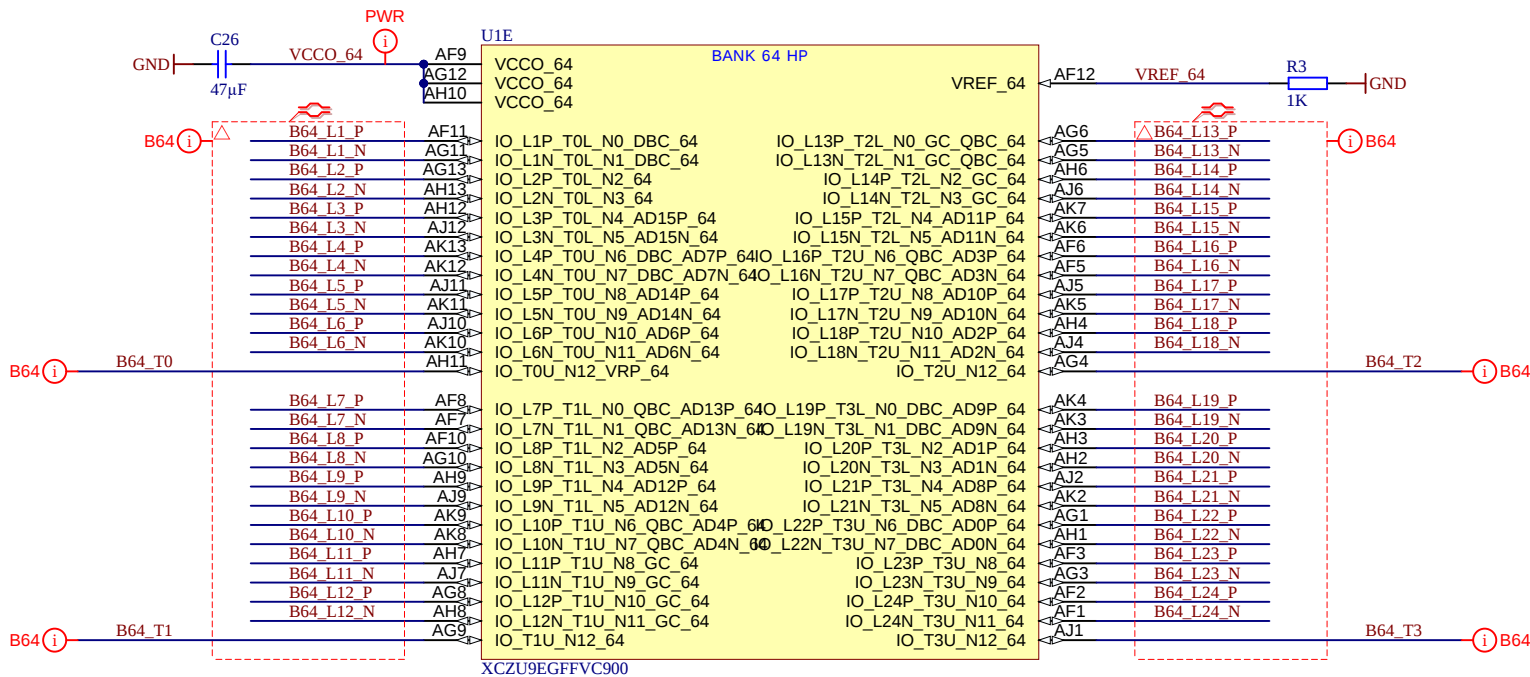
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Title: TE0808 - B47HD and B48HD			
A4	Number: TE0808 9BE81-F		Rev. 05
Date: 2020-04-02	Copyright: Trenz Electronic GmbH / TT		Page 6 of 28
Filename: B_HD.SchDoc			



Title: TE0808 - Config		
A4	Number: TE0808 9BE81-F	Rev. 05
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Filename: CONFIG.SchDoc		



Title: TE0808 - B64		
A4	Number: TE0808 9BE81-F	Rev. 05
Date: 2020-04-02	Copyright: Trenz Electronic GmbH / TT	Page 8 of 28
Filename: B64.SchDoc		

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A

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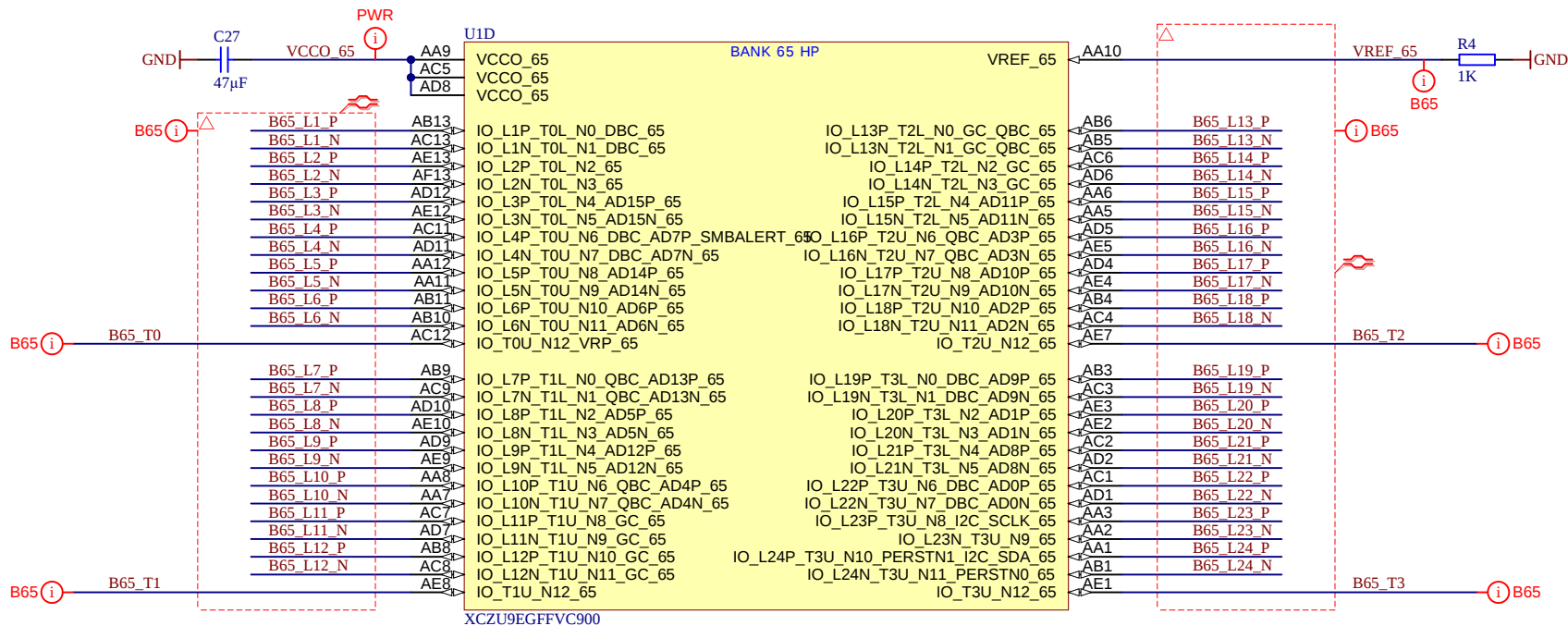
B

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Title: TE0808 - B65			
A4	Number: TE0808 9BE81-F		Rev. 05
	Date: 2020-04-02	Copyright: Trenz Electronic GmbH / TT	Page 9 of 28
Filename: B65.SchDoc			

1

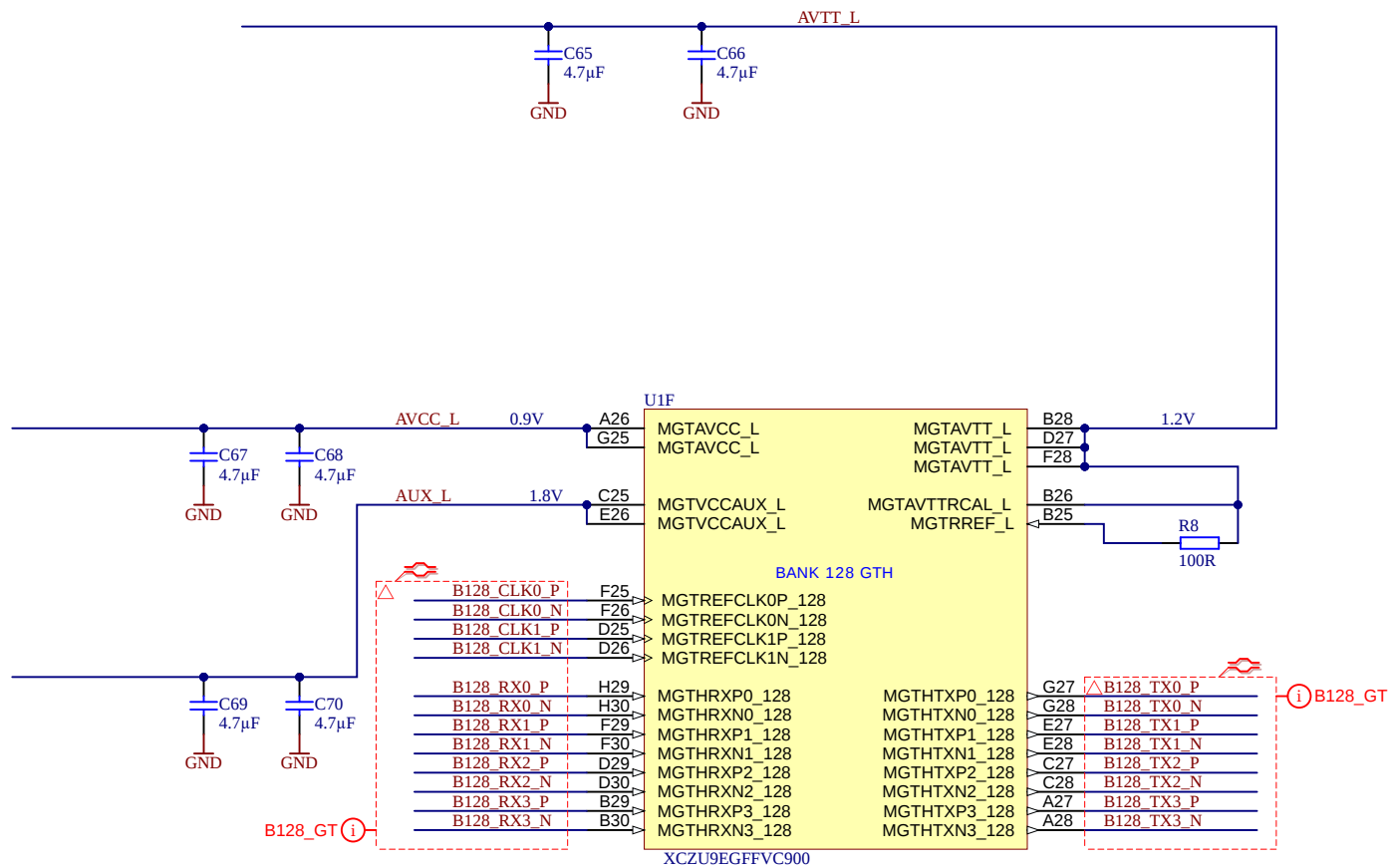
2

3

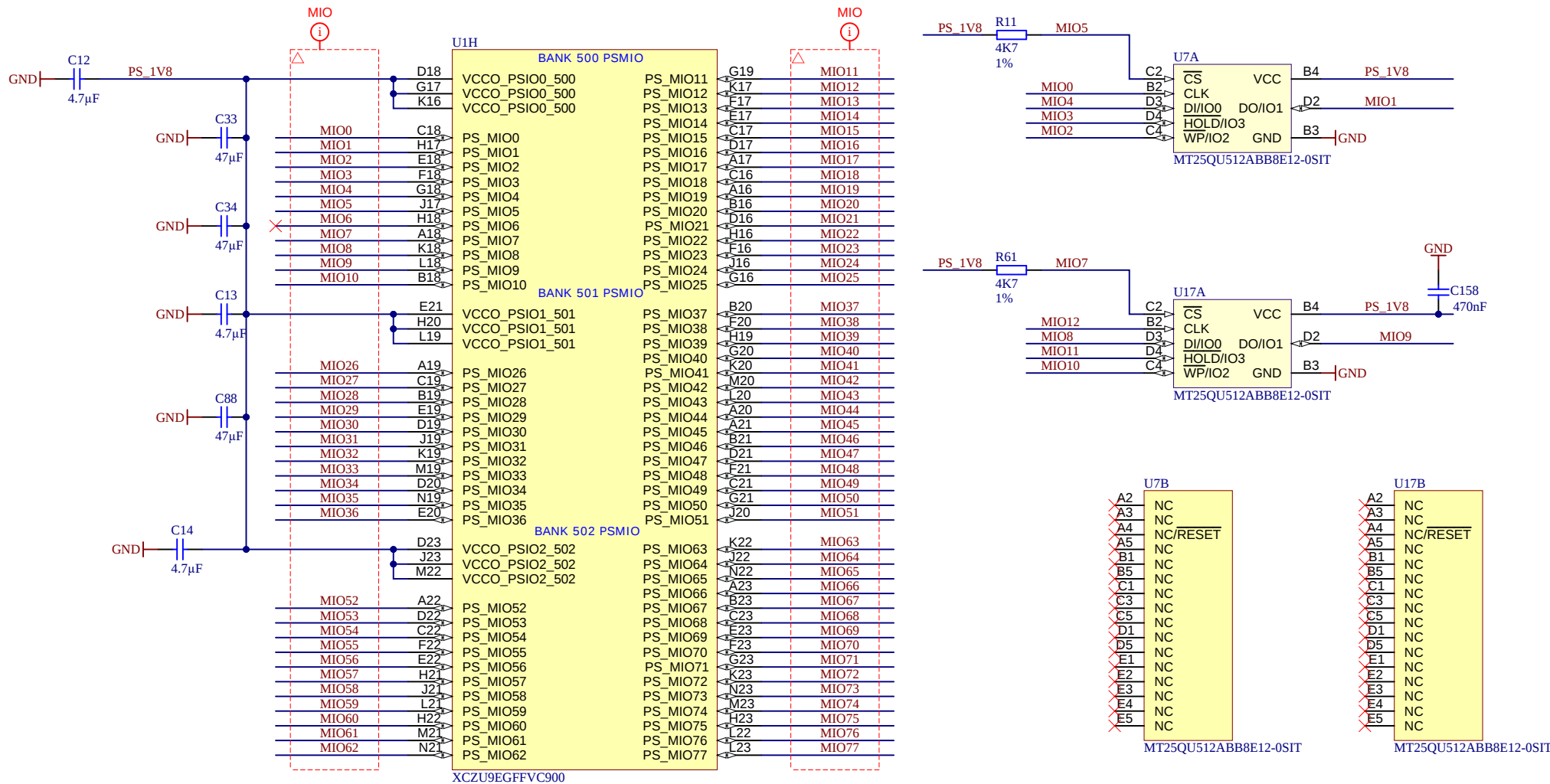
4



A4	Number: TE0808 9BE81-F	Rev. 05
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Filename: B GT.SchDoc		



	Title: TE0808 - B128GTH		
	A4	Number: TE0808 9BE81-F	Rev. 05
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	Filename: B_GT_2.SchDoc		



Title: TE0808 - MIO Banks		
A4	Number: TE0808 9BE81-F	Rev. 05
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Filename: B_MIO.SchDoc		

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A

B

C

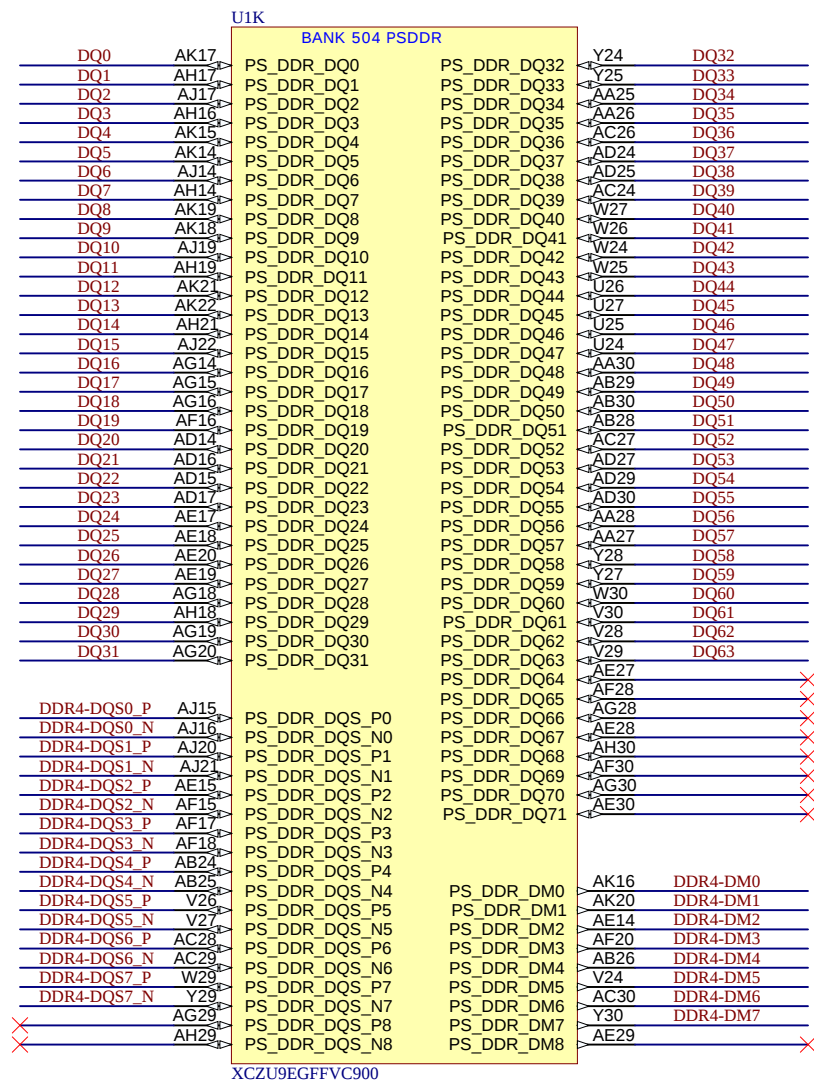
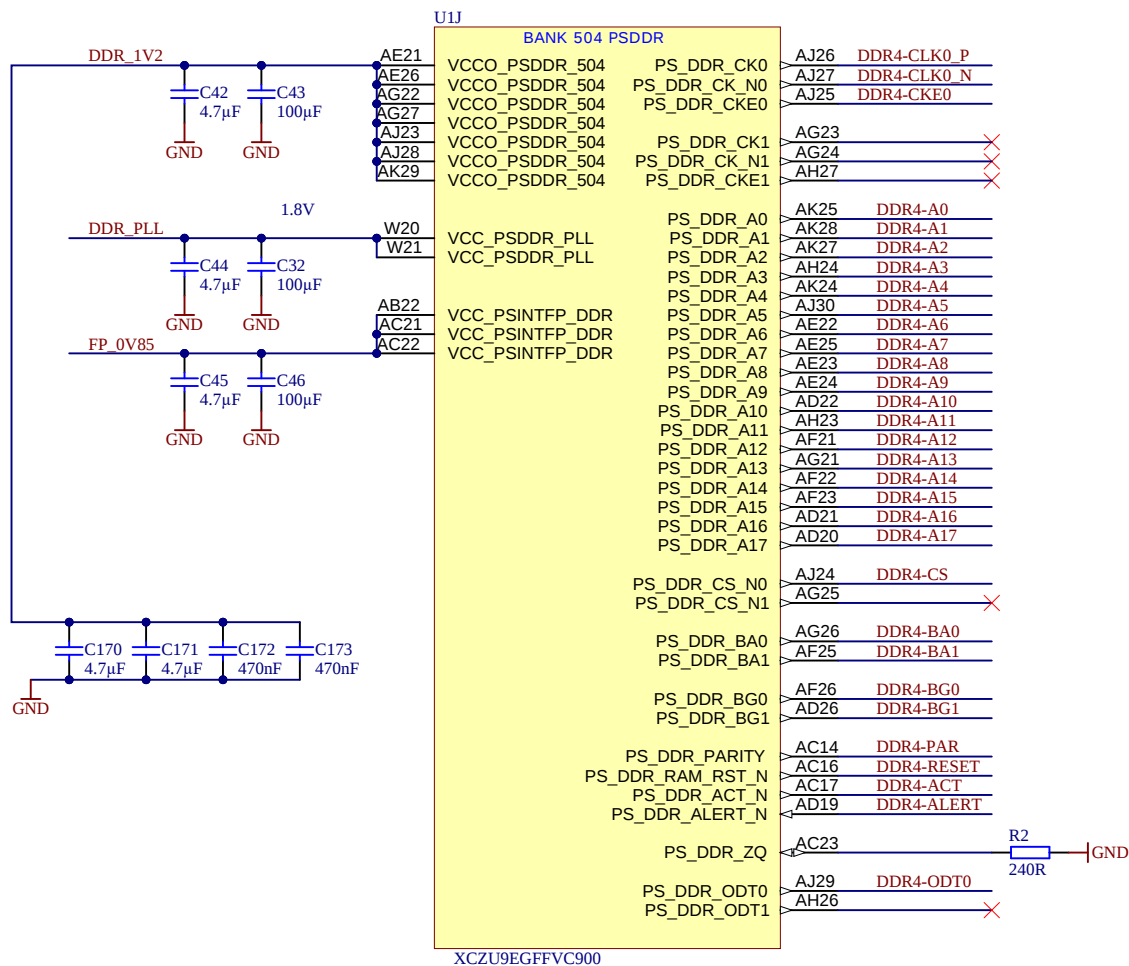
D

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Title: TE0808 - PS_DDR			
A4	Number: TE0808 9BE81-F		Rev. 05
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Filename: PS_DDR.SchDoc			

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A

A

B

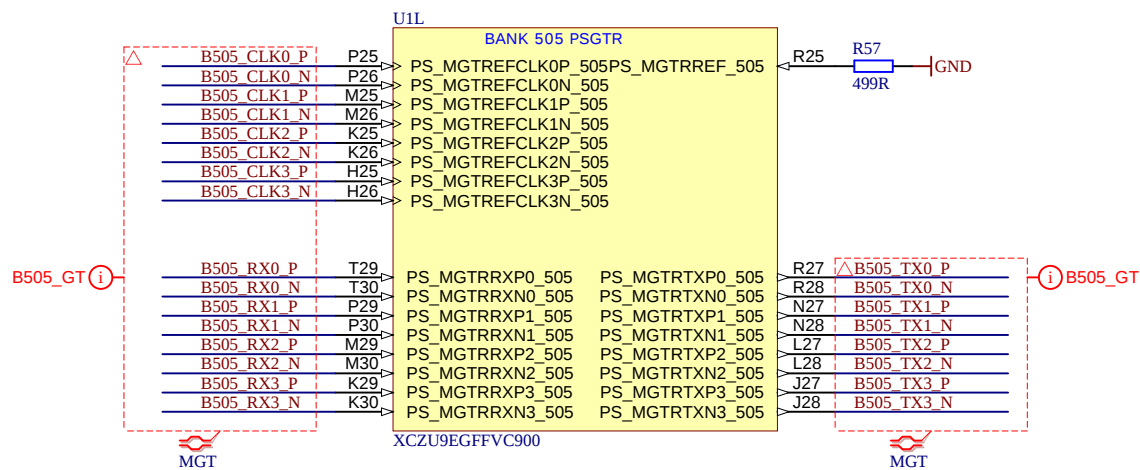
B

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C

D

D



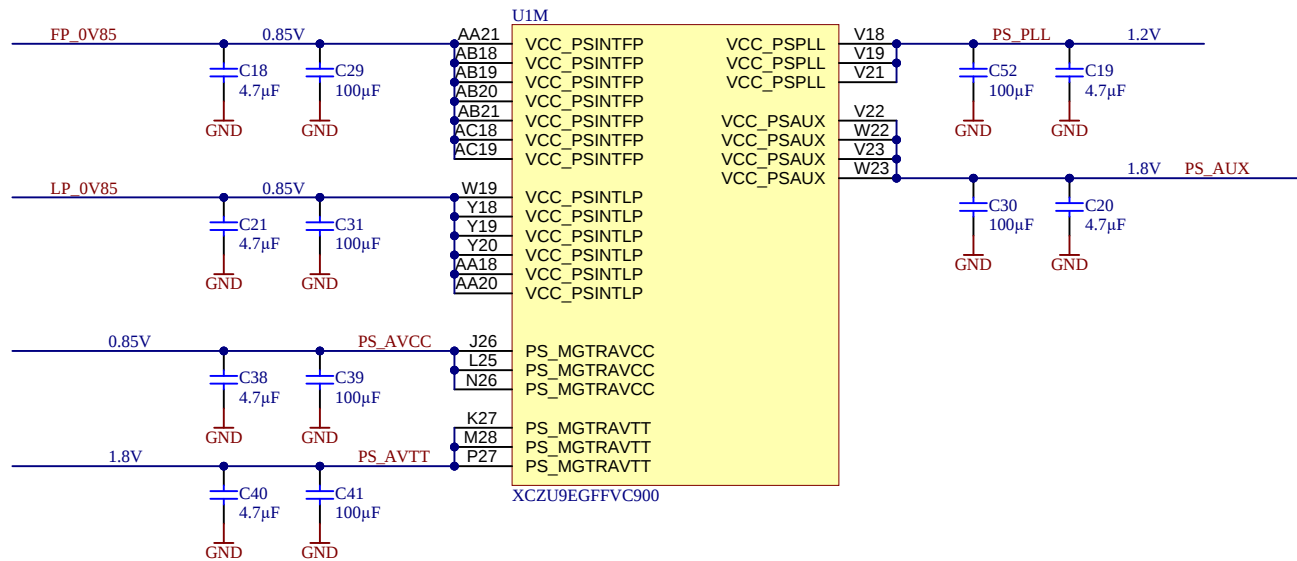
	Title: TE0808 - PS_GT		
	A4	Number: TE0808 9BE81-F	Rev. 05
	Date: 2020-04-02	Copyright: Trenz Electronic GmbH / TT	Page 15 of 28
	Filename: B_PS_GT.SchDoc		

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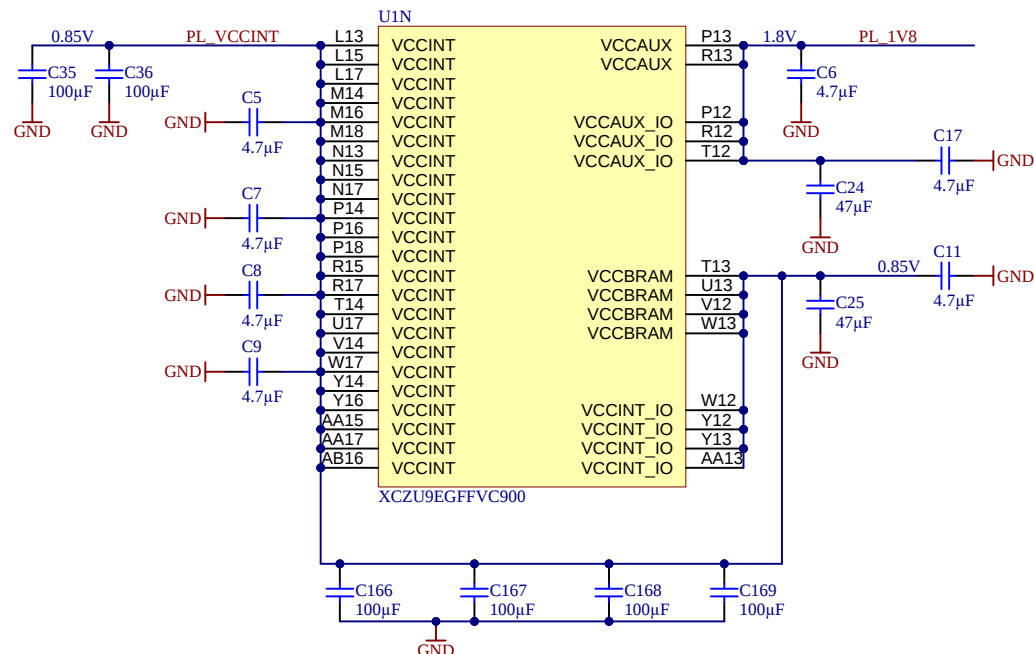
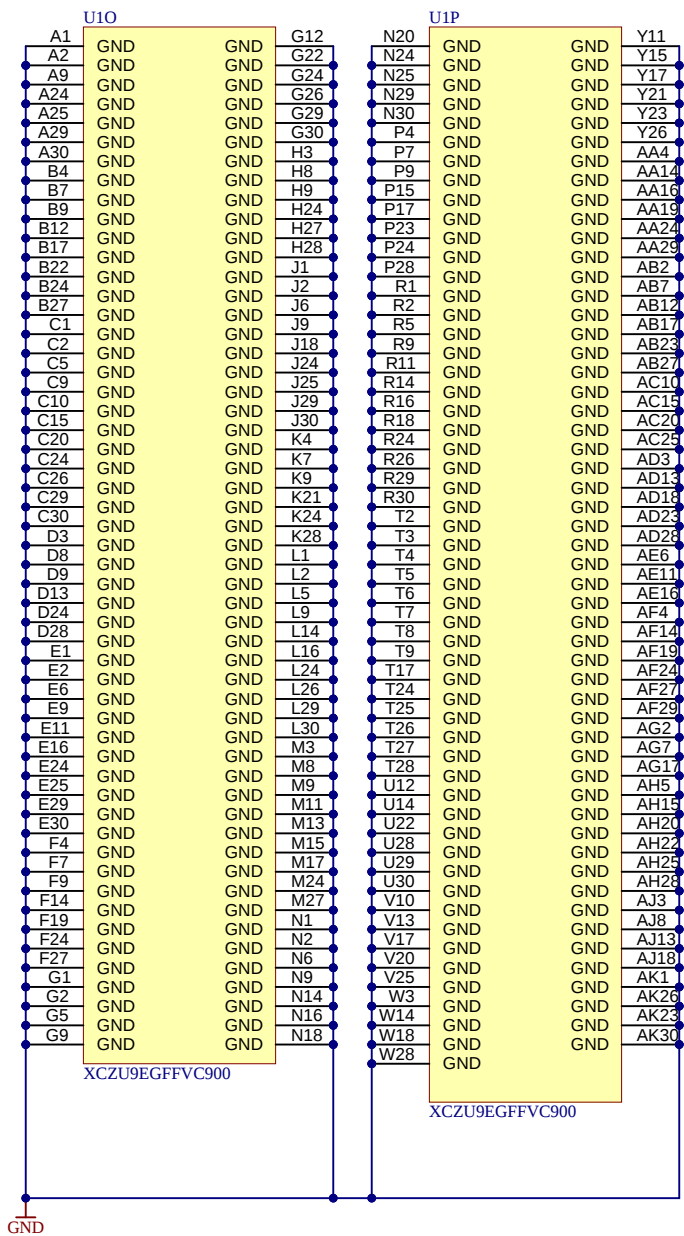
Title: TE0808 - ZU_PS_POWER		
A4	Number: TE0808 9BE81-F	Rev. 05
Date: 2020-04-02	Copyright: Trenz Electronic GmbH / TT	Page 16 of 28
Filename: ZU_PS_POWER.SchDoc		

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Title: TE0808 - ZU_POWER		
A4	Number: TE0808 9BE81-F	Rev. 05
Date: 2020-04-02	Copyright: Trenz Electronic GmbH / TT	Page 17 of 28
Filename: ZU_POWER.SchDoc		

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A

A

B

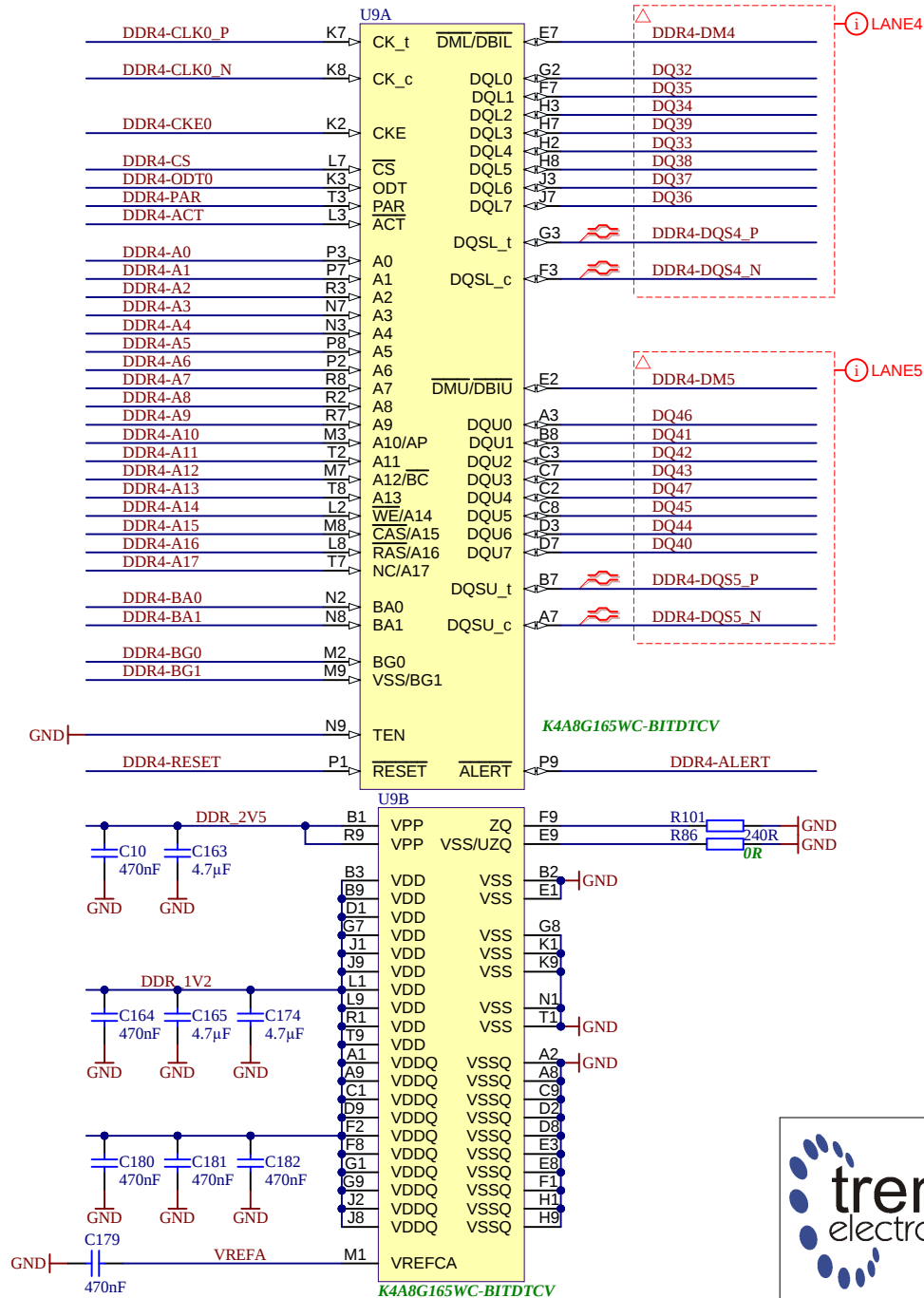
B

C

C

D

D



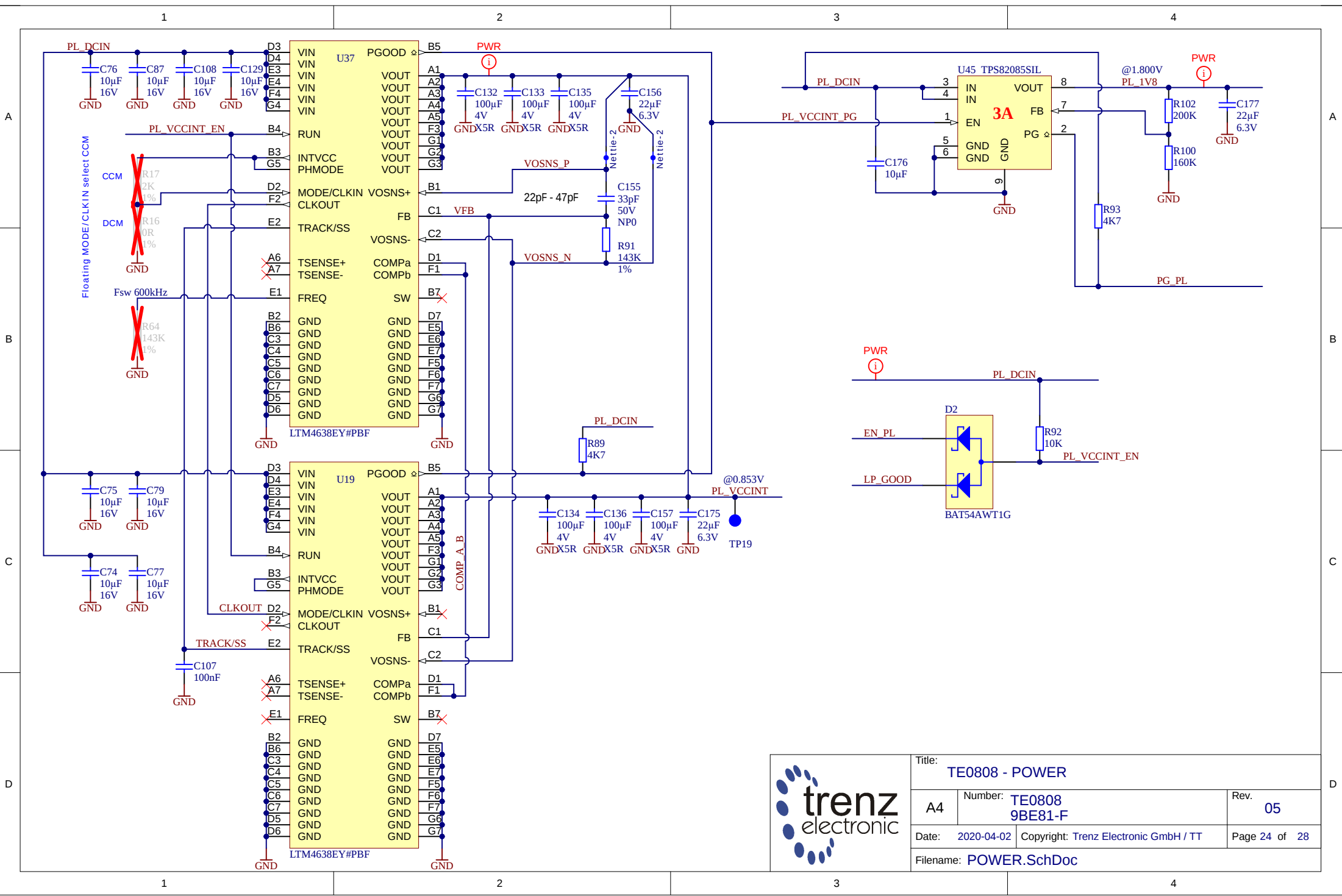
Title: TE0808 - DDR4_3_RAM		
A4	Number: TE0808 9BE81-F	Rev. 05
Date: 2020-04-02	Copyright: Trenz Electronic GmbH / TT	Page 20 of 28
Filename: DDR4-RAM_3.SchDoc		

1

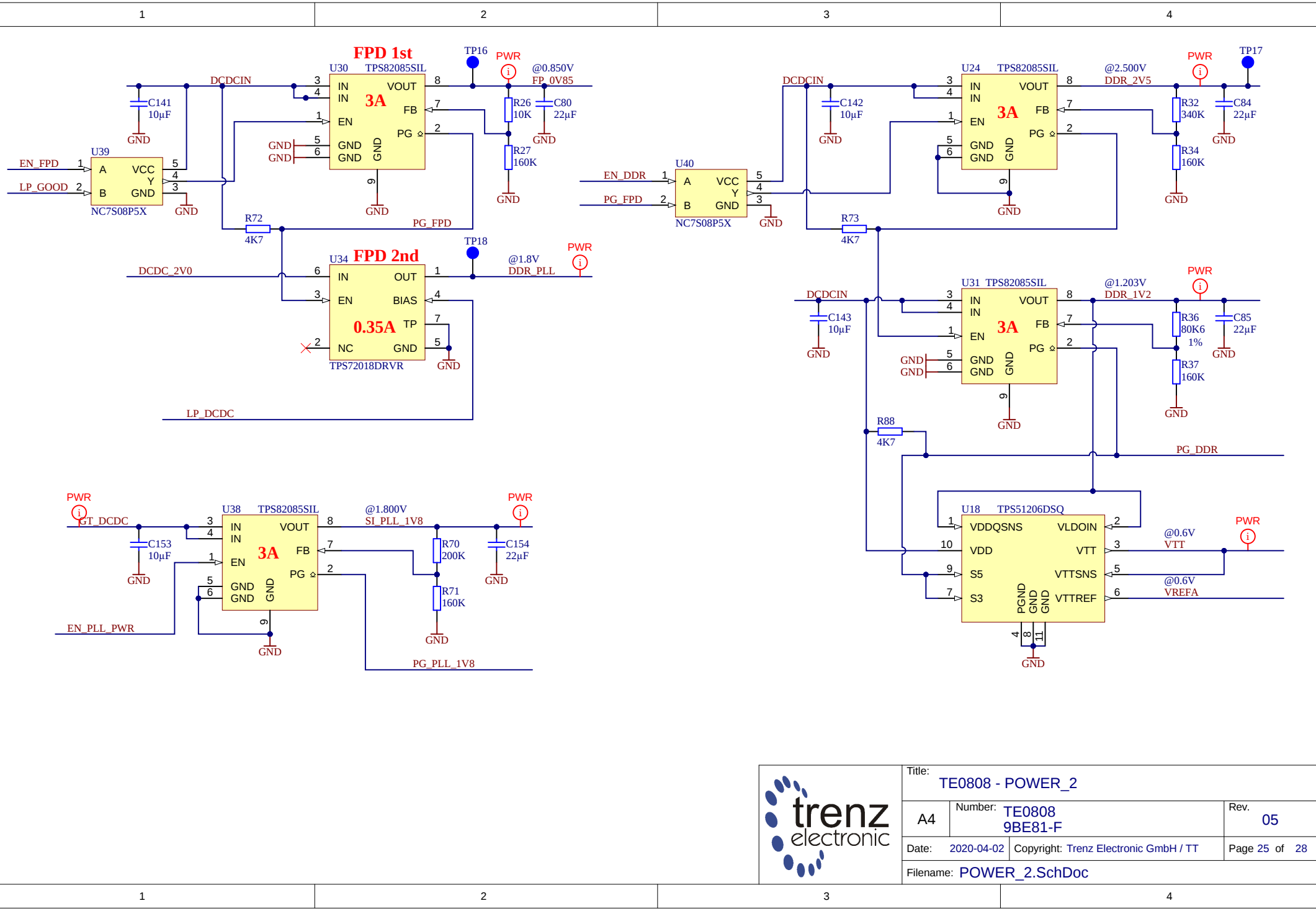
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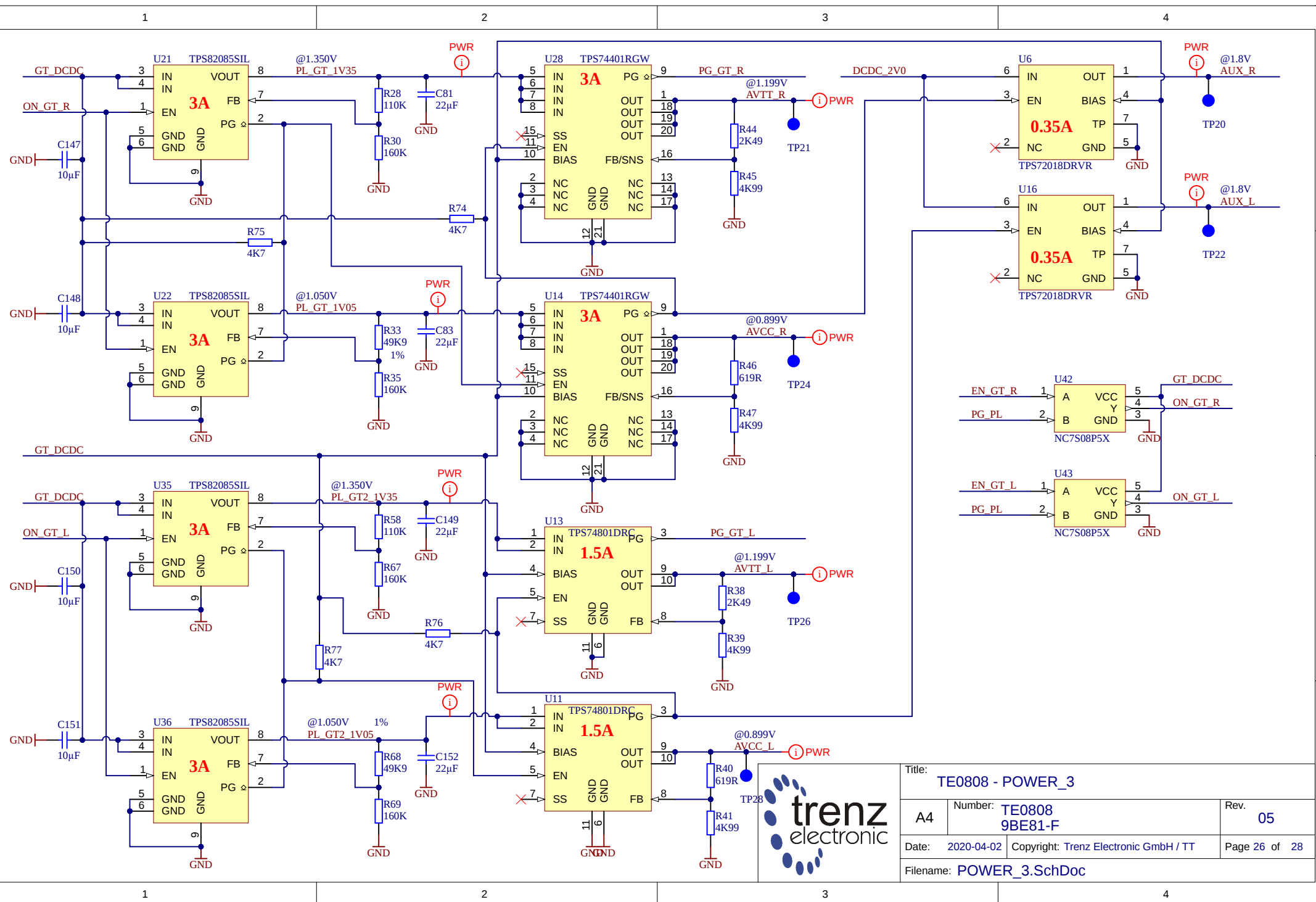
3

4

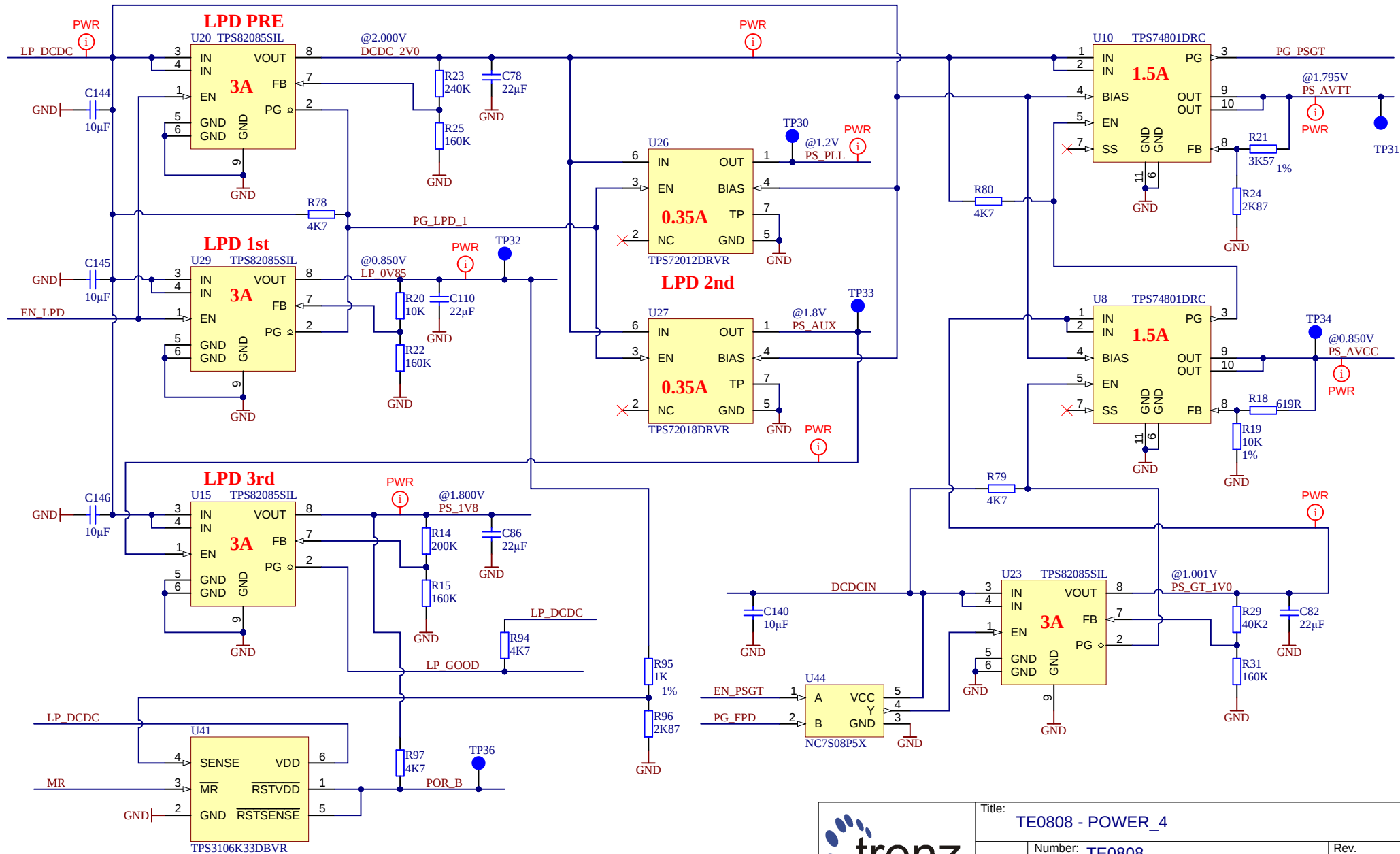


Title: TE0808 - POWER		
A4	Number: TE0808 9BE81-F	Rev. 05
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Title: TE0808 - POWER_3		
A4	Number: TE0808 9BE81-F	Rev. 05
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POR_B is "0" when LP_OV85 lower 0.74V



Title: TE0808 - POWER_4		
A4	Number: TE0808 9BE81-F	Rev. 05
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Filename: POWER_4.SchDoc		Page 27 of 28

	1	2	3	4	
A	Revision 04: 1. Added resistors R65,R82,R86,R87 (240R) for each DDR4-chip for supporting DDP DDR4 chips 2. Full library update Revision 04a (09.06.2020): 1. VY: R19 value was changed to 10K (was: 4K99) to set PS_MGTRAVCC 0.85V Revision 05: 1. VY: revised PL_VCCINT power supply. PCB: revised routing and components placement; 2. VY: added signal BG1 for DDP DDR4 IC. Added support of new packages. PCB: revised routing and components placement; 3. VY: R5 pulled up to 1.2V. R5 value changed to 49.9R; 4. VY: added test points; 5. VY: added MAC EEPROM U48. I2C bus PLL_SCL/SDA. Added pull up resistor to I2C bus; 6. VY: PCB - revised FPGA location. Package placed 1.5mm closer to connector J3; 7. VY: PCB - updated silkscreen. Added company address, CE and WEEE symbols; 8. VY: PCB - updated signal trace lengths. 29.11.2022 9. Chnaged note near J2.97 and net PG_GT_L from "On board pull-up R" to "External pull-up R Required" 30.04.2025 10. MT: L1-L5, L7 (obsolete) changed to MPZ0603S121HT000				A
B					B
C					C
D	 Title:TE0808 - Changes list A4 Number: TE08089BE81-F Rev.05 Date: 2020-04-02 Copyright: Trenz Electronic GmbH Page 28 of 28 Filename: Revision_Changes.SchDoc				D
	1	2	3	4	