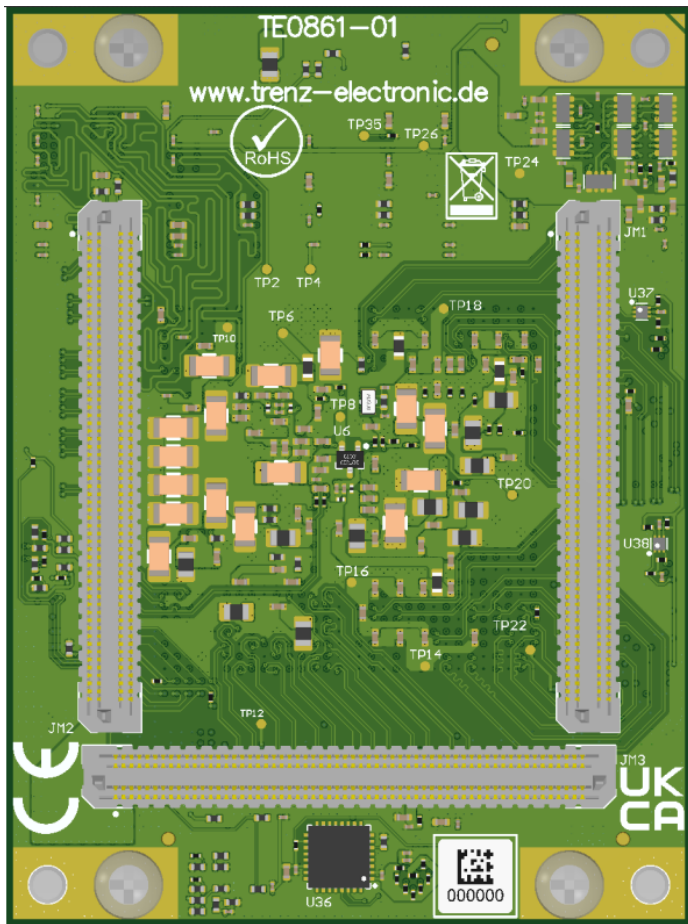
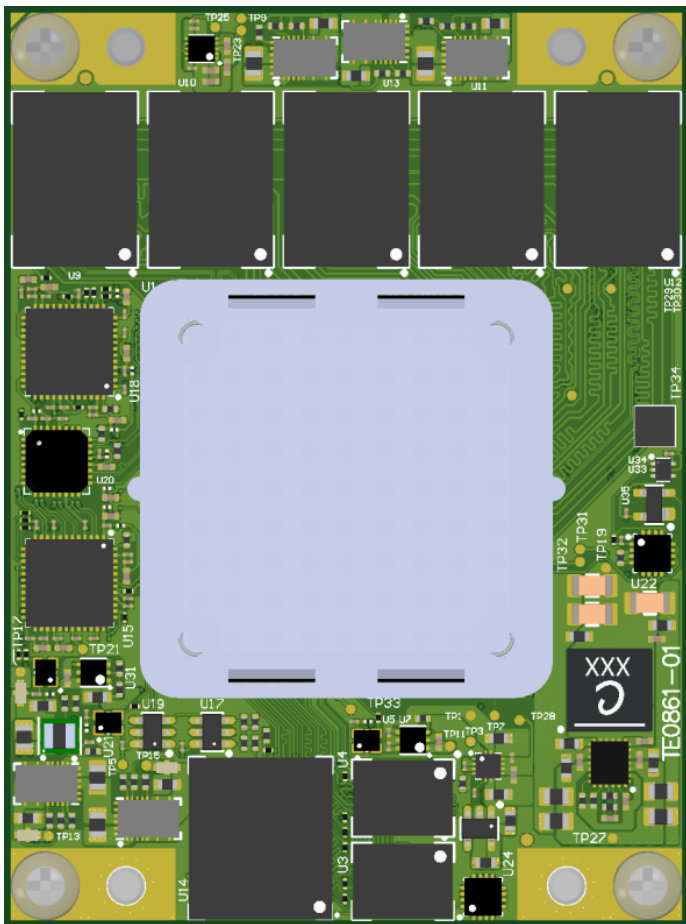




Regarding the usage of our schematics and alike documentation for Trenz module TE0861.  
 Project is protected under copyright and we strongly and strictly prohibit the reverse engineering or recreation, even if the design is just adapted or modified.  
 TE0861 is protected under such right and in case of plagiarism we will have to do anything necessary in order to protect our assets.  
 Schematics and other handouts serve for informational purposes only!

|  |                                               |                                         |                            |
|--|-----------------------------------------------|-----------------------------------------|----------------------------|
|  | Title: <b>TE0861 – Legal Notices Modules</b>  |                                         |                            |
|  | A4                                            | Number: <b>TE0861<br/>6BE61QA</b>       | Rev. <b>01</b>             |
|  | Date: <b>20.11.2025</b>                       | Copyright: <b>Trenz Electronic GmbH</b> | Page <b>1</b> of <b>35</b> |
|  | Filename: <b>Legal Notices Modules.SchDoc</b> |                                         |                            |

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A

A

B

B

C

C

D

D

|     |                  |       |
|-----|------------------|-------|
| REV | Description      |       |
| -01 | Initial revision | ED/VY |

|                                                                                       |  |                                   |              |
|---------------------------------------------------------------------------------------|--|-----------------------------------|--------------|
|  |  | Title: TE0861 – Revision Changes  |              |
|                                                                                       |  | A4                                | Rev. 01      |
|                                                                                       |  | Date: 20.11.2025                  | Page 2 of 35 |
|                                                                                       |  | Filename: Revision_Changes.SchDoc |              |

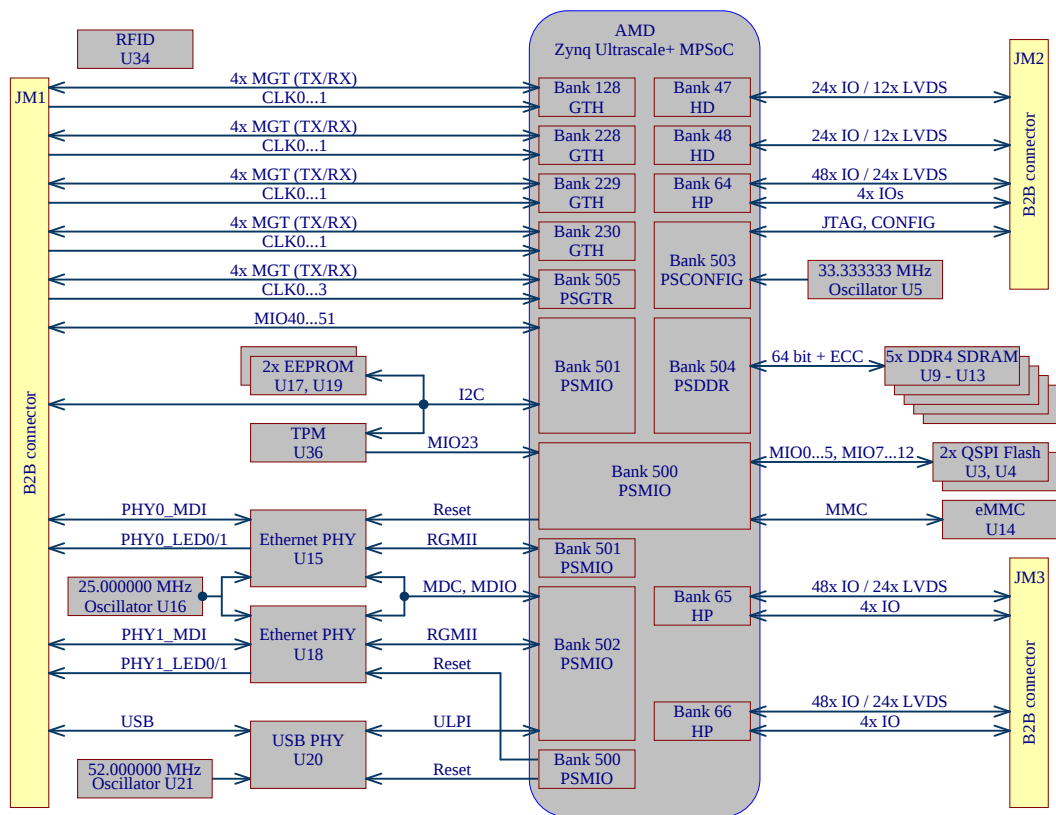
1

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## System Overview



## Supported Voltage Ranges:

| Power Rail | Direction | Range           | Tolerance | Description              | Note |
|------------|-----------|-----------------|-----------|--------------------------|------|
| VIN        | IN        | 5 V - 12 V      | +/- 10 %  | Module main power supply | -    |
| VCCO_47    | IN        | 1.2 V - 3.3 V   | 1)        | HD IO Bank 47            | -    |
| VCCO_48    | IN        | 1.2 V - 3.3 V   | 1)        | HD IO Bank 48            | -    |
| VCCO_64    | IN        | 1.0 V ... 1.8 V | +/- 5 %   | HP IO Bank 64            | -    |
| VCCO_65    | IN        | 1.0 V ... 1.8 V | +/- 5 %   | HP IO Bank 65            | -    |
| VCCO_66    | IN        | 1.0 V ... 1.8 V | +/- 5 %   | HP IO Bank 66            | -    |
| PSBATT     | IN        | 1.2 V - 1.5 V   | -         | RTC / BBRAM              | -    |
| 3V3        | OUT       | 3.3 V           | +/- 3 %   | Periphery supply         | -    |
| 1V8        | OUT       | 1.8 V           | +/- 3 %   | Periphery supply         | -    |

1) +/- 5 %, - 5 % / +3 % for 3.3 V

## I2C Addresses:

| Device            | Address | Note                          |
|-------------------|---------|-------------------------------|
| MPSoC <b>U1H</b>  | -       | AMD Zynq Ultrascale+ MPSoC PS |
| B2B <b>JM1A</b>   | -       | B2B connector                 |
| TPM <b>U36</b>    | 0x2E    | Trusted Platform Modul        |
| EEPROM <b>U17</b> | 0x50    | MAC-EEPROM with user area     |
| EEPROM <b>U19</b> | 0x51    | MAC-EEPROM with user area     |

U\_DDR4-TERM  
DDR4-TERM.SchDoc

U\_ZU\_POWER  
ZU\_POWER.SchDoc

U\_ZU\_PS\_POWER  
ZU\_PS\_POWER.SchDoc

U\_POWER\_1  
POWER\_1.SchDoc

U\_POWER\_2  
POWER\_2.SchDoc

U\_POWER\_3  
POWER\_3.SchDoc

U\_POWER\_4  
POWER\_4.SchDoc



Title:

TE0861 – System Overview

A4

Number: **TE0861**  
**6BE61QA**

Rev.  
**01**

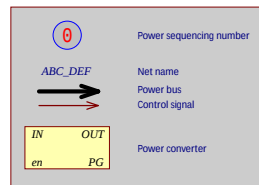
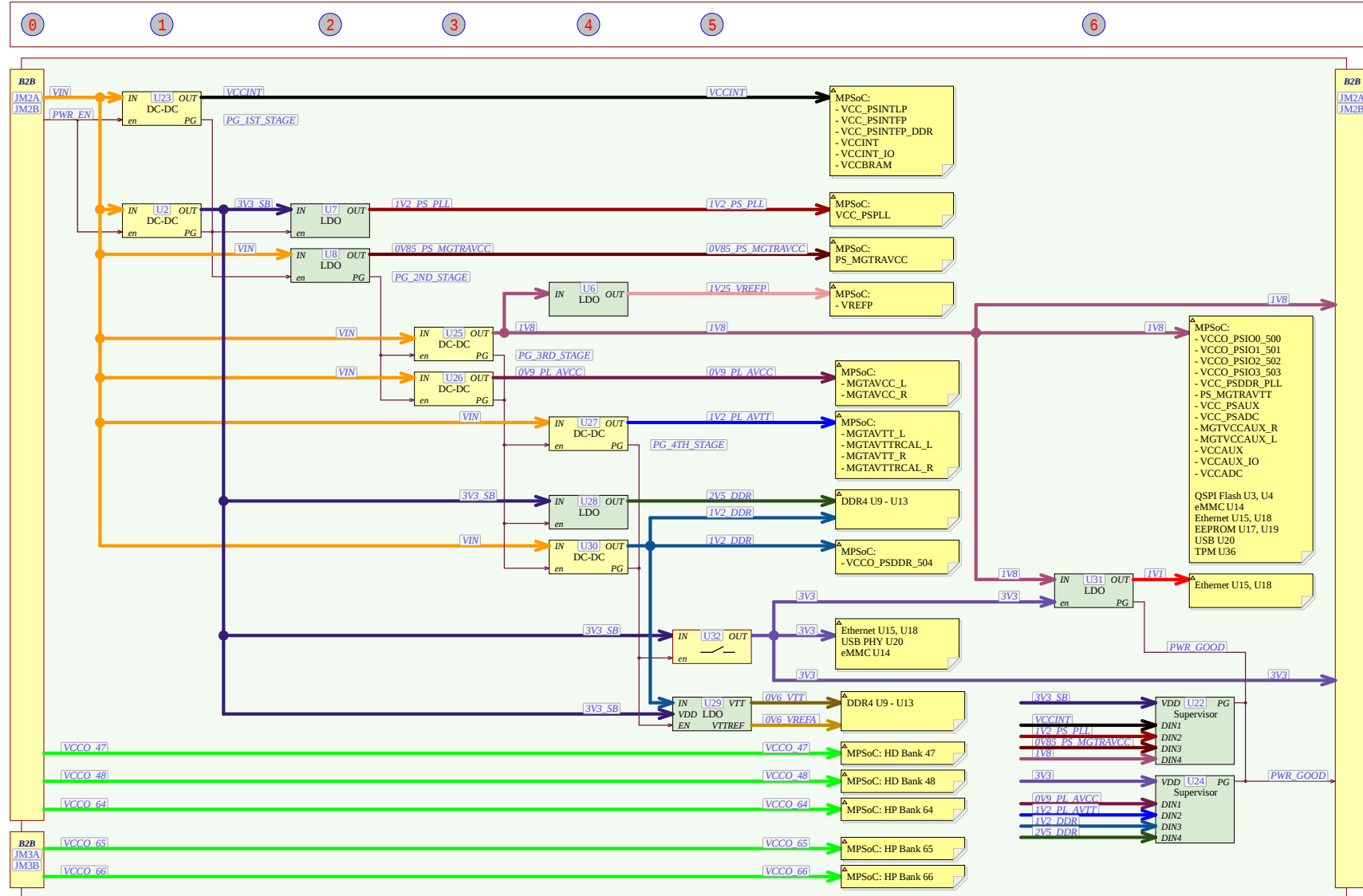
Date: 20.11.2025

Copyright: Trenz Electronic GmbH

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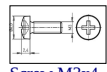
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# Power Supply and Power Sequencing

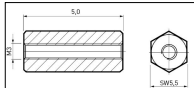


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| A3                                     | Number: <b>TE0861<br/>6BE61QA</b> | Rev. <b>01</b> |
| Datum: 20.11.2025                      | Copyright: Trenz Electronic GmbH  | Page 4 of 35   |
| Filename: <b>POWER_Overview.SchDoc</b> |                                   |                |

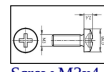
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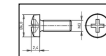
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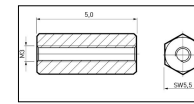
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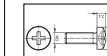
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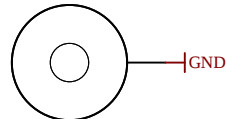
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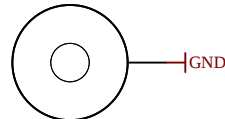
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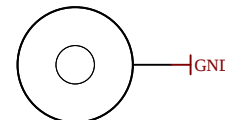
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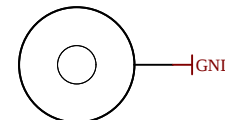
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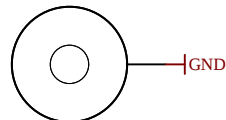
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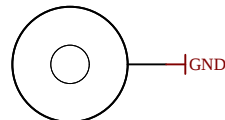
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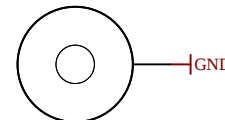
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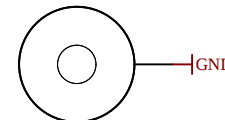
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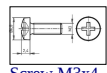
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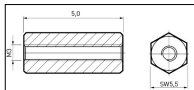
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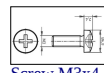
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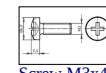
Screw M3x4



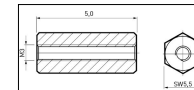
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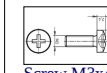
Screw M3x4



Screw M3x4



Distance Holder M3x5 Male/Male



Screw M3x4

PM1



FIDU-DOT - mini  
PM3

PM2



FIDU-DOT - mini  
PM4



FIDU-DOT - mini  
PM5



FIDU-DOT - mini  
PM6



FIDU-DOT - mini



FIDU-DOT - mini



906-31-2-23-2-B-0



LXMS33HCNG-134

Design drawn by: ED  
Checked by: MR  
Assembly variant: 6BE61QA  
Created by: ED  
Modified by: ED  
Modified at: 2025-10-01



Title:  
TE0861

A4

Number: TE0861  
6BE61QA

Rev.  
01

Date: 20.11.2025 Copyright: Trenz Electronic GmbH

Page 5 of 35

Filename: TE0861.SchDoc

Serial1  
Serial  
Serialnumber 6,3 x 6.3mm  
RoHS1

RoHS Logo on Top Overlay

RoHS-TOOVERLAY  
WEEE1

WEEE Logo on Top Overlay

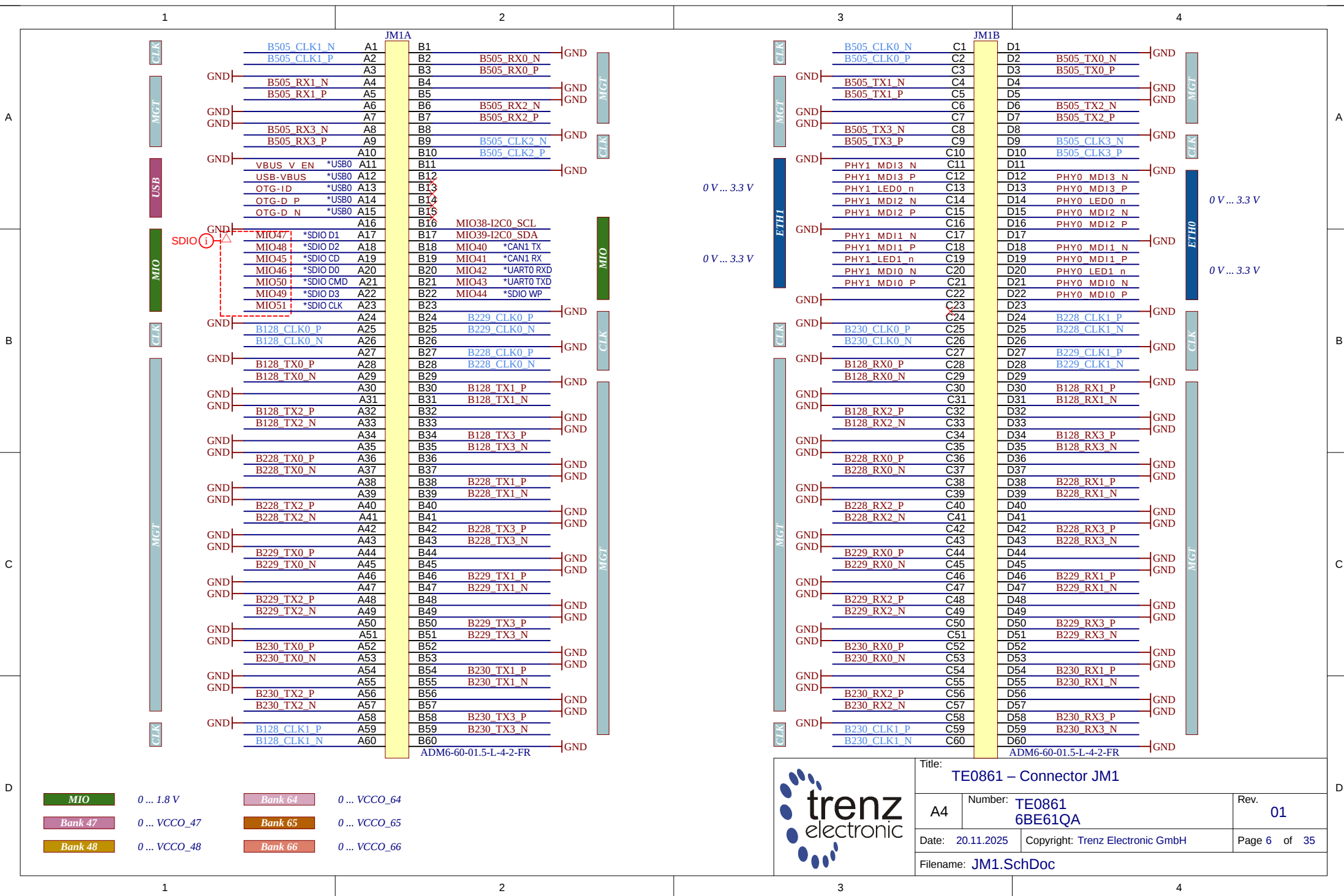
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CE1

CE Logo on Top Overlay

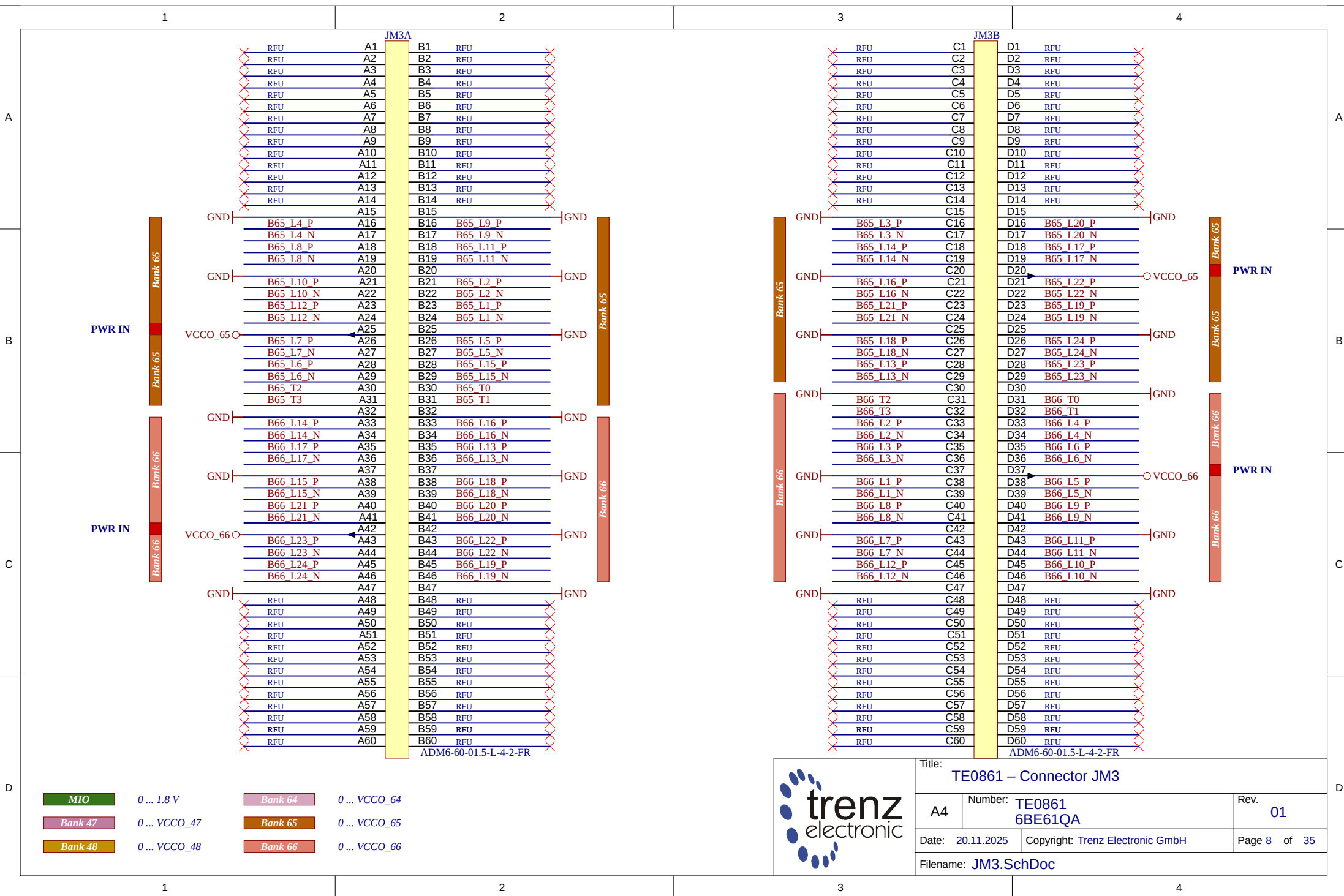
CE-TOOVERLAY  
UKCA1

UKCA Logo on Top Overlay

UKCA-TOOVERLAY





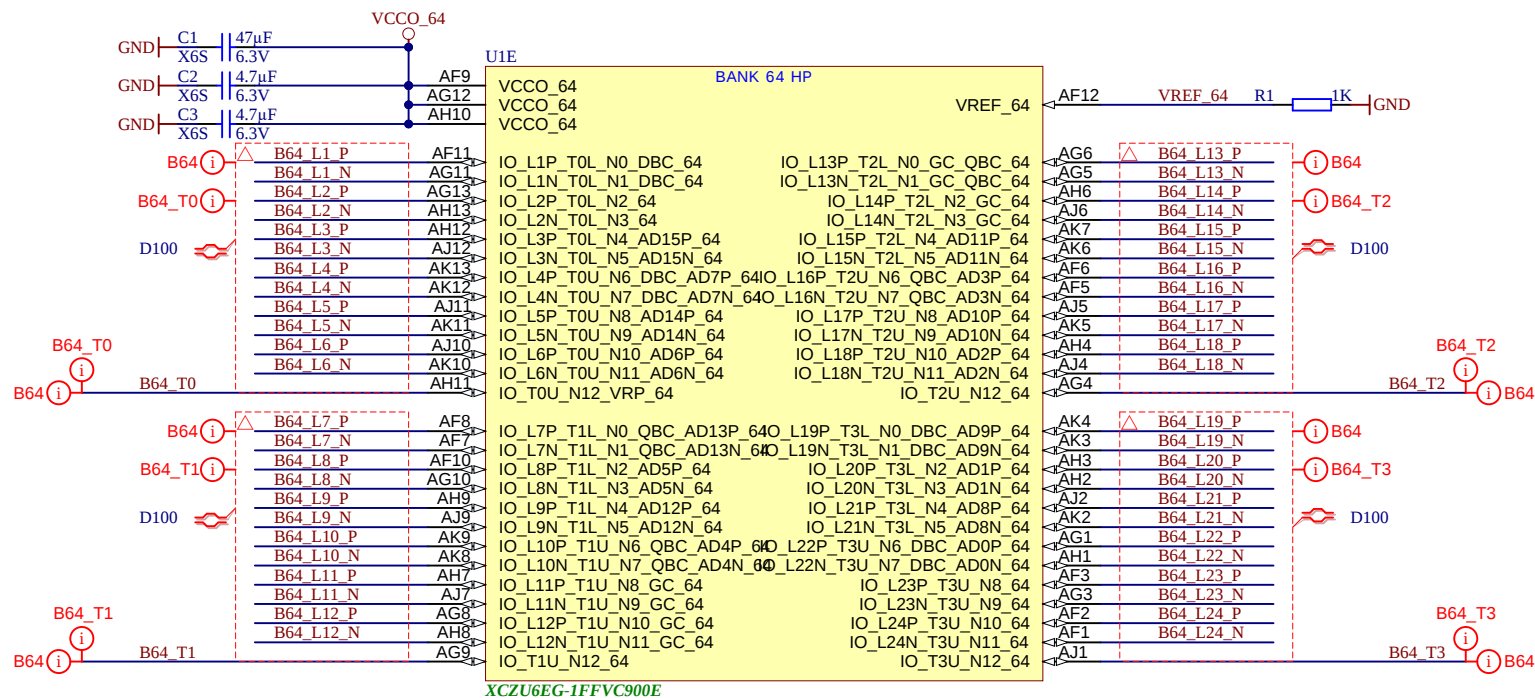


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XCZU6EG-1FFVC900E



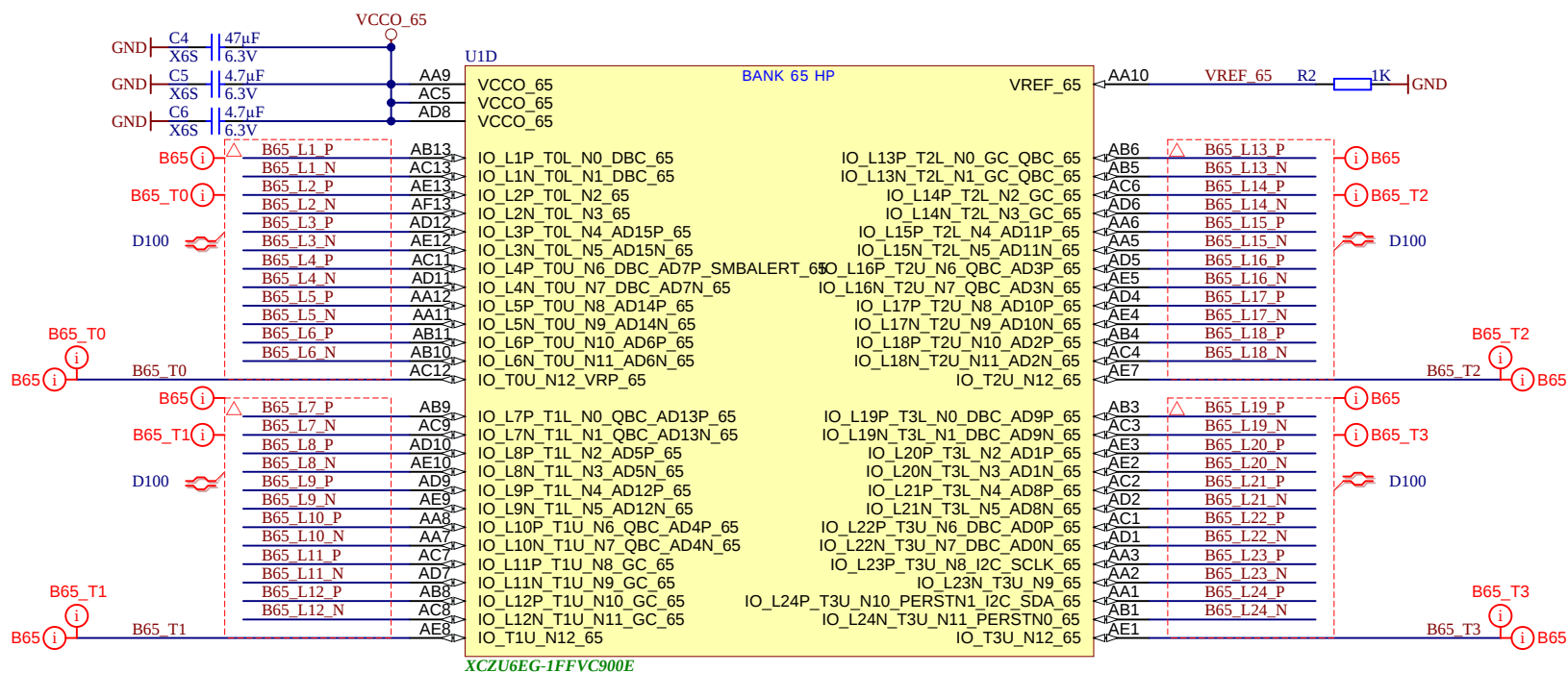
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| A4                   | Number: TE0861<br>6BE61QA        | Rev. 01      |
| Date: 20.11.2025     | Copyright: Trenz Electronic GmbH | Page 9 of 35 |
| Filename: B64.SchDoc |                                  |              |

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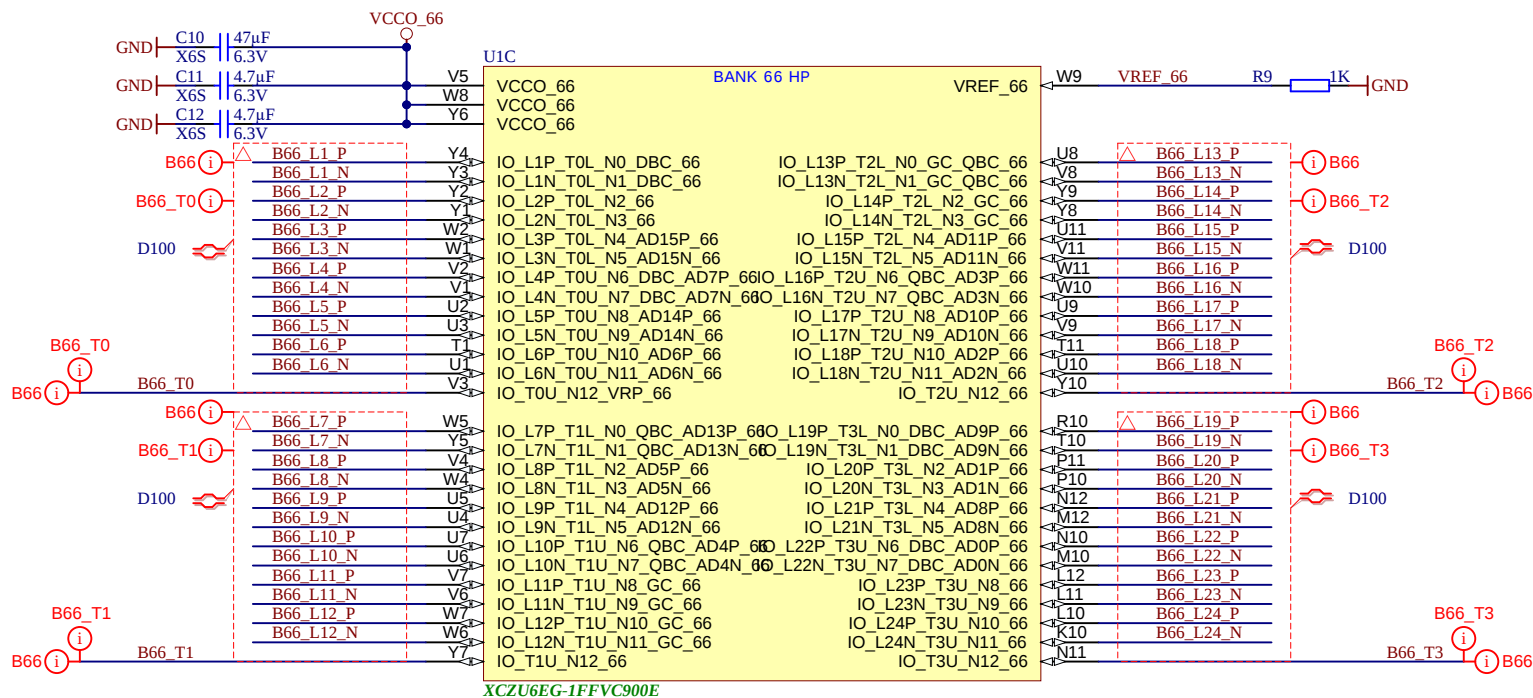
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| A4                   | Number: TE0861 6BE61QA           | Rev. 01       |
| Date: 20.11.2025     | Copyright: Trenz Electronic GmbH | Page 10 of 35 |
| Filename: B65.SchDoc |                                  |               |

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|                      |                                  |               |
|----------------------|----------------------------------|---------------|
| Title: TE0861 – B66  |                                  |               |
| A4                   | Number: TE0861 6BE61QA           | Rev. 01       |
| Date: 20.11.2025     | Copyright: Trenz Electronic GmbH | Page 11 of 35 |
| Filename: B66.SchDoc |                                  |               |

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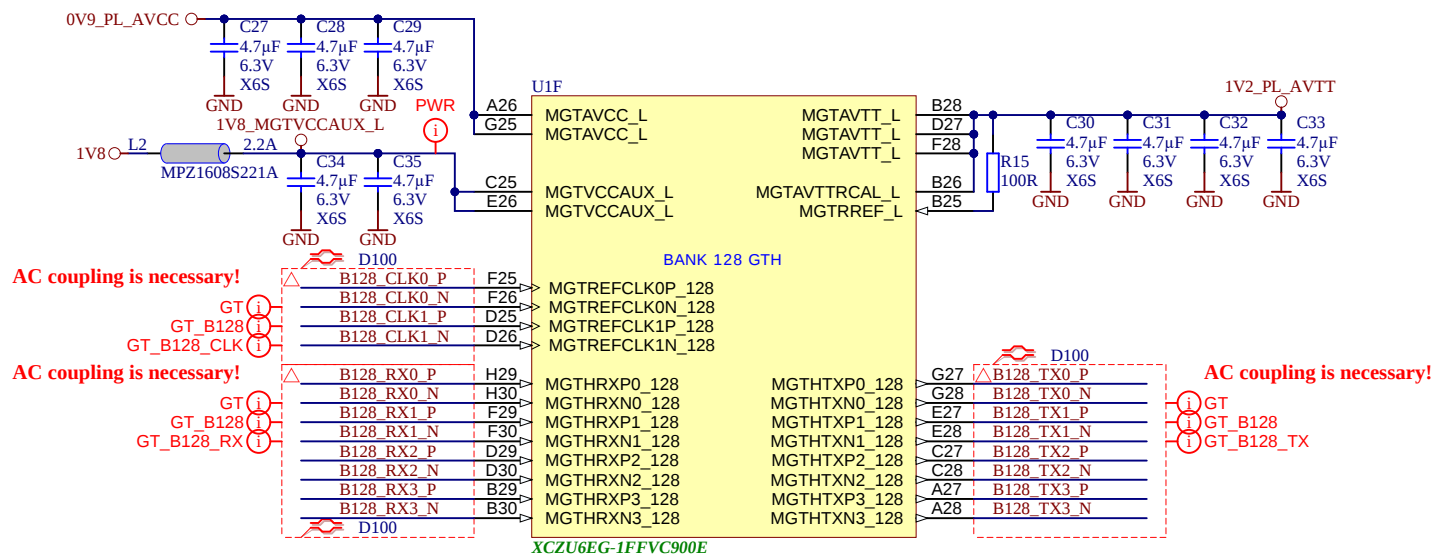


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|                         |                                  |               |
|-------------------------|----------------------------------|---------------|
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| A4                      | Number: TE0861 6BE1QA            | Rev. 01       |
| Date: 20.11.2025        | Copyright: Trenz Electronic GmbH | Page 14 of 35 |
| Filename: B_GT_L.SchDoc |                                  |               |

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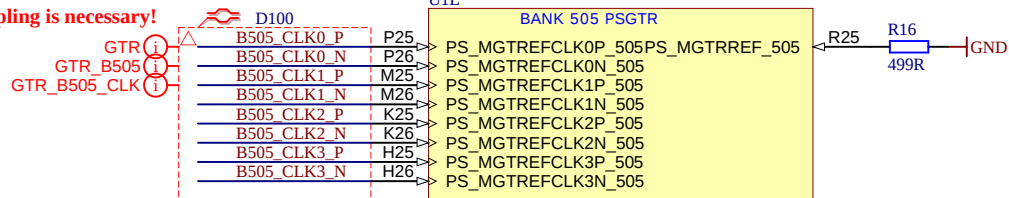
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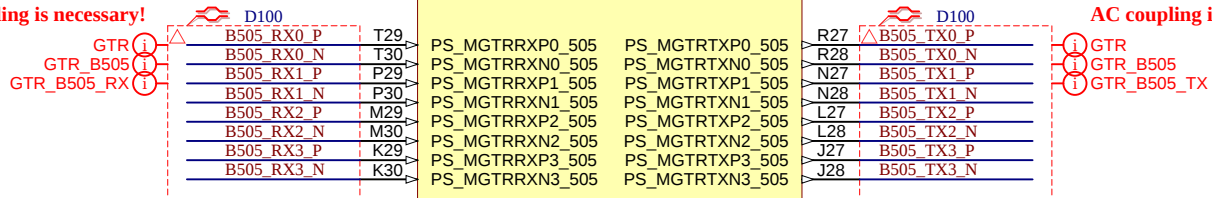
4



AC coupling is necessary!



AC coupling is necessary!



AC coupling is necessary!

GTR  
GTR\_B505  
GTR\_B505\_TX

XCZU6EG-1FFVC900E



Title: TE0861 – B\_PS\_GT

|                          |                                  |               |
|--------------------------|----------------------------------|---------------|
| A4                       | Number: TE0861<br>6BE61QA        | Rev. 01       |
| Date: 20.11.2025         | Copyright: Trenz Electronic GmbH | Page 16 of 35 |
| Filename: B_PS_GT.SchDoc |                                  |               |

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A

A

B

B

C

C

D

D

U1J

BANK 504 PSDDR

VCCO\_PSDDR\_504 PS\_DDR\_CK0  
VCCO\_PSDDR\_504 PS\_DDR\_CK\_N0  
VCCO\_PSDDR\_504 PS\_DDR\_CKE0  
VCCO\_PSDDR\_504 PS\_DDR\_CK1  
VCCO\_PSDDR\_504 PS\_DDR\_CK\_N1  
VCCO\_PSDDR\_504 PS\_DDR\_CKE1

VCC\_PSDDR\_PLL PS\_DDR\_A0  
VCC\_PSDDR\_PLL PS\_DDR\_A1  
VCC\_PSDDR\_PLL PS\_DDR\_A2  
VCC\_PSDDR\_PLL PS\_DDR\_A3  
VCC\_PSDDR\_PLL PS\_DDR\_A4  
VCC\_PSDDR\_PLL PS\_DDR\_A5  
VCC\_PSDDR\_PLL PS\_DDR\_A6  
VCC\_PSDDR\_PLL PS\_DDR\_A7  
VCC\_PSDDR\_PLL PS\_DDR\_A8  
VCC\_PSDDR\_PLL PS\_DDR\_A9  
VCC\_PSDDR\_PLL PS\_DDR\_A10  
VCC\_PSDDR\_PLL PS\_DDR\_A11  
VCC\_PSDDR\_PLL PS\_DDR\_A12  
VCC\_PSDDR\_PLL PS\_DDR\_A13  
VCC\_PSDDR\_PLL PS\_DDR\_A14  
VCC\_PSDDR\_PLL PS\_DDR\_A15  
VCC\_PSDDR\_PLL PS\_DDR\_A16  
VCC\_PSDDR\_PLL PS\_DDR\_A17

VCC\_PSINTFP\_DDR PS\_DDR\_CS\_N0  
VCC\_PSINTFP\_DDR PS\_DDR\_CS\_N1  
VCC\_PSINTFP\_DDR PS\_DDR\_BA0  
VCC\_PSINTFP\_DDR PS\_DDR\_BA1  
VCC\_PSINTFP\_DDR PS\_DDR\_BG0  
VCC\_PSINTFP\_DDR PS\_DDR\_BG1  
VCC\_PSINTFP\_DDR PS\_DDR\_PARITY  
VCC\_PSINTFP\_DDR PS\_DDR\_RAM\_RST\_N  
VCC\_PSINTFP\_DDR PS\_DDR\_ACT\_N  
VCC\_PSINTFP\_DDR PS\_DDR\_ALERT\_N  
VCC\_PSINTFP\_DDR PS\_DDR\_ZQ  
VCC\_PSINTFP\_DDR PS\_DDR\_ODT0  
VCC\_PSINTFP\_DDR PS\_DDR\_ODT1

XCZU6EG-1FFVC900E

AJ26 DDR4-CLK0\_P  
AJ27 DDR4-CLK0\_N  
AJ25 DDR4-CKE0

AG23  
AG24  
AH27

AK25 DDR4-A0  
AK28 DDR4-A1  
AK27 DDR4-A2  
AH24 DDR4-A3  
AK24 DDR4-A4  
AJ30 DDR4-A5  
AE22 DDR4-A6  
AE25 DDR4-A7  
AE23 DDR4-A8  
AE24 DDR4-A9  
AD22 DDR4-A10  
AH23 DDR4-A11  
AF21 DDR4-A12  
AG21 DDR4-A13  
AF22 DDR4-A14  
AF23 DDR4-A15  
AD21 DDR4-A16  
AD20 DDR4-A17

AJ24 DDR4-CS  
AG25

AG26 DDR4-BA0  
AF25 DDR4-BA1

AF26 DDR4-BG0  
AD26 DDR4-BG1

AC14 DDR4-PAR  
AC16 DDR4-RESET  
AC17 DDR4-ACT  
AD19 DDR4-ALERT

AC23 R17 240R GND

AJ29 DDR4-ODT0  
AH26

DQ0  
DQ1  
DQ2  
DQ3  
DQ4  
DQ5  
DQ6  
DQ7  
DQ8  
DQ9  
DQ10  
DQ11  
DQ12  
DQ13  
DQ14  
DQ15  
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DQ18  
DQ19  
DQ20  
DQ21  
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DQ26  
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DQ28  
DQ29  
DQ30  
DQ31

AK17  
AH17  
AJ17  
AH16  
AK15  
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AF16  
AD14  
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AE19  
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AH18  
AG19  
AG20

PS\_DDR\_DQ0  
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PS\_DDR\_DQ29  
PS\_DDR\_DQ30  
PS\_DDR\_DQ31

DDR4-DQS0\_P  
DDR4-DQS0\_N  
DDR4-DQS1\_P  
DDR4-DQS1\_N  
DDR4-DQS2\_P  
DDR4-DQS2\_N  
DDR4-DQS3\_P  
DDR4-DQS3\_N  
DDR4-DQS4\_P  
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DDR4-DQS5\_P  
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DDR4-DQS7\_P  
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DDR4-DQS8\_N

D80

U1K

BANK 504 PSDDR

PS\_DDR\_DQ32  
PS\_DDR\_DQ33  
PS\_DDR\_DQ34  
PS\_DDR\_DQ35  
PS\_DDR\_DQ36  
PS\_DDR\_DQ37  
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PS\_DDR\_DQ71

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Y25  
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AA26  
AC26  
AD24  
AD25  
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U27  
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AA27  
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Y27  
W30  
V30  
V28  
V29  
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AF28  
AG28  
AE28  
AH30  
AF30  
AG30  
AE30

PS\_DDR\_DQ0  
PS\_DDR\_DQ1  
PS\_DDR\_DQ2  
PS\_DDR\_DQ3  
PS\_DDR\_DQ4  
PS\_DDR\_DQ5  
PS\_DDR\_DQ6  
PS\_DDR\_DQ7  
PS\_DDR\_DQ8  
PS\_DDR\_DQ9  
PS\_DDR\_DQ10  
PS\_DDR\_DQ11  
PS\_DDR\_DQ12  
PS\_DDR\_DQ13  
PS\_DDR\_DQ14  
PS\_DDR\_DQ15  
PS\_DDR\_DQ16  
PS\_DDR\_DQ17  
PS\_DDR\_DQ18  
PS\_DDR\_DQ19  
PS\_DDR\_DQ20  
PS\_DDR\_DQ21  
PS\_DDR\_DQ22  
PS\_DDR\_DQ23  
PS\_DDR\_DQ24  
PS\_DDR\_DQ25  
PS\_DDR\_DQ26  
PS\_DDR\_DQ27  
PS\_DDR\_DQ28  
PS\_DDR\_DQ29  
PS\_DDR\_DQ30  
PS\_DDR\_DQ31

PS\_DDR\_DQ32  
PS\_DDR\_DQ33  
PS\_DDR\_DQ34  
PS\_DDR\_DQ35  
PS\_DDR\_DQ36  
PS\_DDR\_DQ37  
PS\_DDR\_DQ38  
PS\_DDR\_DQ39  
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PS\_DDR\_DQ43  
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PS\_DDR\_DQ65  
PS\_DDR\_DQ66  
PS\_DDR\_DQ67  
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PS\_DDR\_DQ69  
PS\_DDR\_DQ70  
PS\_DDR\_DQ71

XCZU6EG-1FFVC900E



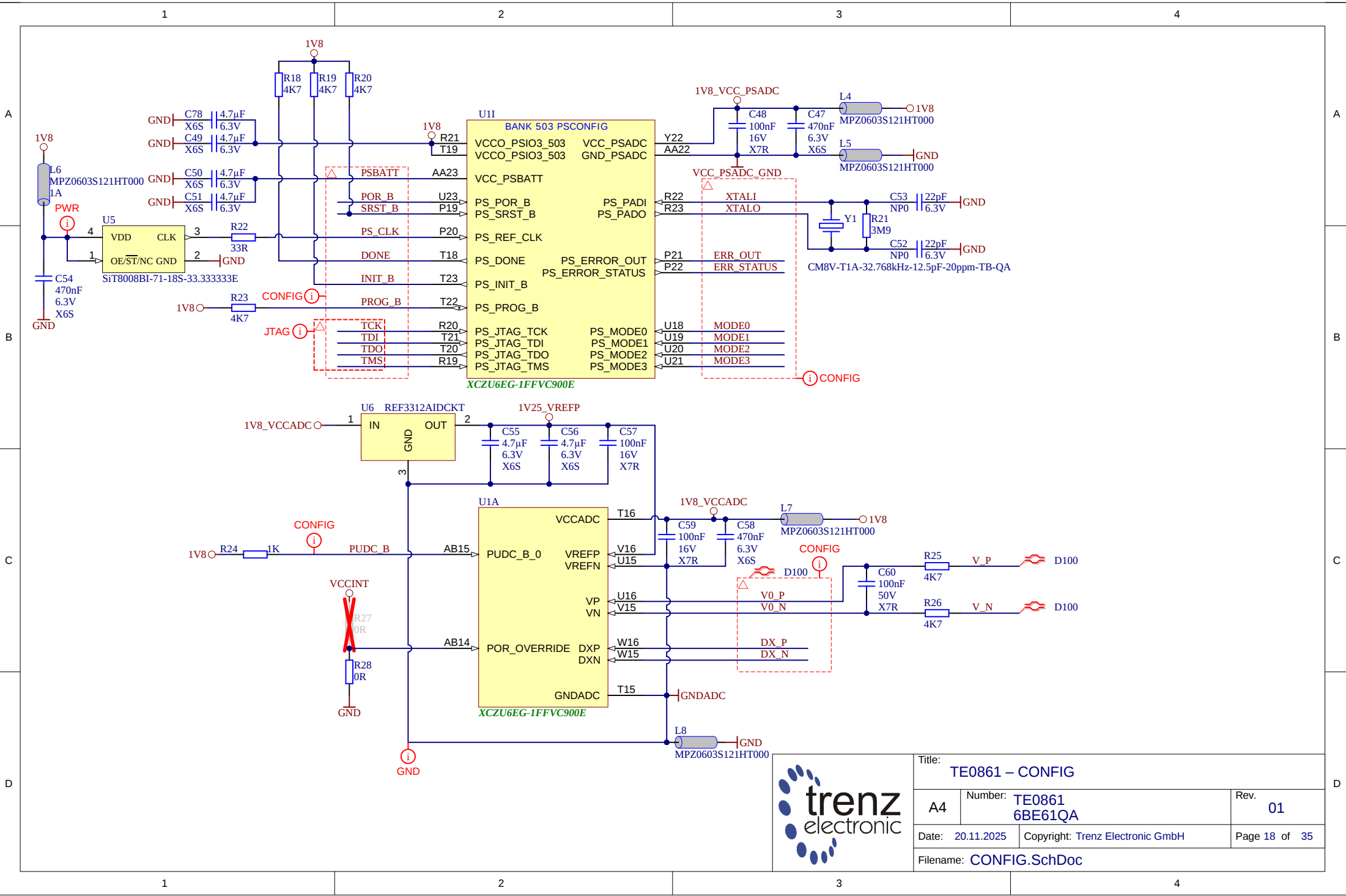
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| Title: TE0861 – PS_DDR  |                                  |               |
| A4                      | Number: TE0861 6BE61QA           | Rev. 01       |
| Date: 20.11.2025        | Copyright: Trenz Electronic GmbH | Page 17 of 35 |
| Filename: PS_DDR.SchDoc |                                  |               |

1

2

3

4



|                         |                                  |               |
|-------------------------|----------------------------------|---------------|
| Title: TE0861 – CONFIG  |                                  |               |
| A4                      | Number: TE0861<br>6BE61QA        | Rev. 01       |
| Date: 20.11.2025        | Copyright: Trenz Electronic GmbH | Page 18 of 35 |
| Filename: CONFIG.SchDoc |                                  |               |

1

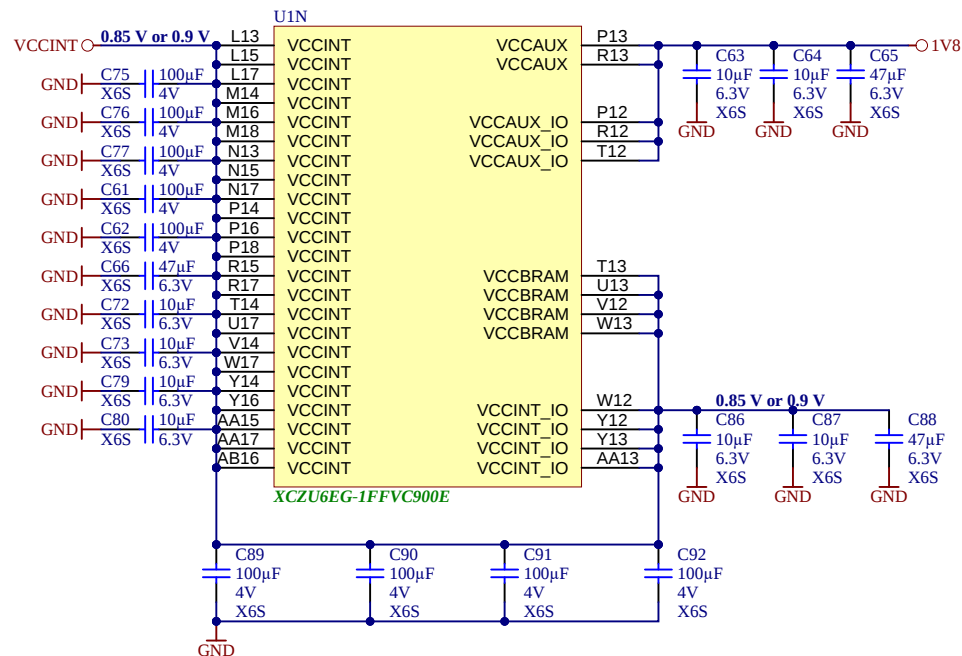
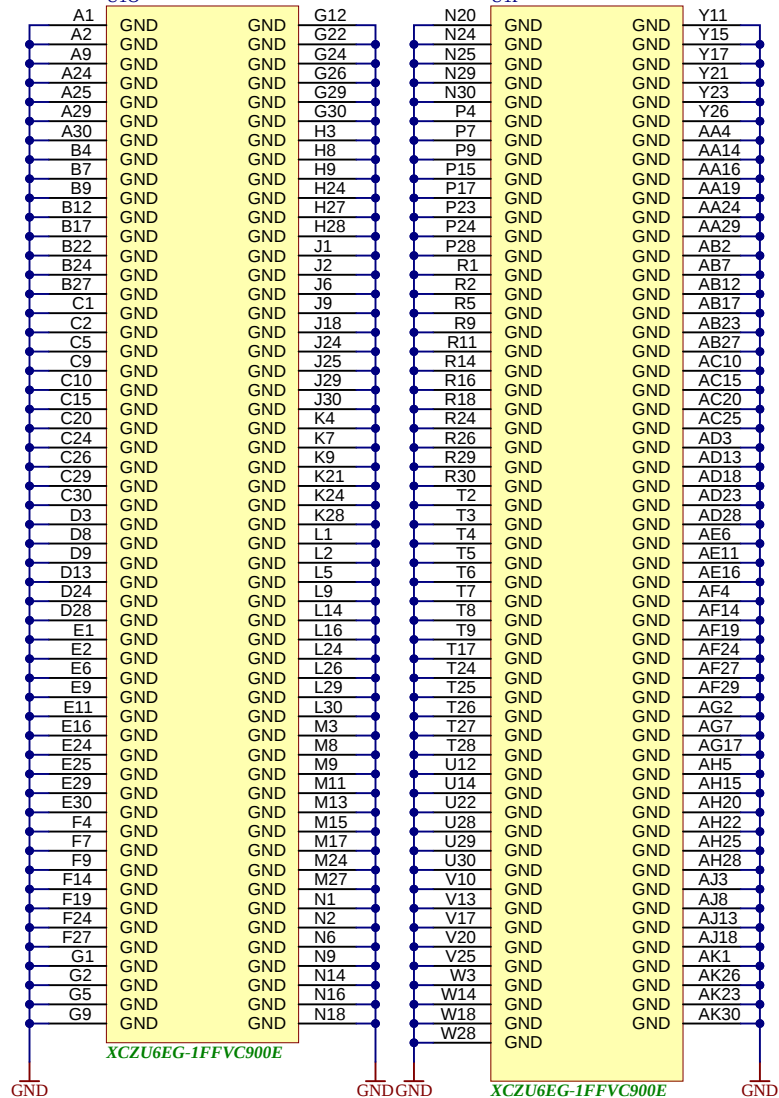
2

3

4

U10

U1P



|                           |                                  |               |
|---------------------------|----------------------------------|---------------|
| Title: TE0861 – ZU_POWER  |                                  |               |
| A4                        | Number: TE0861<br>6BE61QA        | Rev. 01       |
| Date: 20.11.2025          | Copyright: Trenz Electronic GmbH | Page 19 of 35 |
| Filename: ZU_POWER.SchDoc |                                  |               |

1

2

3

4



1

2

3

4

A

A

B

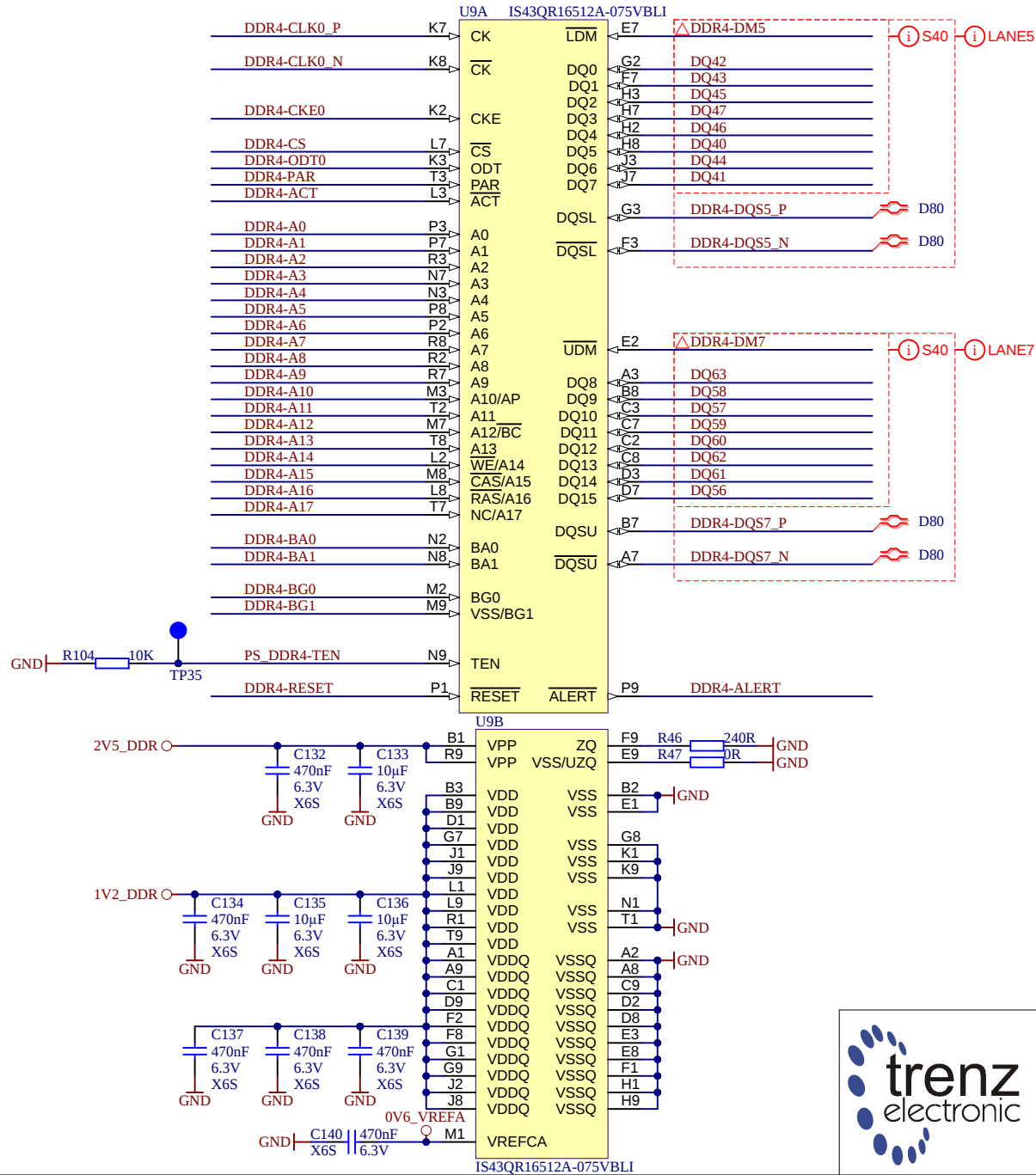
B

C

C

D

D



|                             |                                  |               |
|-----------------------------|----------------------------------|---------------|
| Title: TE0861 – DDR4-RAM_1  |                                  |               |
| A4                          | Number: TE0861 6BE61QA           | Rev. 01       |
| Date: 17.12.2025            | Copyright: Trenz Electronic GmbH | Page 21 of 35 |
| Filename: DDR4-RAM_1.SchDoc |                                  |               |

1

2

3

4

1

2

3

4

A

A

B

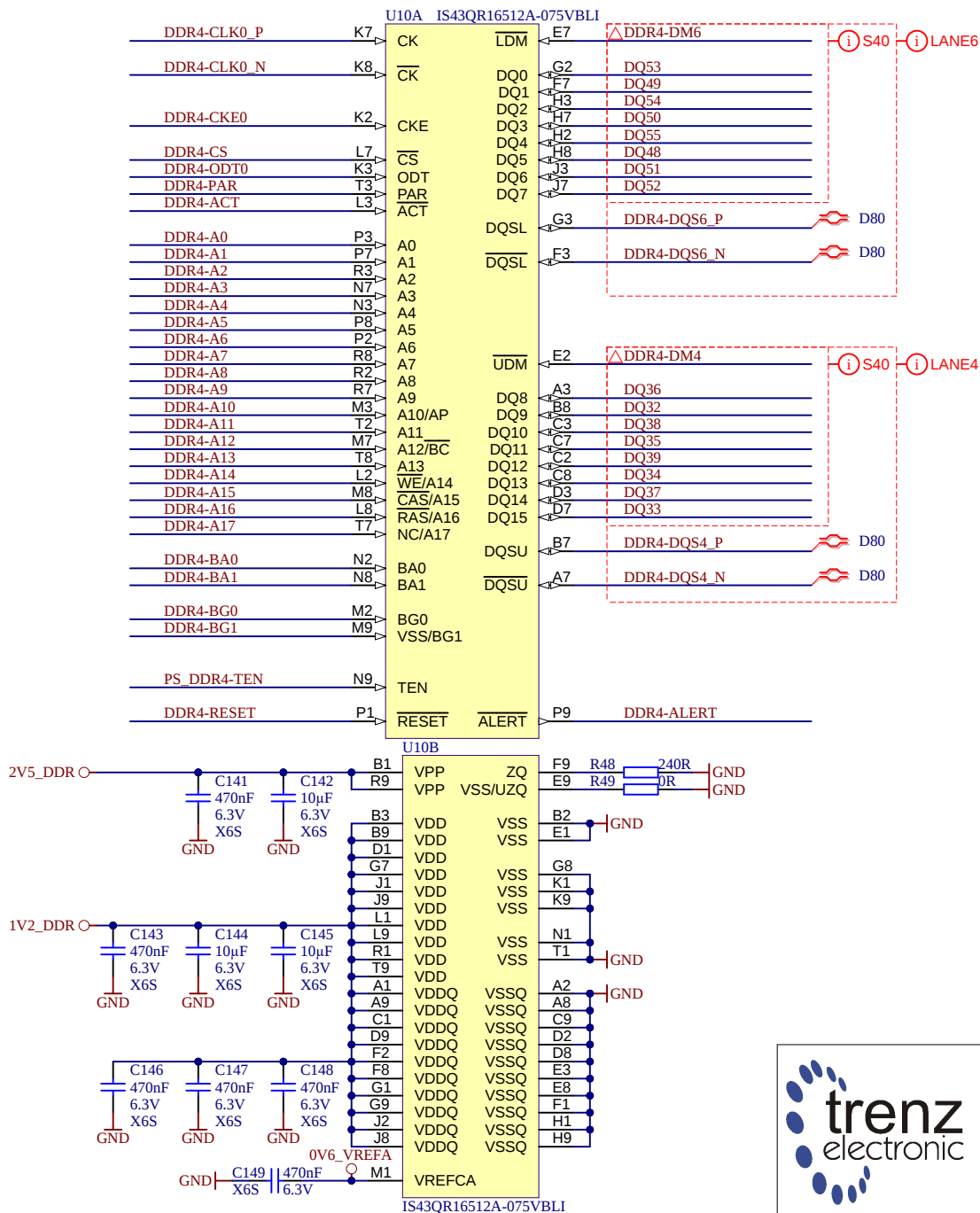
B

C

C

D

D



|                             |                                  |               |
|-----------------------------|----------------------------------|---------------|
| Title: TE0861 – DDR4-RAM_2  |                                  |               |
| A4                          | Number: TE0861 6BE61QA           | Rev. 01       |
| Date: 17.12.2025            | Copyright: Trenz Electronic GmbH | Page 22 of 35 |
| Filename: DDR4-RAM_2.SchDoc |                                  |               |

1

2

3

4



1

2

3

4

A

A

B

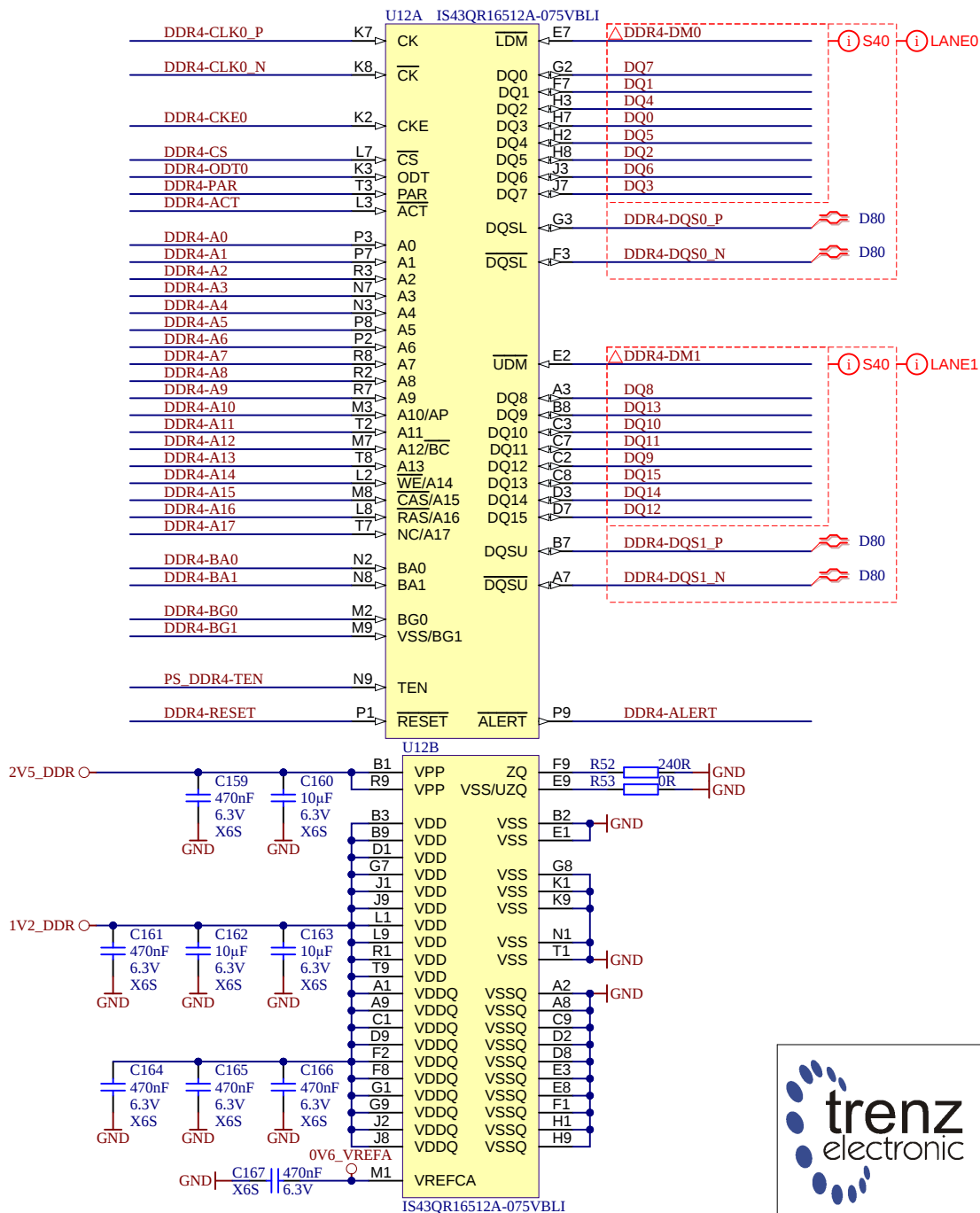
B

C

C

D

D



|                             |                                  |               |
|-----------------------------|----------------------------------|---------------|
| Title: TE0861 – DDR4-RAM_4  |                                  |               |
| A4                          | Number: TE0861 6BE61QA           | Rev. 01       |
| Date: 17.12.2025            | Copyright: Trenz Electronic GmbH | Page 24 of 35 |
| Filename: DDR4-RAM_4.SchDoc |                                  |               |

1

2

3

4

1

2

3

4

A

A

B

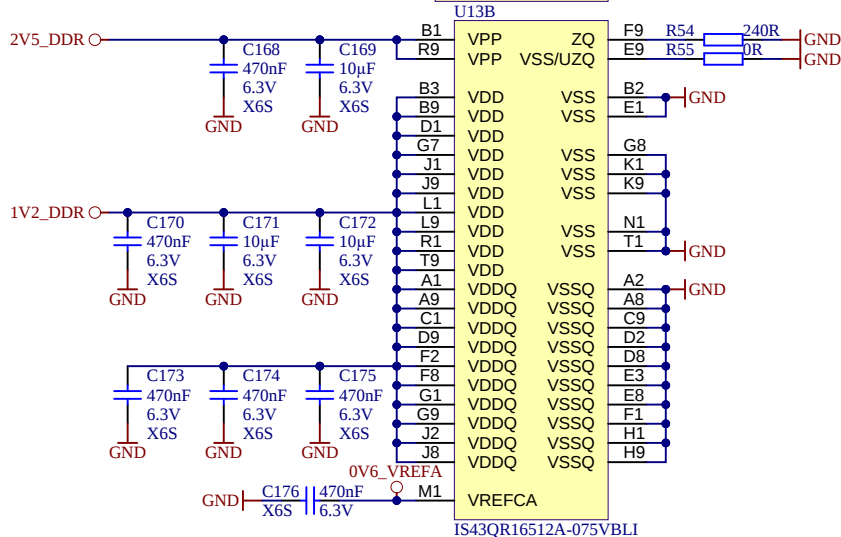
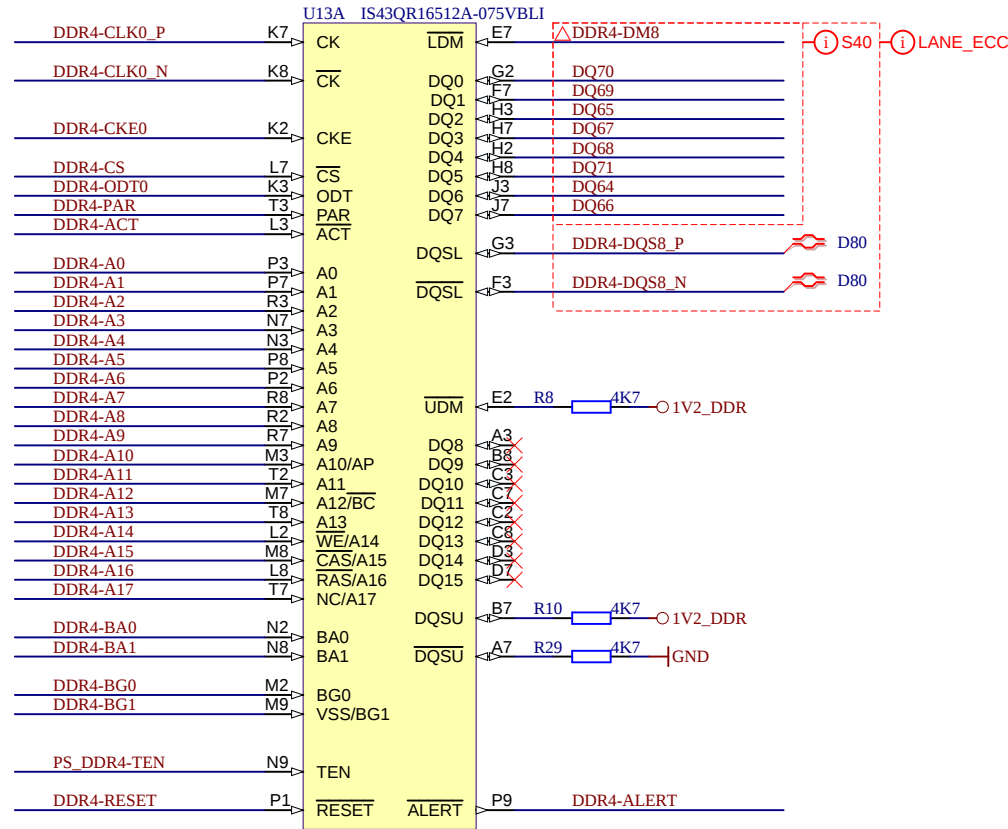
B

C

C

D

D



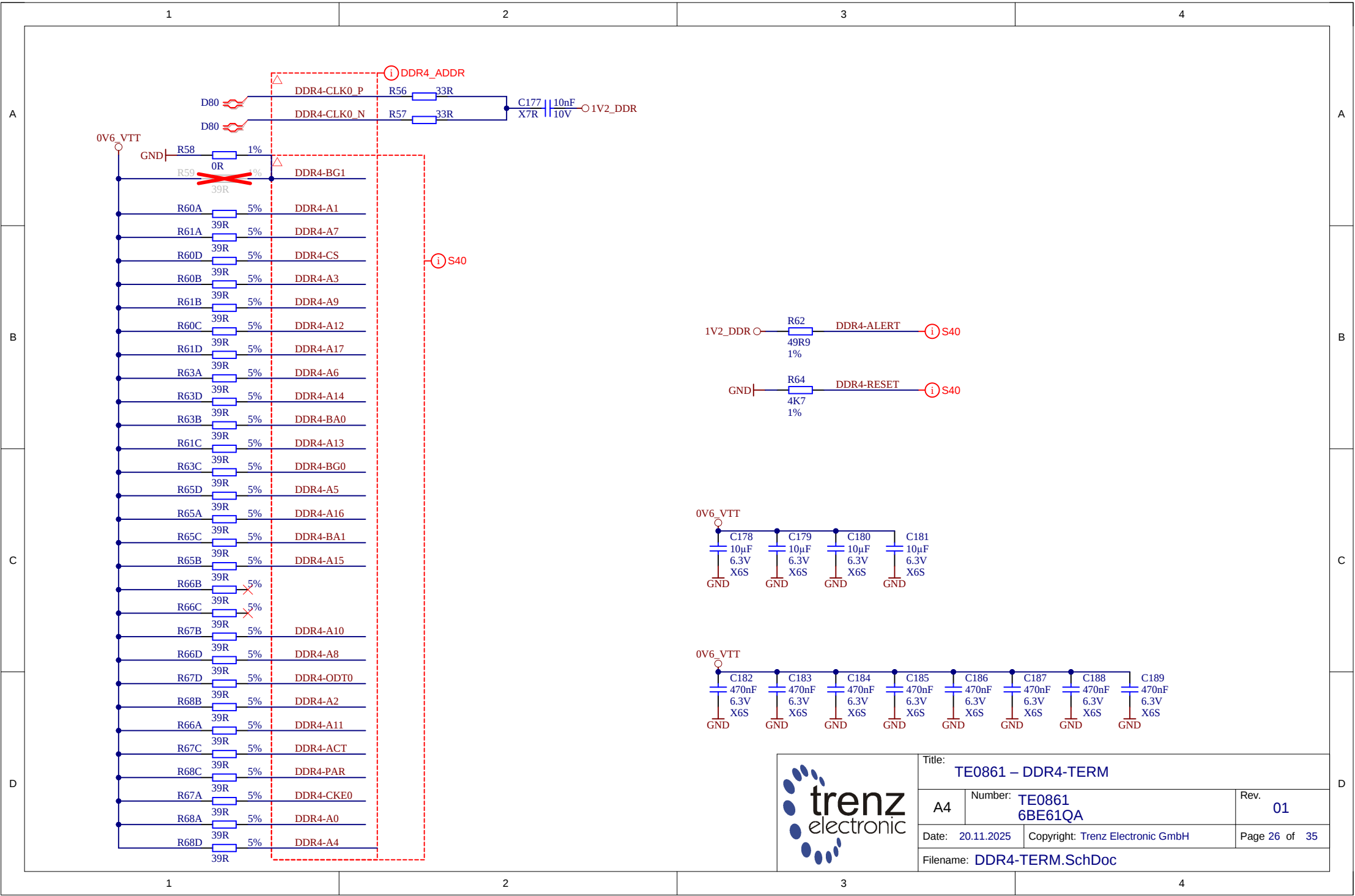
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| Title: TE0861 – DDR4-RAM_5  |                                  |               |
| A4                          | Number: TE0861<br>6BE61QA        | Rev. 01       |
| Date: 17.12.2025            | Copyright: Trenz Electronic GmbH | Page 25 of 35 |
| Filename: DDR4-RAM_5.SchDoc |                                  |               |

1

2

3

4



1

2

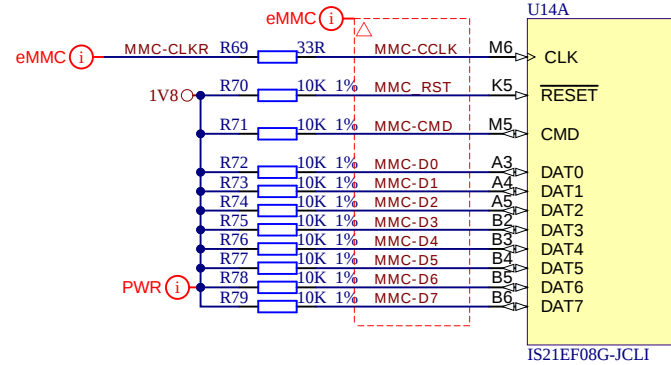
3

4

U14D

|     |    |    |     |
|-----|----|----|-----|
| A1  | NC | NC | H2  |
| A2  | NC | NC | H3  |
| A8  | NC | NC | H12 |
| A9  | NC | NC | H13 |
| A10 | NC | NC | H14 |
| A11 | NC | NC | J1  |
| A12 | NC | NC | J2  |
| A13 | NC | NC | J3  |
| A14 | NC | NC | J12 |
| B1  | NC | NC | J13 |
| B7  | NC | NC | J14 |
| B8  | NC | NC | K1  |
| B9  | NC | NC | K2  |
| B10 | NC | NC | K3  |
| B11 | NC | NC | K12 |
| B12 | NC | NC | K13 |
| B13 | NC | NC | K14 |
| B14 | NC | NC | L1  |
| C1  | NC | NC | L2  |
| C3  | NC | NC | L3  |
| C7  | NC | NC | L12 |
| C8  | NC | NC | L13 |
| C9  | NC | NC | L14 |
| C10 | NC | NC | M1  |
| C11 | NC | NC | M2  |
| C12 | NC | NC | M3  |
| C13 | NC | NC | M7  |
| C14 | NC | NC | M8  |
| D1  | NC | NC | M9  |
| D2  | NC | NC | M10 |
| D3  | NC | NC | M11 |
| D4  | NC | NC | M12 |
| D12 | NC | NC | M13 |
| D13 | NC | NC | M14 |
| D14 | NC | NC | N1  |
| E1  | NC | NC | N3  |
| E2  | NC | NC | N6  |
| E3  | NC | NC | N7  |
| E12 | NC | NC | N8  |
| E13 | NC | NC | N9  |
| E14 | NC | NC | N10 |
| F1  | NC | NC | N12 |
| F2  | NC | NC | N13 |
| F3  | NC | NC | N14 |
| F12 | NC | NC | P1  |
| F13 | NC | NC | P2  |
| F14 | NC | NC | P8  |
| G1  | NC | NC | P9  |
| G2  | NC | NC | P11 |
| G12 | NC | NC | P12 |
| G13 | NC | NC | P13 |
| G14 | NC | NC | P14 |
| H1  | NC | NC |     |

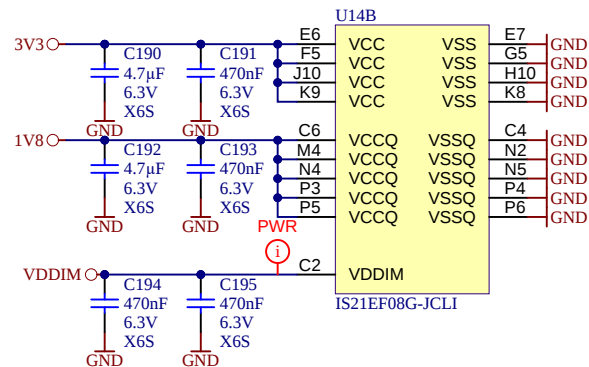
IS21EF08G-JCLI



U14C

|     |         |
|-----|---------|
| A6  | RFU/VSS |
| A7  | RFU     |
| C5  | RFU/NC  |
| E5  | RFU     |
| E8  | RFU     |
| E9  | RFU     |
| F10 | RFU     |
| F10 | RFU     |
| G3  | RFU/NC  |
| G10 | RFU     |
| H5  | RFU/DS  |
| J5  | RFU/VSS |
| K6  | RFU     |
| K7  | RFU     |
| K10 | RFU     |
| P7  | RFU/NC  |
| P10 | RFU     |

IS21EF08G-JCLI



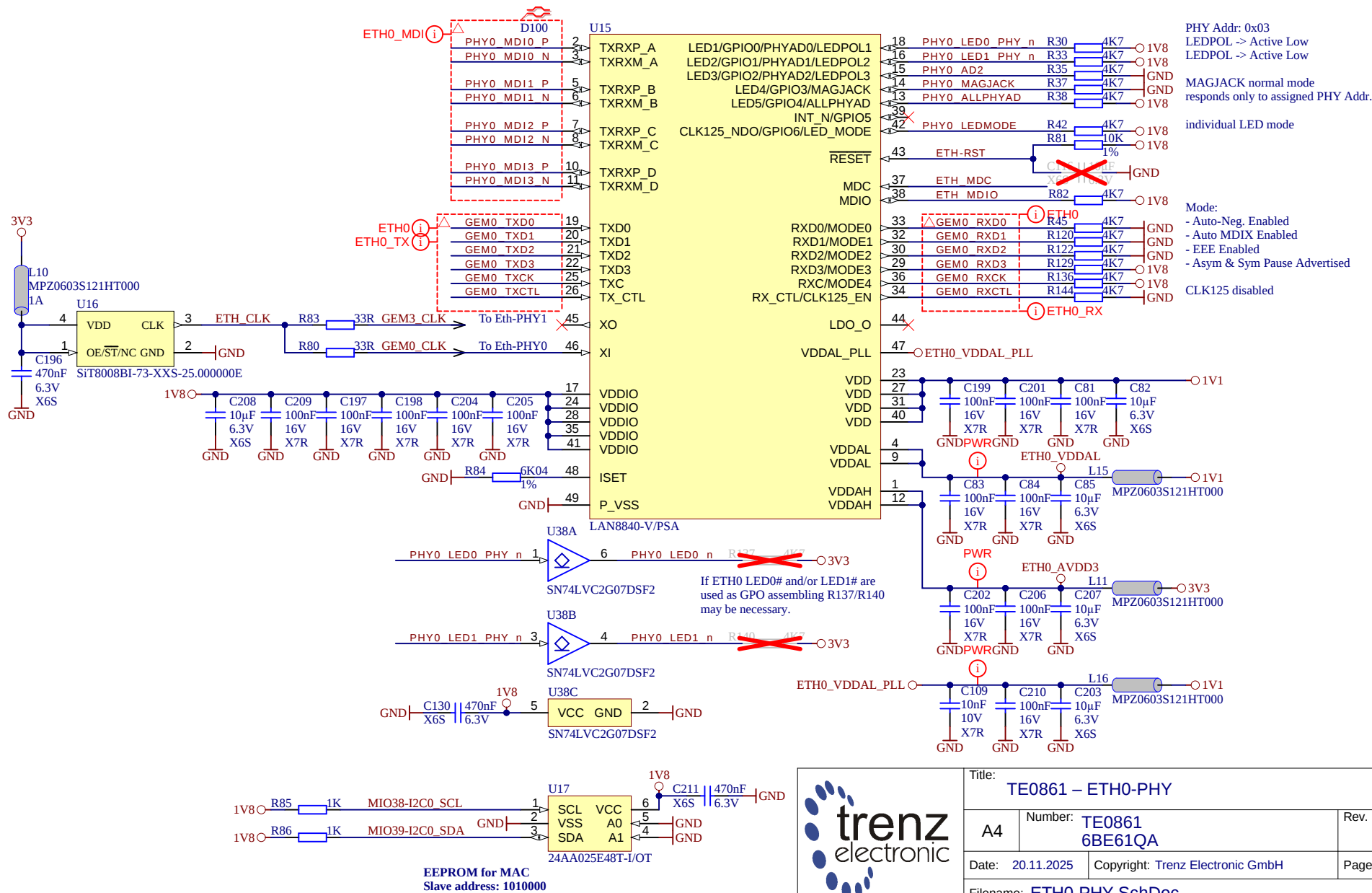
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|-----------------------|----------------------------------|---------------|
| Title: TE0861 – eMMC  |                                  |               |
| A4                    | Number: TE0861<br>6BE61QA        | Rev. 01       |
| Date: 20.11.2025      | Copyright: Trenz Electronic GmbH | Page 27 of 35 |
| Filename: eMMC.SchDoc |                                  |               |

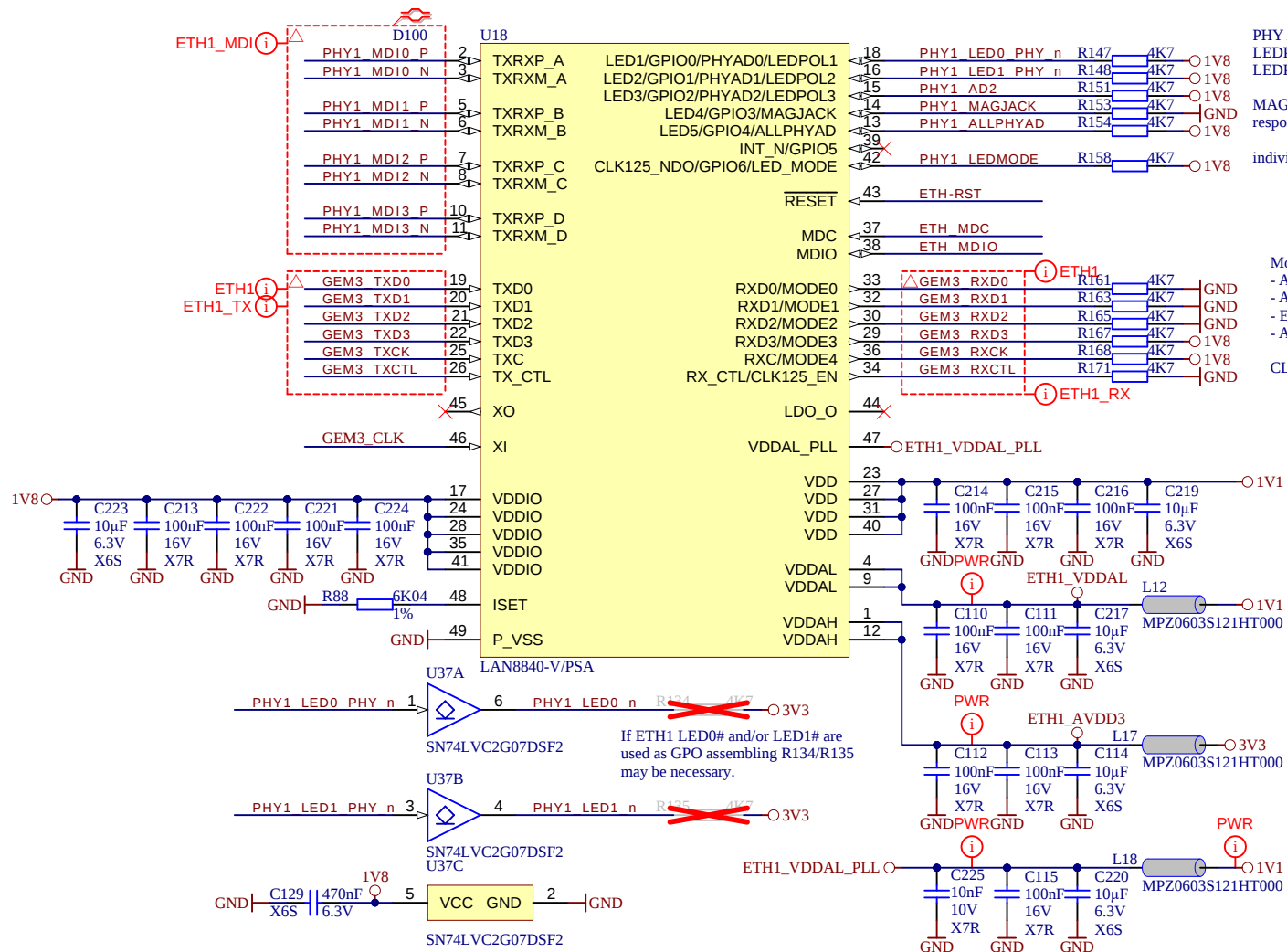
1

2

3

4





PHY Addr: 0x07  
 LEDPOL -> Active Low  
 LEDPOL -> Active Low  
 MAGJACK normal mode  
 responds only to assigned PHY Addr.  
 individual LED mode

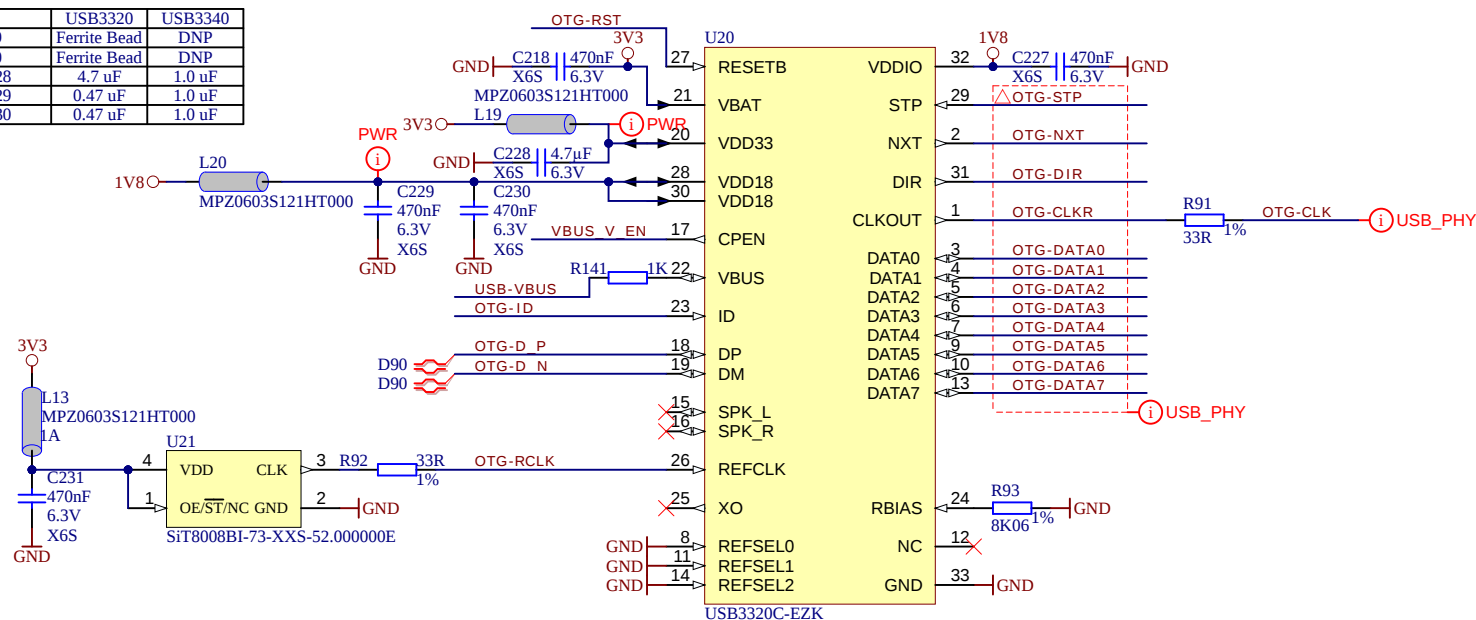
Mode:  
 - Auto-Neg. Enabled  
 - Auto MDIX Enabled  
 - EEE Enabled  
 - Asym & Sym Pause Advertised  
 CLK125 disabled

If ETH1 LED0# and/or LED1# are  
 used as GPO assembling R134/R135  
 may be necessary.

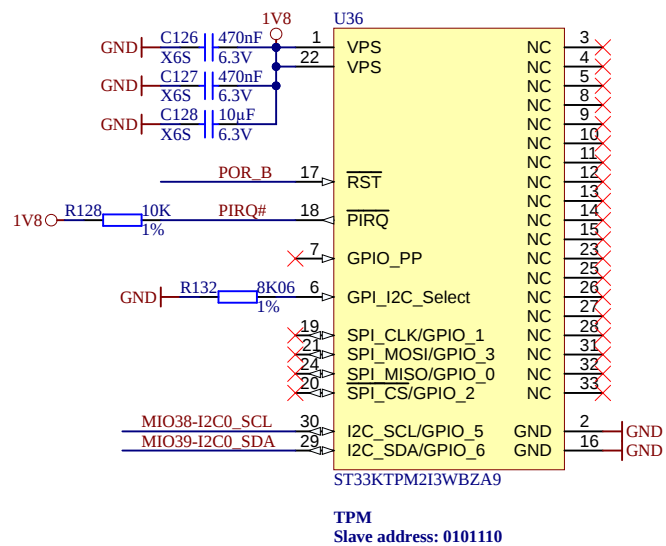


|                           |                                  |               |
|---------------------------|----------------------------------|---------------|
| Title: TE0861 – ETH1-PHY  |                                  |               |
| A4                        | Number: TE0861<br>6BE61QA        | Rev. 01       |
| Date: 20.11.2025          | Copyright: Trenz Electronic GmbH | Page 29 of 35 |
| Filename: ETH1-PHY.SchDoc |                                  |               |

|      | USB3320      | USB3340 |
|------|--------------|---------|
| L19  | Ferrite Bead | DNP     |
| L20  | Ferrite Bead | DNP     |
| C228 | 4.7 uF       | 1.0 uF  |
| C229 | 0.47 uF      | 1.0 uF  |
| C230 | 0.47 uF      | 1.0 uF  |

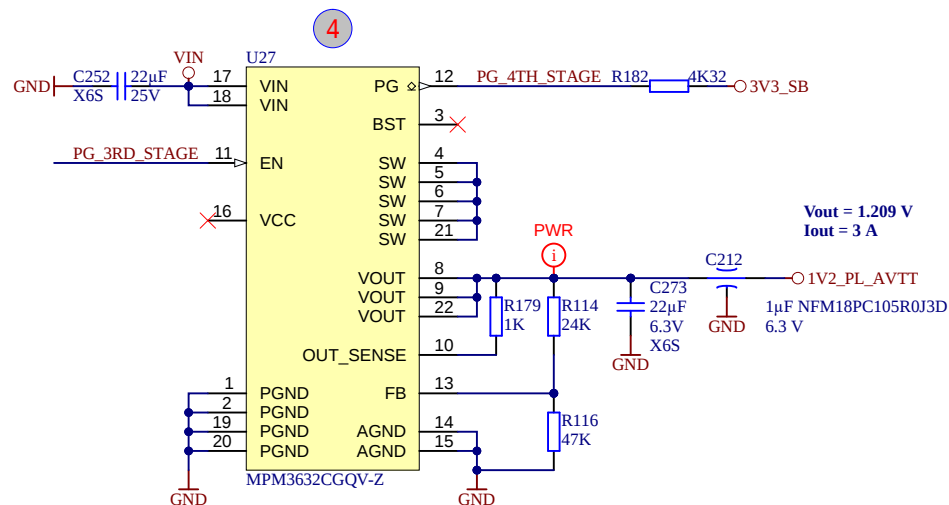
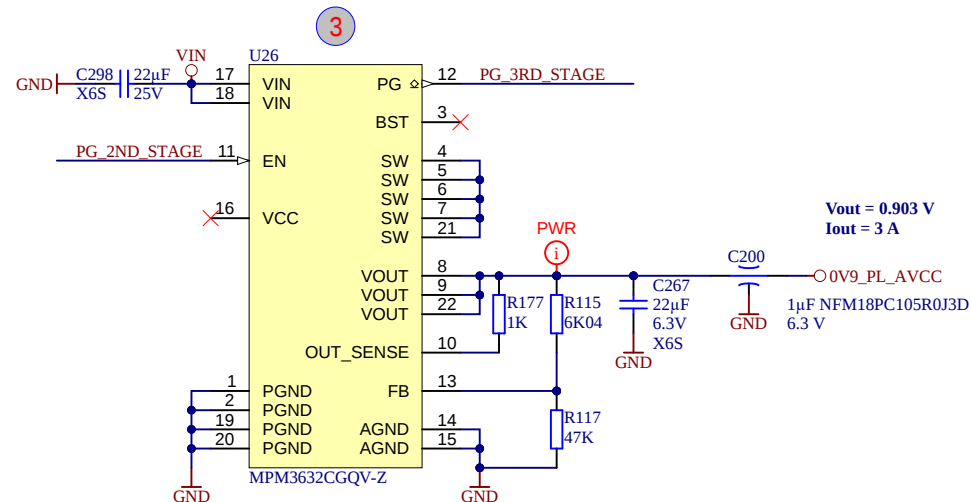
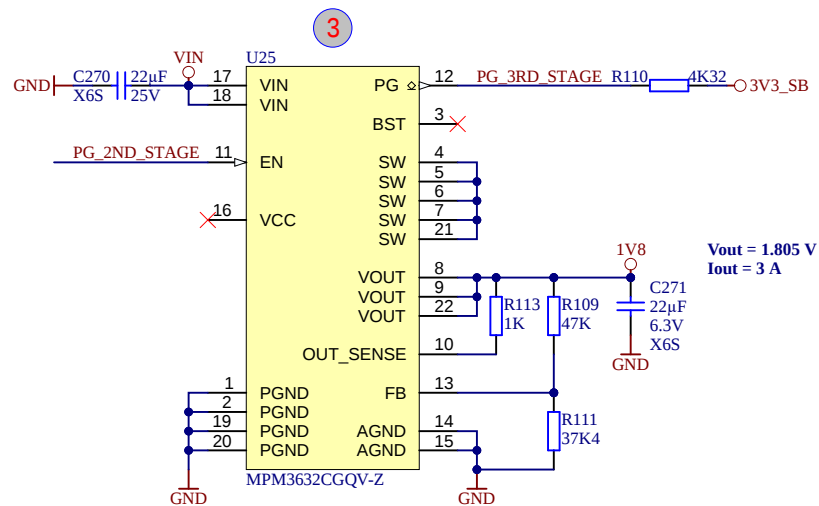


|                          |                                  |               |
|--------------------------|----------------------------------|---------------|
| Title: TE0861 – USB-PHY  |                                  |               |
| A4                       | Number: TE0861<br>6BE61QA        | Rev. 01       |
| Date: 20.11.2025         | Copyright: Trenz Electronic GmbH | Page 30 of 35 |
| Filename: USB-PHY.SchDoc |                                  |               |

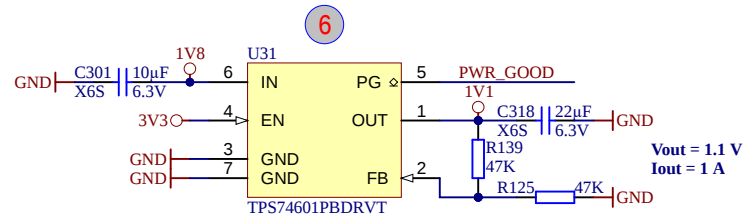
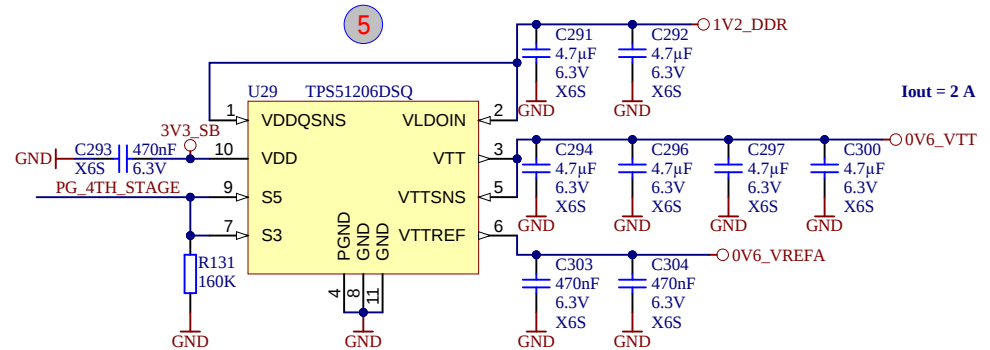
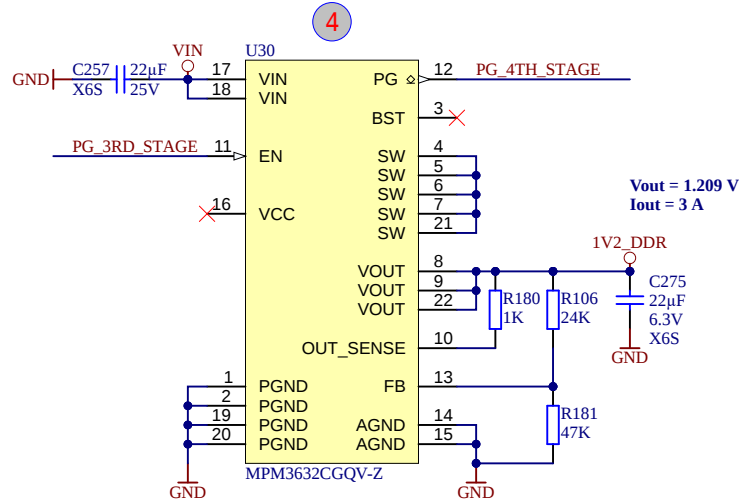
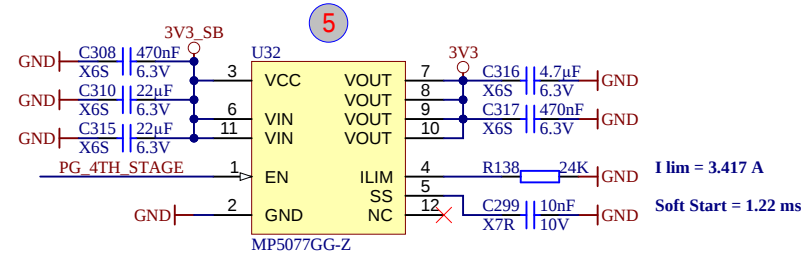
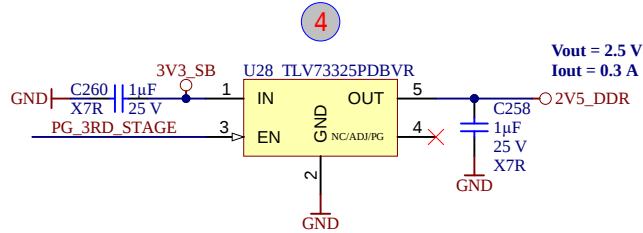


|                      |                                  |               |
|----------------------|----------------------------------|---------------|
| Title: TE0861 – TPM  |                                  |               |
| A4                   | Number: TE0861<br>6BE61QA        | Rev. 01       |
| Date: 20.11.2025     | Copyright: Trenz Electronic GmbH | Page 31 of 35 |
| Filename: TPM.SchDoc |                                  |               |





|                          |                                  |               |
|--------------------------|----------------------------------|---------------|
| Title: TE0861 – POWER 2  |                                  |               |
| A4                       | Number: TE0861<br>6BE61QA        | Rev. 01       |
| Date: 20.11.2025         | Copyright: Trenz Electronic GmbH | Page 33 of 35 |
| Filename: POWER_2.SchDoc |                                  |               |



|                          |                                  |               |
|--------------------------|----------------------------------|---------------|
| Title: TE0861 – POWER 3  |                                  |               |
| A4                       | Number: TE0861 6BE61QA           | Rev. 01       |
| Date: 20.11.2025         | Copyright: Trenz Electronic GmbH | Page 34 of 35 |
| Filename: POWER_3.SchDoc |                                  |               |

