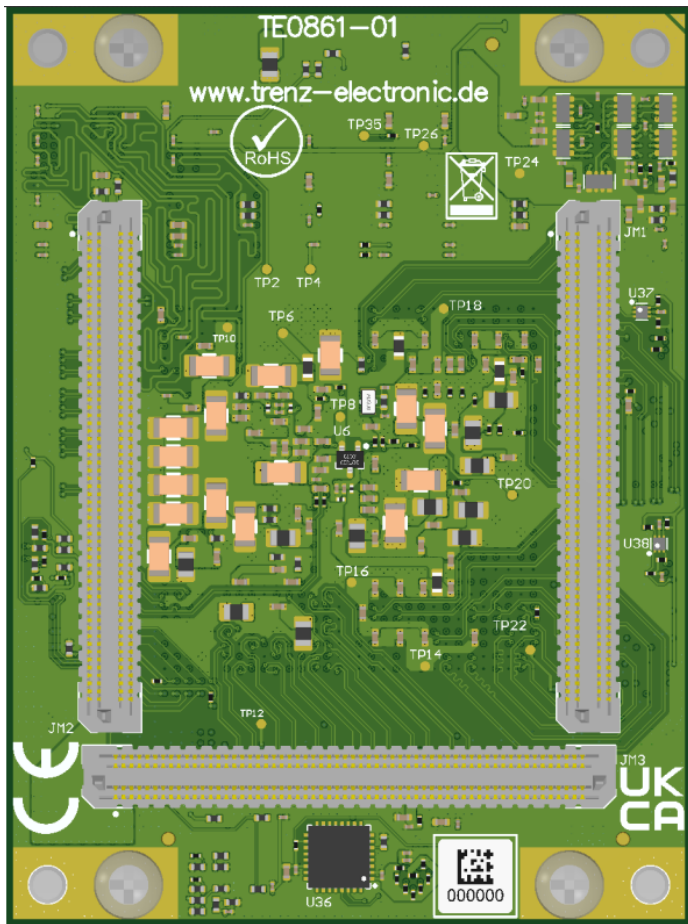
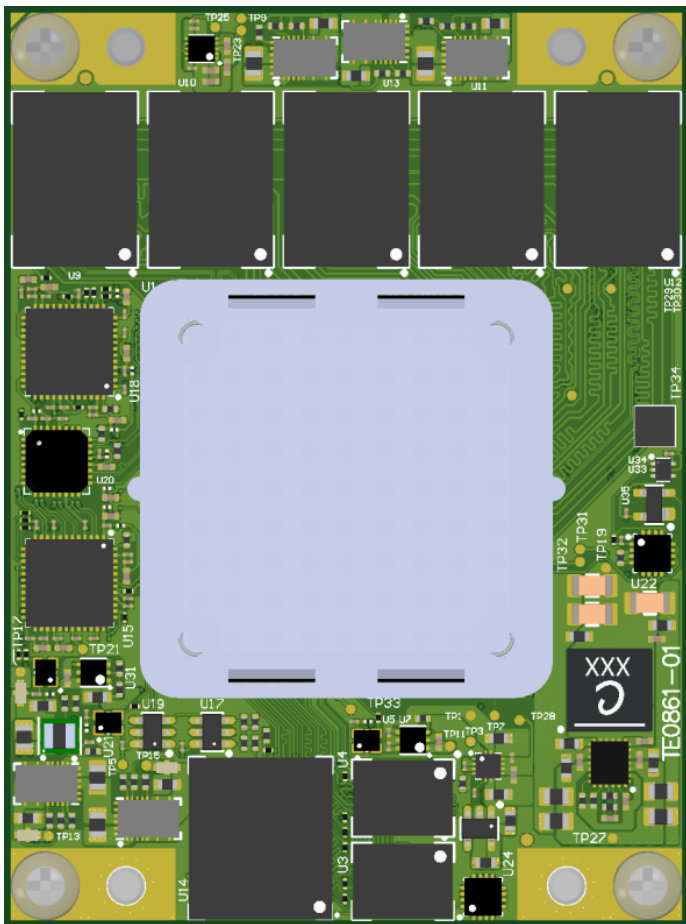




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 Project is protected under copyright and we strongly and strictly prohibit the reverse engineering or recreation, even if the design is just adapted or modified.  
 TE0861 is protected under such right and in case of plagiarism we will have to do anything necessary in order to protect our assets.  
 Schematics and other handouts serve for informational purposes only!

|  |                                               |                                         |                            |
|--|-----------------------------------------------|-----------------------------------------|----------------------------|
|  | Title: <b>TE0861 – Legal Notices Modules</b>  |                                         |                            |
|  | A4                                            | Number: <b>TE0861<br/>6BI51QA</b>       | Rev. <b>01</b>             |
|  | Date: <b>21.11.2025</b>                       | Copyright: <b>Trenz Electronic GmbH</b> | Page <b>1</b> of <b>35</b> |
|  | Filename: <b>Legal Notices Modules.SchDoc</b> |                                         |                            |

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A

A

| REV | Description      |       |
|-----|------------------|-------|
| -01 | Initial revision | ED/VY |

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|                                                                                       |  |  |                                   |                                  |              |
|---------------------------------------------------------------------------------------|--|--|-----------------------------------|----------------------------------|--------------|
|  |  |  | Title: TE0861 – Revision Changes  |                                  |              |
|                                                                                       |  |  | A4                                | Number: TE0861<br>6BI51QA        | Rev. 01      |
|                                                                                       |  |  | Date: 21.11.2025                  | Copyright: Trenz Electronic GmbH | Page 2 of 35 |
|                                                                                       |  |  | Filename: Revision_Changes.SchDoc |                                  |              |

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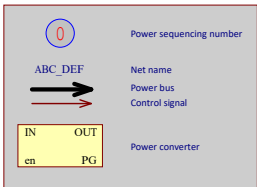
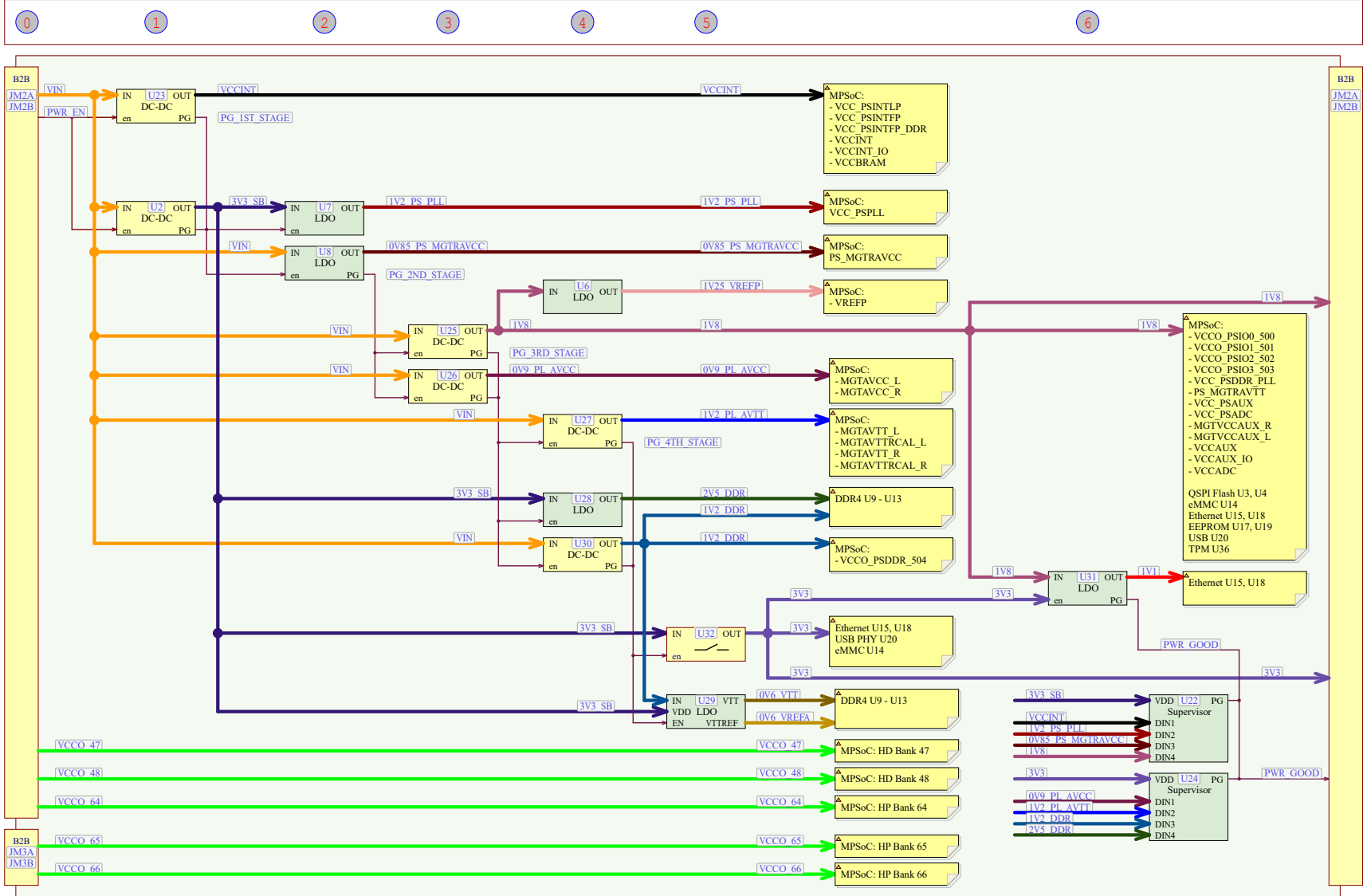
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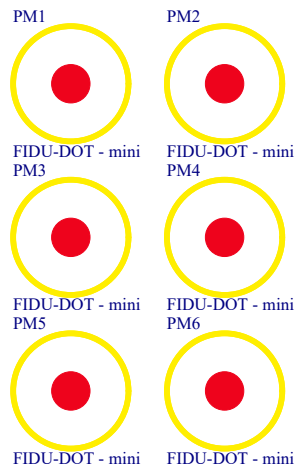
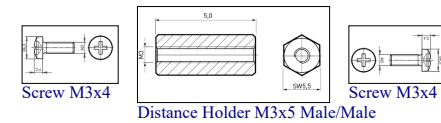
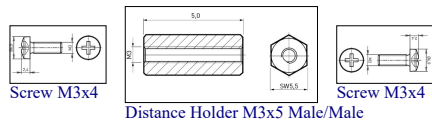
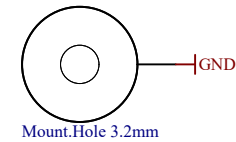
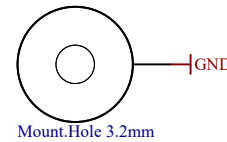
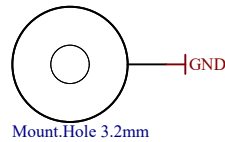
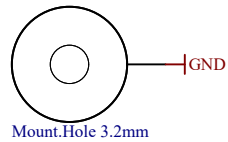
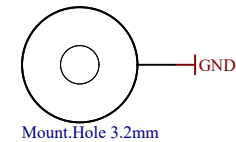
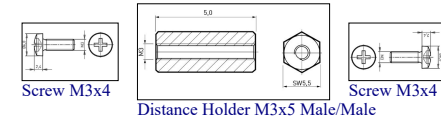
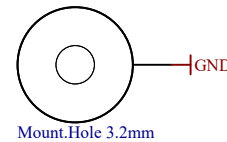
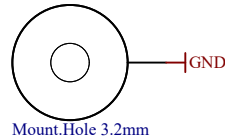
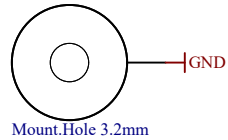
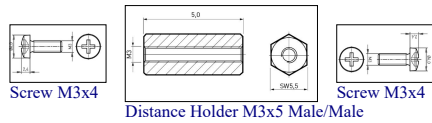
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Power Supply and Power Sequencing



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| A3                              | Number: TE0861 6BI51QA          | Rev. 01      |
| Datum: 21.11.2025               | Copyright:Trenz Electronic GmbH | Page 4 of 35 |
| Filename: POWER_Overview.SchDoc |                                 |              |

Special notes:



Serial1  
Serial  
Serialnumber 6,3 x 6.3mm  
RoHS1

RoHS Logo on Top Overlay

RoHS-TOPOVERLAY  
WEEE1

WEEE Logo on Top Overlay

WEEE-TOPOVERLAY  
CE1

CE Logo on Top Overlay

CE-TOPOVERLAY  
UKCA1

UKCA Logo on Top Overlay

UKCA-TOPOVERLAY

Design drawn by: ED  
Checked by: MR  
Assembly variant: 6BI51QA  
Created by: VG  
Modified by: VG  
Modified at: 2026-02-02



Title: TE0861

A4

Number: TE0861  
6BI51QA

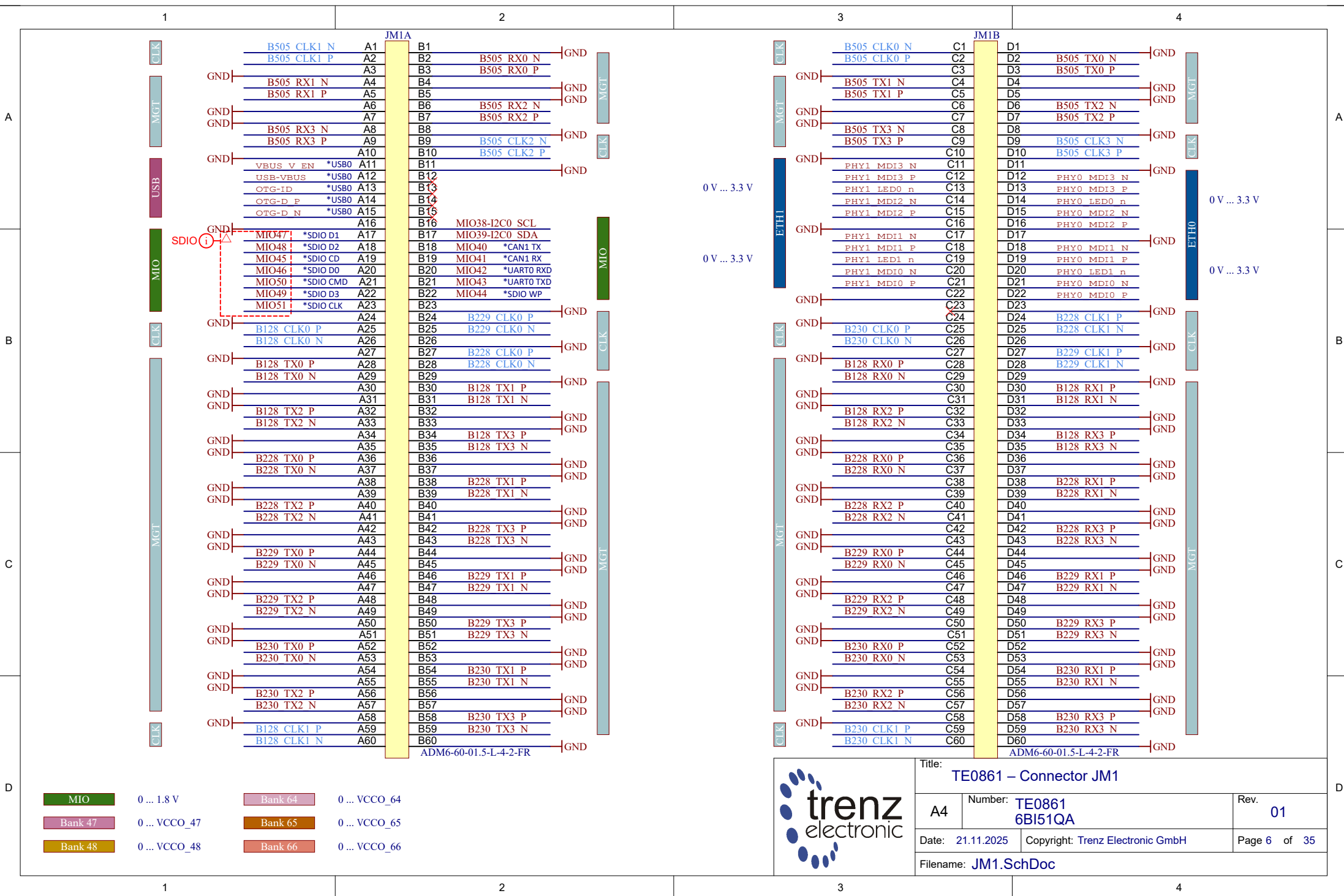
Rev. 01

Date: 21.11.2025

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Page 5 of 35

Filename: TE0861.SchDoc



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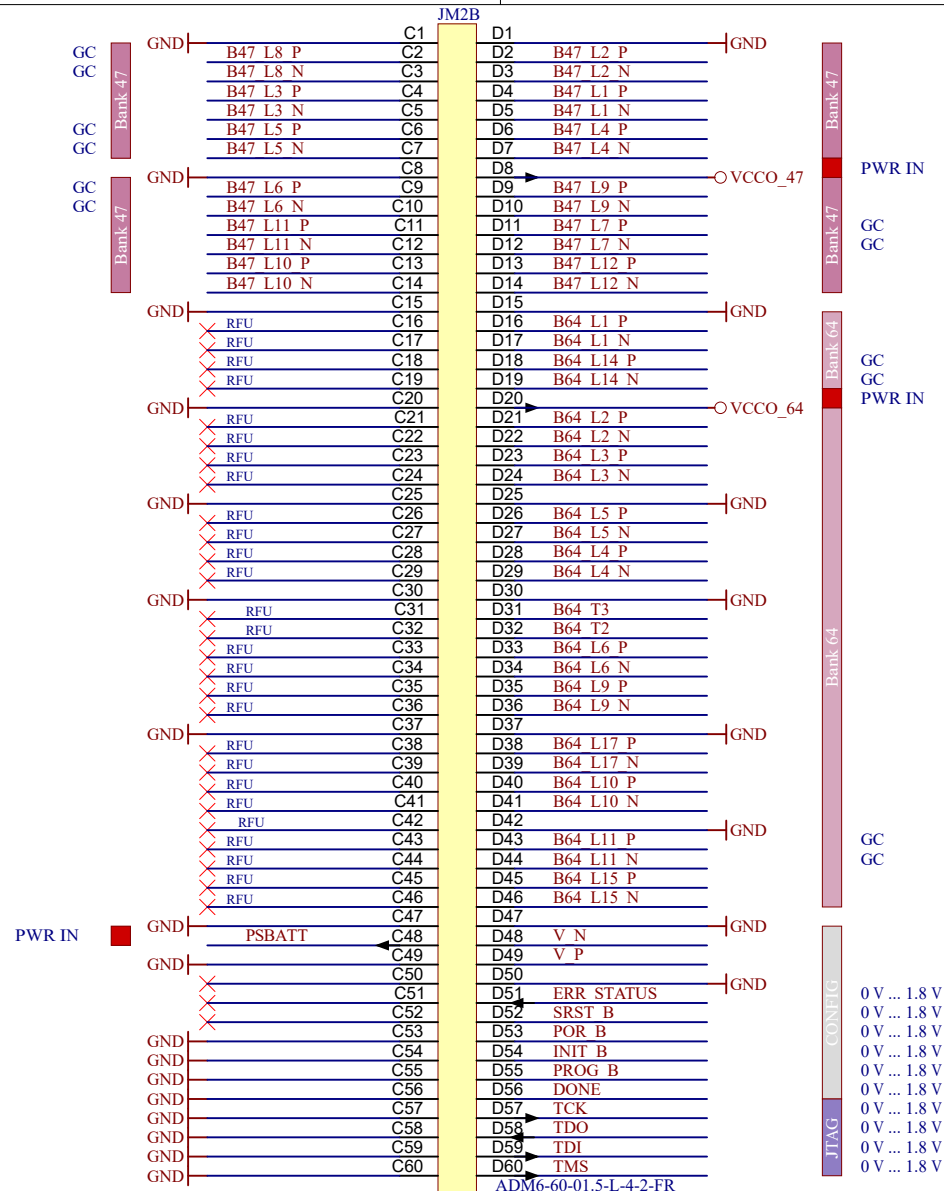
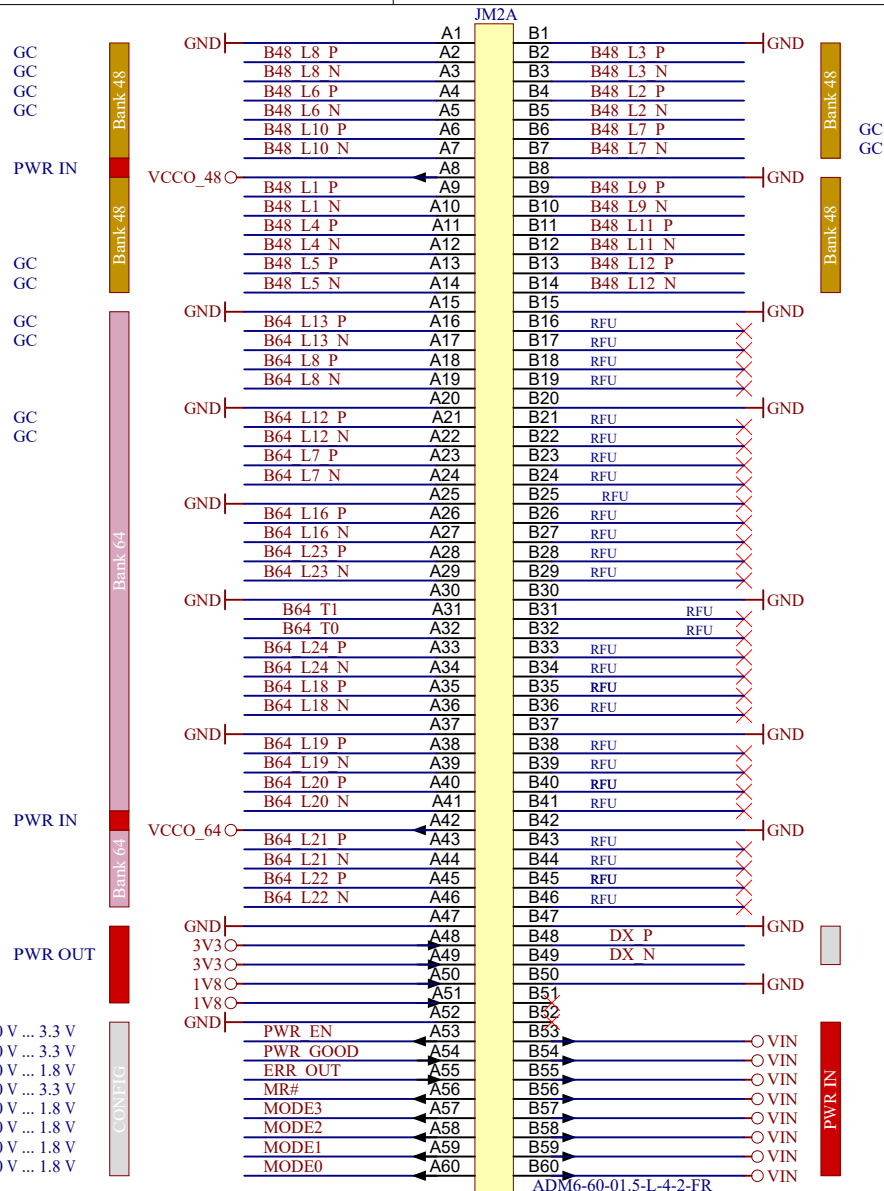
B

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|         |               |         |               |
|---------|---------------|---------|---------------|
| MIO     | 0 ... 1.8 V   | Bank 64 | 0 ... VCCO_64 |
| Bank 47 | 0 ... VCCO_47 | Bank 65 | 0 ... VCCO_65 |
| Bank 48 | 0 ... VCCO_48 | Bank 66 | 0 ... VCCO_66 |



|                                      |                                   |                                         |                            |
|--------------------------------------|-----------------------------------|-----------------------------------------|----------------------------|
| Title: <b>TE0861 – Connector JM2</b> |                                   |                                         |                            |
| A4                                   | Number: <b>TE0861<br/>6BI51QA</b> |                                         | Rev. <b>01</b>             |
| Date: <b>21.11.2025</b>              |                                   | Copyright: <b>Trenz Electronic GmbH</b> | Page <b>7</b> of <b>35</b> |
| Filename: <b>JM2.SchDoc</b>          |                                   |                                         |                            |

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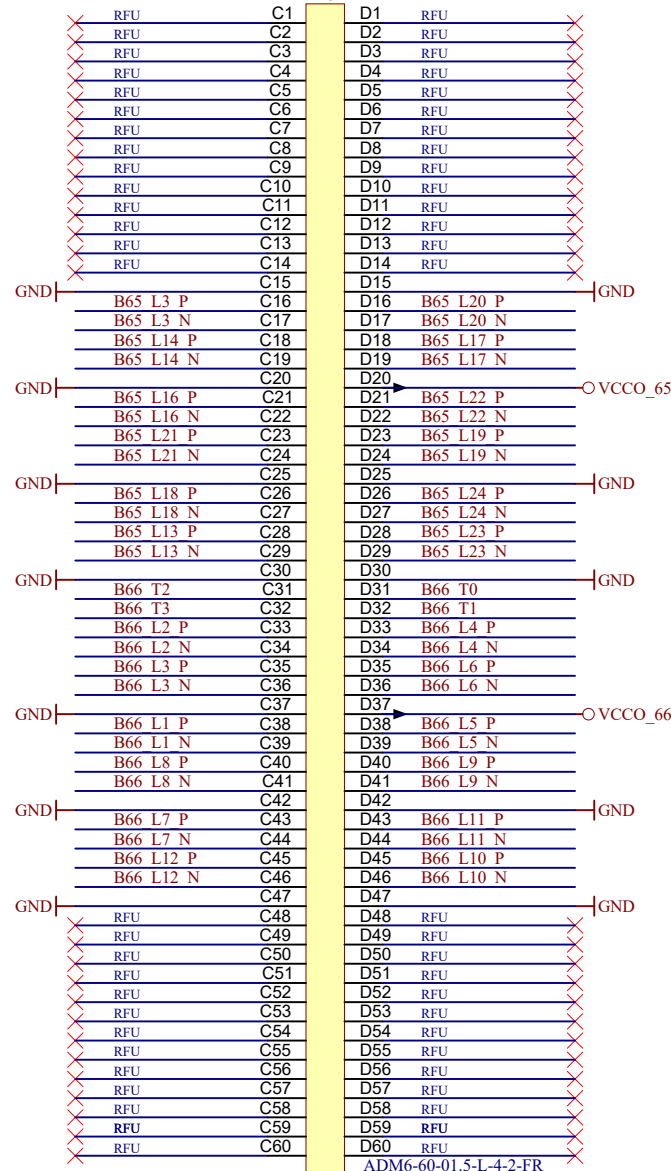
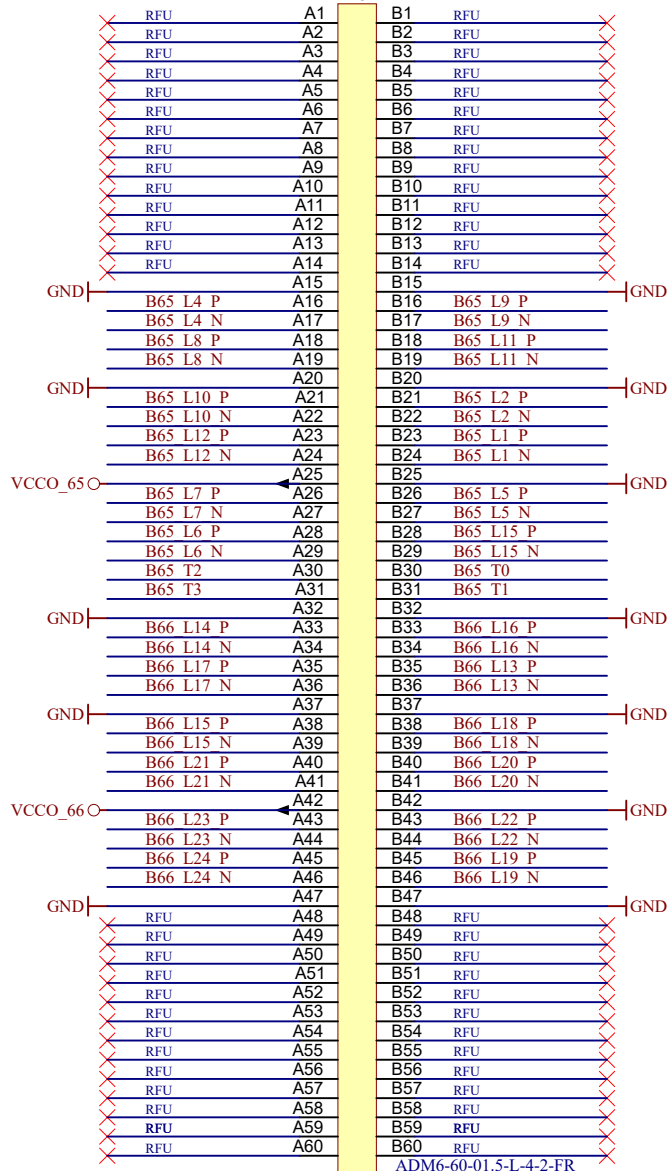
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JM3A

JM3B



ADM6-60-01.5-L-4-2-FR

ADM6-60-01.5-L-4-2-FR

|         |               |         |               |
|---------|---------------|---------|---------------|
| MIO     | 0 ... 1.8 V   | Bank 64 | 0 ... VCCO_64 |
| Bank 47 | 0 ... VCCO_47 | Bank 65 | 0 ... VCCO_65 |
| Bank 48 | 0 ... VCCO_48 | Bank 66 | 0 ... VCCO_66 |



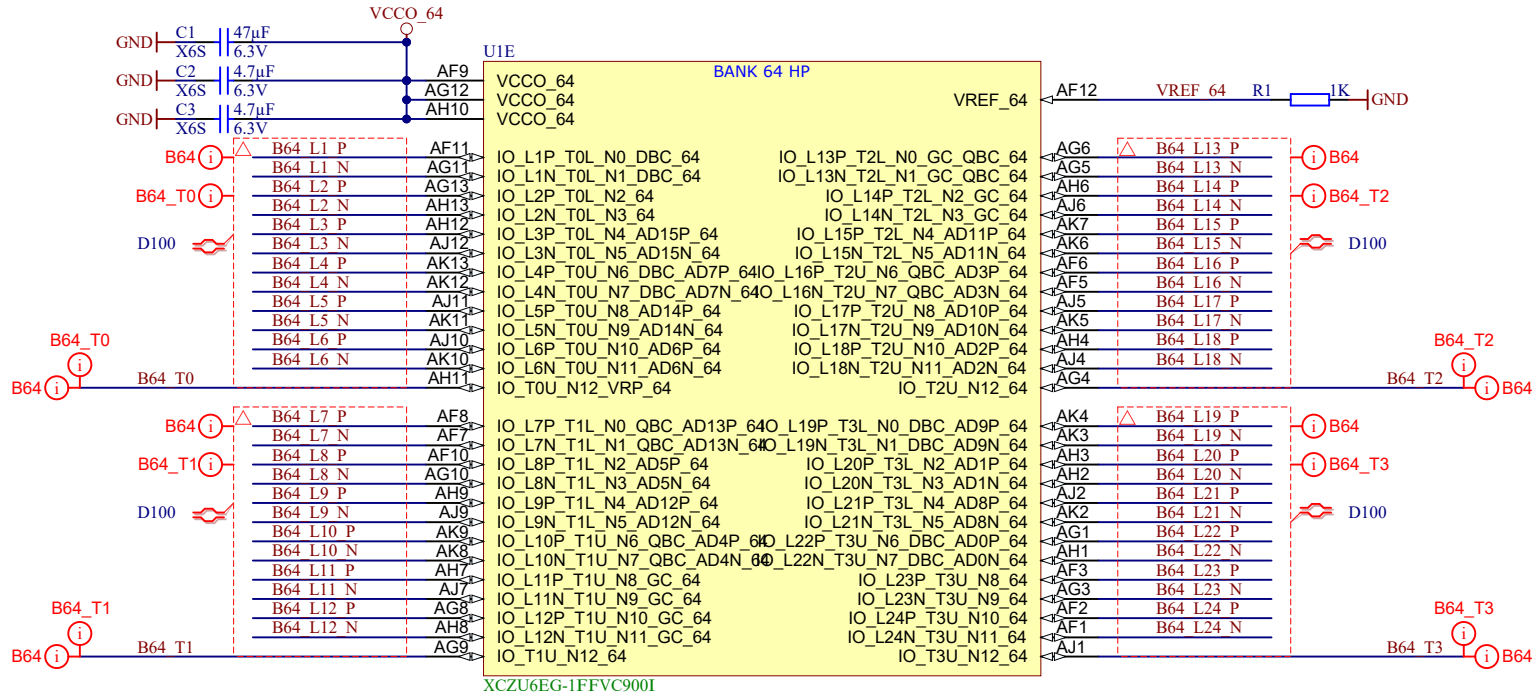
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| Title: TE0861 – Connector JM3 |                                  |              |
| A4                            | Number: TE0861 6BI51QA           | Rev. 01      |
| Date: 21.11.2025              | Copyright: Trenz Electronic GmbH | Page 8 of 35 |
| Filename: JM3.SchDoc          |                                  |              |

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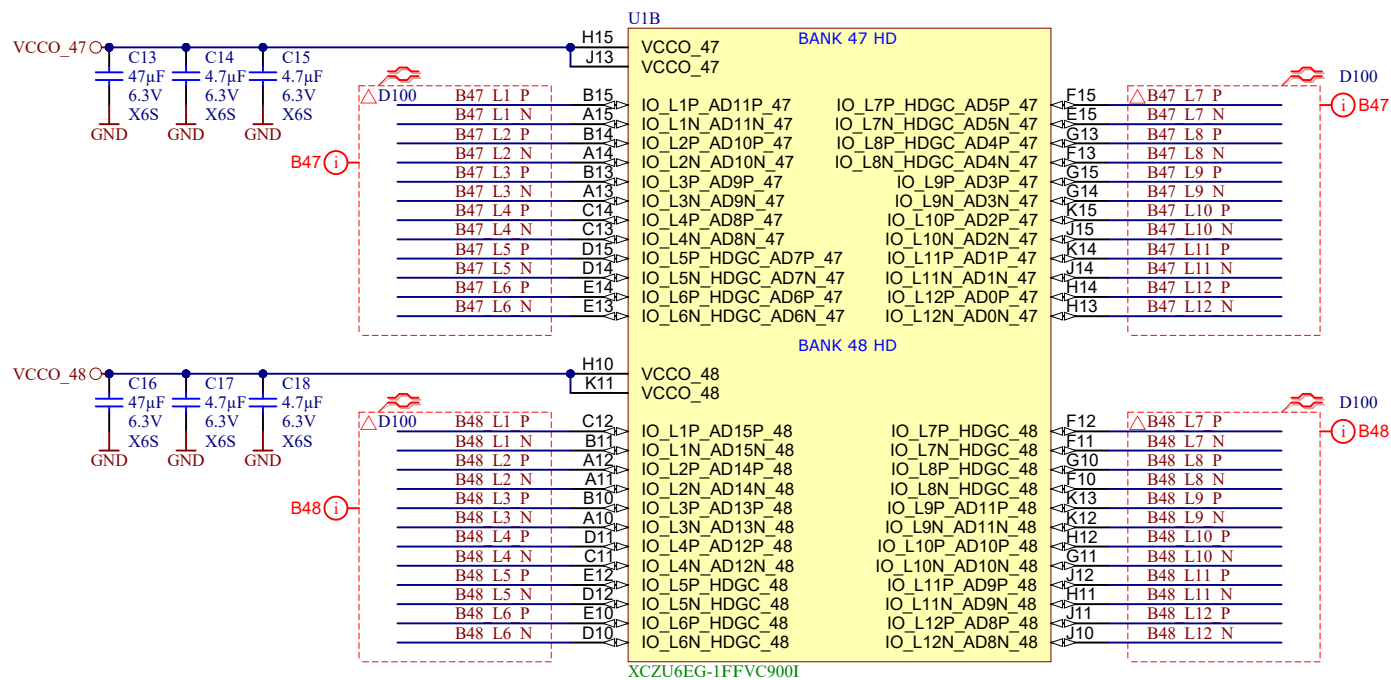
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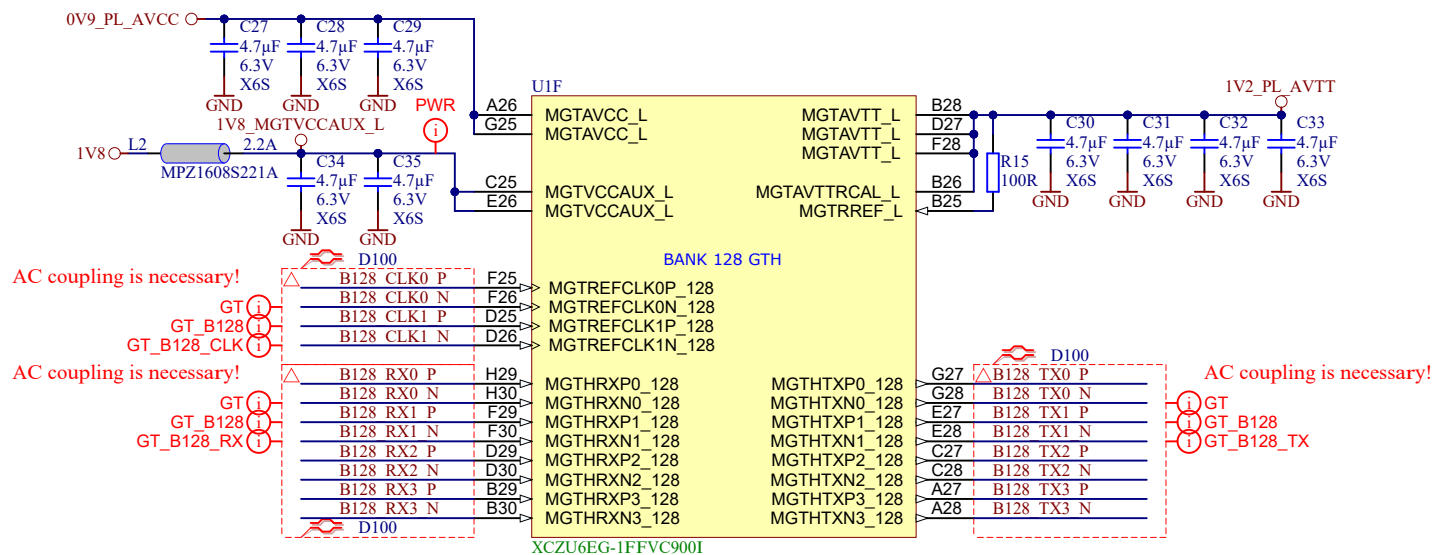


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|-------------------------|----------------------------------|---------------|
| Title: TE0861 – B_GT_L  |                                  |               |
| A4                      | Number: TE0861 6BI51QA           | Rev. 01       |
| Date: 21.11.2025        | Copyright: Trenz Electronic GmbH | Page 14 of 35 |
| Filename: B_GT_L.SchDoc |                                  |               |

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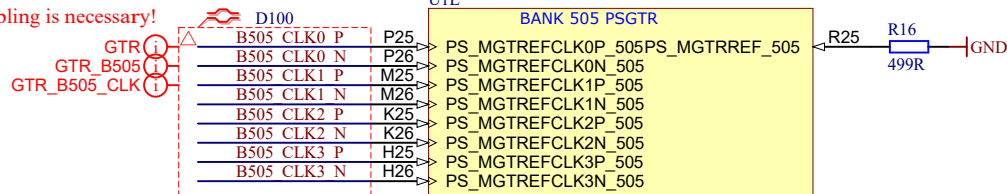
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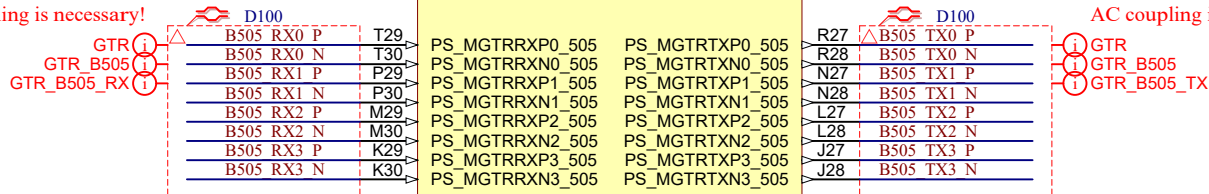
D

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AC coupling is necessary!



AC coupling is necessary!



AC coupling is necessary!

XCZU6EG-1FFVC900I



|                          |                                  |               |
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| Title: TE0861 – B_PS_GT  |                                  |               |
| A4                       | Number: TE0861<br>6BI51QA        | Rev. 01       |
| Date: 21.11.2025         | Copyright: Trenz Electronic GmbH | Page 16 of 35 |
| Filename: B_PS_GT.SchDoc |                                  |               |

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UIJ

## BANK 504 PSDDR

|                  |              |
|------------------|--------------|
| VCCO_PSDDR_504   | PS_DDR_CK0   |
| VCCO_PSDDR_504   | PS_DDR_CK_N0 |
| VCCO_PSDDR_504   | PS_DDR_CKE0  |
| VCCO_PSDDR_504   | PS_DDR_CK1   |
| VCCO_PSDDR_504   | PS_DDR_CK_N1 |
| VCCO_PSDDR_504   | PS_DDR_CKE1  |
| VCC_PSDDR_PLL    | PS_DDR_A0    |
| VCC_PSDDR_PLL    | PS_DDR_A1    |
| VCC_PSDDR_PLL    | PS_DDR_A2    |
| VCC_PSDDR_PLL    | PS_DDR_A3    |
| VCC_PSDDR_PLL    | PS_DDR_A4    |
| VCC_PSDDR_PLL    | PS_DDR_A5    |
| VCC_PSDDR_PLL    | PS_DDR_A6    |
| VCC_PSDDR_PLL    | PS_DDR_A7    |
| VCC_PSDDR_PLL    | PS_DDR_A8    |
| VCC_PSDDR_PLL    | PS_DDR_A9    |
| VCC_PSDDR_PLL    | PS_DDR_A10   |
| VCC_PSDDR_PLL    | PS_DDR_A11   |
| VCC_PSDDR_PLL    | PS_DDR_A12   |
| VCC_PSDDR_PLL    | PS_DDR_A13   |
| VCC_PSDDR_PLL    | PS_DDR_A14   |
| VCC_PSDDR_PLL    | PS_DDR_A15   |
| VCC_PSDDR_PLL    | PS_DDR_A16   |
| VCC_PSDDR_PLL    | PS_DDR_A17   |
| PS_DDR_CS_N0     |              |
| PS_DDR_CS_N1     |              |
| PS_DDR_BA0       |              |
| PS_DDR_BA1       |              |
| PS_DDR_BG0       |              |
| PS_DDR_BG1       |              |
| PS_DDR_PARITY    |              |
| PS_DDR_RAM_RST_N |              |
| PS_DDR_ACT_N     |              |
| PS_DDR_ALERT_N   |              |
| PS_DDR_ZQ        |              |
| PS_DDR_ODT0      |              |
| PS_DDR_ODT1      |              |

XCZU6EG-1FFVC9001

|      |             |     |
|------|-------------|-----|
| AJ26 | DDR4-CLK0 P | D80 |
| AJ27 | DDR4-CLK0 N | D80 |
| AJ25 | DDR4-CKE0   |     |

|      |          |
|------|----------|
| AG23 | DDR4-A0  |
| AG24 | DDR4-A1  |
| AH27 | DDR4-A2  |
| AK25 | DDR4-A3  |
| AK28 | DDR4-A4  |
| AK27 | DDR4-A5  |
| AH24 | DDR4-A6  |
| AK24 | DDR4-A7  |
| AJ30 | DDR4-A8  |
| AE22 | DDR4-A9  |
| AE23 | DDR4-A10 |
| AE24 | DDR4-A11 |
| AD22 | DDR4-A12 |
| AH23 | DDR4-A13 |
| AF21 | DDR4-A14 |
| AG21 | DDR4-A15 |
| AF22 | DDR4-A16 |
| AF23 | DDR4-A17 |
| AD21 | DDR4-A18 |
| AD20 | DDR4-A19 |

|      |         |
|------|---------|
| AJ24 | DDR4-CS |
| AG25 |         |

|      |          |
|------|----------|
| AG26 | DDR4-BA0 |
| AF25 | DDR4-BA1 |

|      |          |
|------|----------|
| AF26 | DDR4-BG0 |
| AD26 | DDR4-BG1 |

|      |            |
|------|------------|
| AC14 | DDR4-PAR   |
| AC16 | DDR4-RESET |
| AC17 | DDR4-ACT   |
| AD19 | DDR4-ALERT |

|      |     |      |     |
|------|-----|------|-----|
| AC23 | R17 | 240R | GND |
|------|-----|------|-----|

|      |           |
|------|-----------|
| AJ29 | DDR4-ODT0 |
| AH26 |           |

UIK

## BANK 504 PSDDR

|               |               |
|---------------|---------------|
| PS_DDR_DQ0    | PS_DDR_DQ32   |
| PS_DDR_DQ1    | PS_DDR_DQ33   |
| PS_DDR_DQ2    | PS_DDR_DQ34   |
| PS_DDR_DQ3    | PS_DDR_DQ35   |
| PS_DDR_DQ4    | PS_DDR_DQ36   |
| PS_DDR_DQ5    | PS_DDR_DQ37   |
| PS_DDR_DQ6    | PS_DDR_DQ38   |
| PS_DDR_DQ7    | PS_DDR_DQ39   |
| PS_DDR_DQ8    | PS_DDR_DQ40   |
| PS_DDR_DQ9    | PS_DDR_DQ41   |
| PS_DDR_DQ10   | PS_DDR_DQ42   |
| PS_DDR_DQ11   | PS_DDR_DQ43   |
| PS_DDR_DQ12   | PS_DDR_DQ44   |
| PS_DDR_DQ13   | PS_DDR_DQ45   |
| PS_DDR_DQ14   | PS_DDR_DQ46   |
| PS_DDR_DQ15   | PS_DDR_DQ47   |
| PS_DDR_DQ16   | PS_DDR_DQ48   |
| PS_DDR_DQ17   | PS_DDR_DQ49   |
| PS_DDR_DQ18   | PS_DDR_DQ50   |
| PS_DDR_DQ19   | PS_DDR_DQ51   |
| PS_DDR_DQ20   | PS_DDR_DQ52   |
| PS_DDR_DQ21   | PS_DDR_DQ53   |
| PS_DDR_DQ22   | PS_DDR_DQ54   |
| PS_DDR_DQ23   | PS_DDR_DQ55   |
| PS_DDR_DQ24   | PS_DDR_DQ56   |
| PS_DDR_DQ25   | PS_DDR_DQ57   |
| PS_DDR_DQ26   | PS_DDR_DQ58   |
| PS_DDR_DQ27   | PS_DDR_DQ59   |
| PS_DDR_DQ28   | PS_DDR_DQ60   |
| PS_DDR_DQ29   | PS_DDR_DQ61   |
| PS_DDR_DQ30   | PS_DDR_DQ62   |
| PS_DDR_DQ31   | PS_DDR_DQ63   |
| PS_DDR_DQS_P0 | PS_DDR_DQS_P0 |
| PS_DDR_DQS_N0 | PS_DDR_DQS_N0 |
| PS_DDR_DQS_P1 | PS_DDR_DQS_P1 |
| PS_DDR_DQS_N1 | PS_DDR_DQS_N1 |
| PS_DDR_DQS_P2 | PS_DDR_DQS_P2 |
| PS_DDR_DQS_N2 | PS_DDR_DQS_N2 |
| PS_DDR_DQS_P3 | PS_DDR_DQS_P3 |
| PS_DDR_DQS_N3 | PS_DDR_DQS_N3 |
| PS_DDR_DQS_P4 | PS_DDR_DQS_P4 |
| PS_DDR_DQS_N4 | PS_DDR_DQS_N4 |
| PS_DDR_DQS_P5 | PS_DDR_DQS_P5 |
| PS_DDR_DQS_N5 | PS_DDR_DQS_N5 |
| PS_DDR_DQS_P6 | PS_DDR_DQS_P6 |
| PS_DDR_DQS_N6 | PS_DDR_DQS_N6 |
| PS_DDR_DQS_P7 | PS_DDR_DQS_P7 |
| PS_DDR_DQS_N7 | PS_DDR_DQS_N7 |
| PS_DDR_DQS_P8 | PS_DDR_DQS_P8 |
| PS_DDR_DQS_N8 | PS_DDR_DQS_N8 |
| PS_DDR_DM0    | PS_DDR_DM0    |
| PS_DDR_DM1    | PS_DDR_DM1    |
| PS_DDR_DM2    | PS_DDR_DM2    |
| PS_DDR_DM3    | PS_DDR_DM3    |
| PS_DDR_DM4    | PS_DDR_DM4    |
| PS_DDR_DM5    | PS_DDR_DM5    |
| PS_DDR_DM6    | PS_DDR_DM6    |
| PS_DDR_DM7    | PS_DDR_DM7    |
| PS_DDR_DM8    | PS_DDR_DM8    |

XCZU6EG-1FFVC9001



Title:

TE0861 – PS\_DDR

A4

Number:

TE0861  
6BI51QA

Rev.

01

Date: 21.11.2025

Copyright: Trenz Electronic GmbH

Page 17 of 35

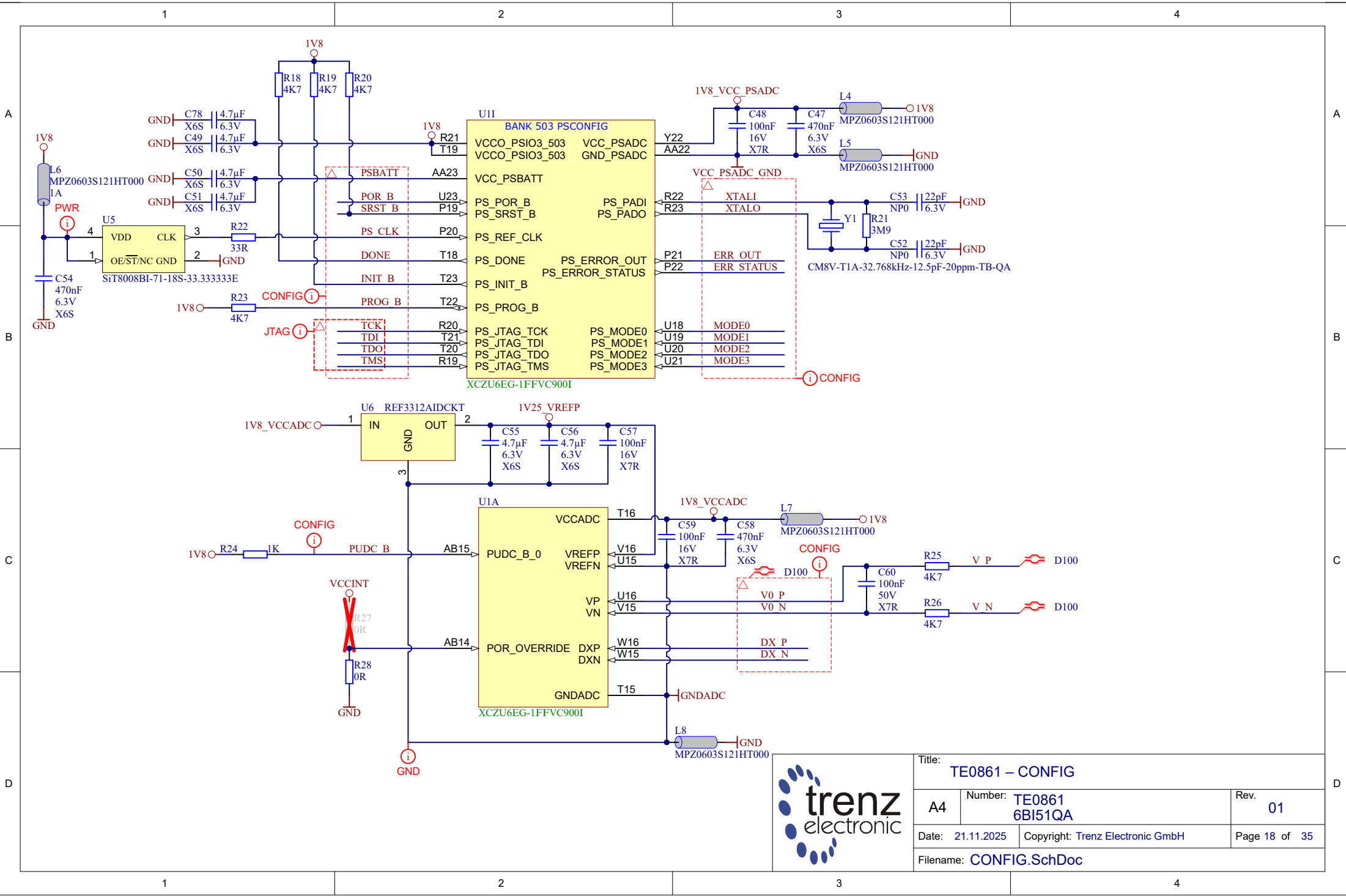
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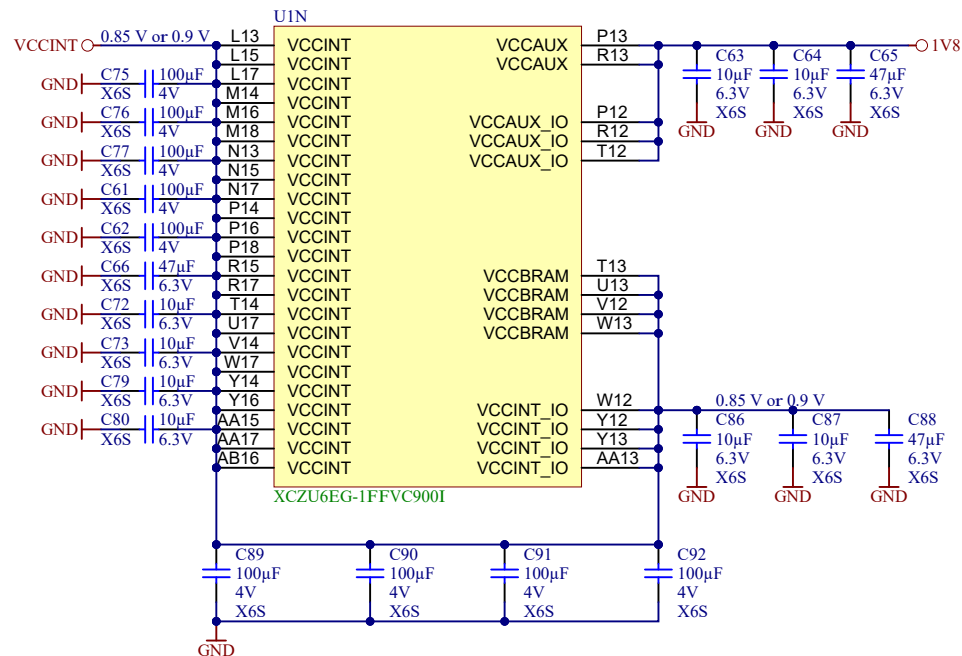
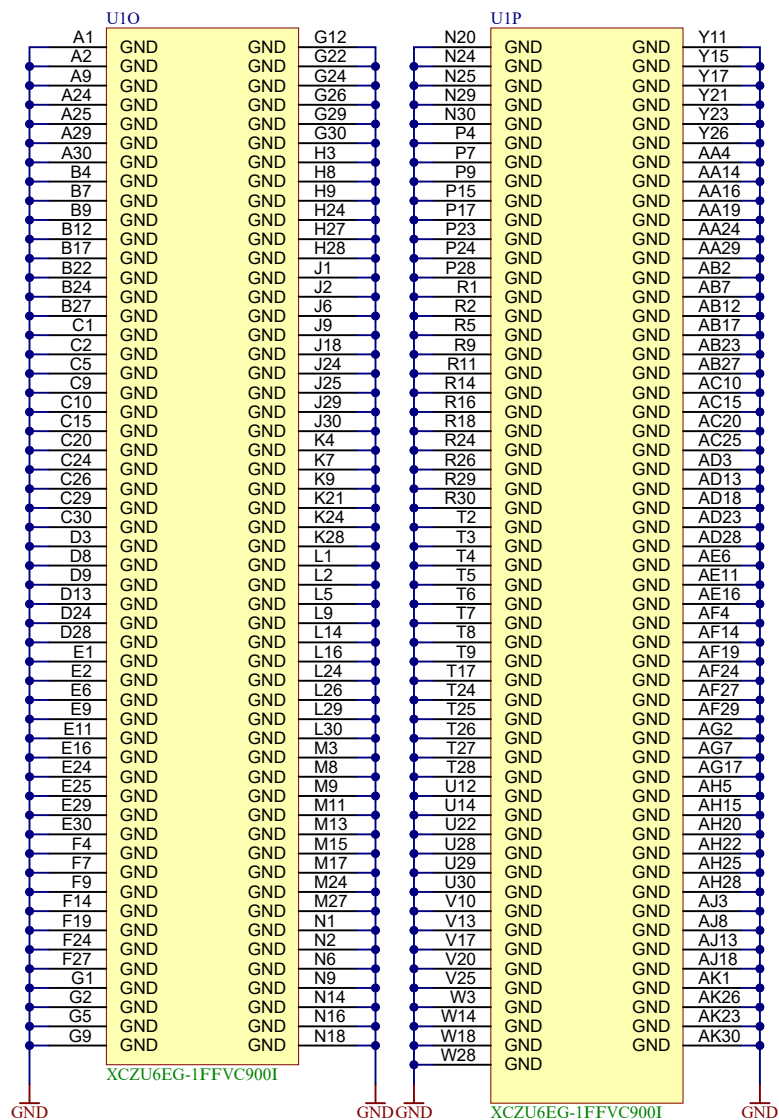
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| A4                      | Number: TE0861<br>6BI51QA        | Rev. 01       |
| Date: 21.11.2025        | Copyright: Trenz Electronic GmbH | Page 18 of 35 |
| Filename: CONFIG.SchDoc |                                  |               |

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|---------------------------|----------------------------------|---------------|
| Title: TE0861 – ZU_POWER  |                                  |               |
| A4                        | Number: TE0861<br>6BI51QA        | Rev. 01       |
| Date: 21.11.2025          | Copyright: Trenz Electronic GmbH | Page 19 of 35 |
| Filename: ZU_POWER.SchDoc |                                  |               |

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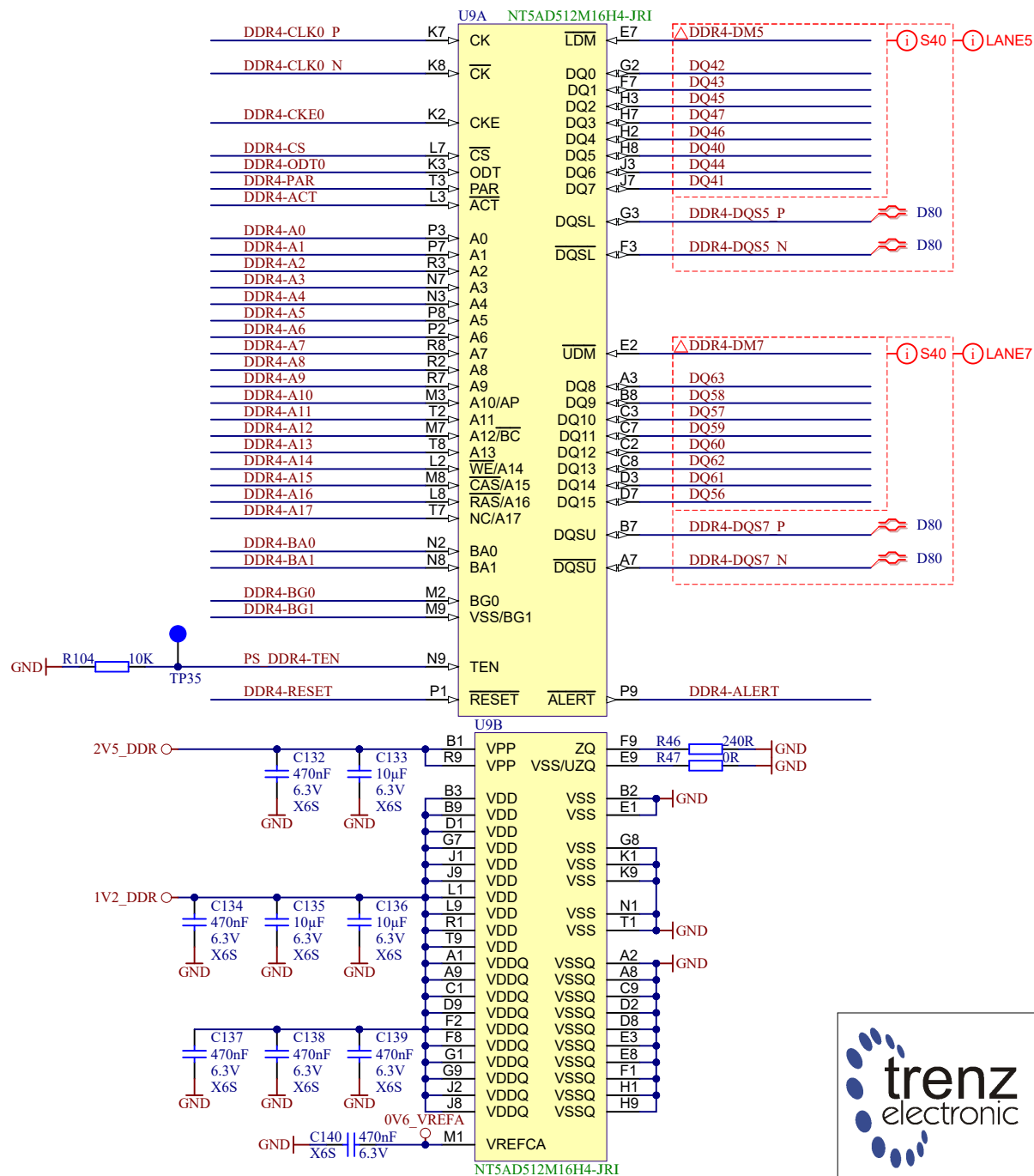
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|                             |                                  |               |
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| Date: 23.01.2026            | Copyright: Trenz Electronic GmbH | Page 21 of 35 |
| Filename: DDR4-RAM_1.SchDoc |                                  |               |

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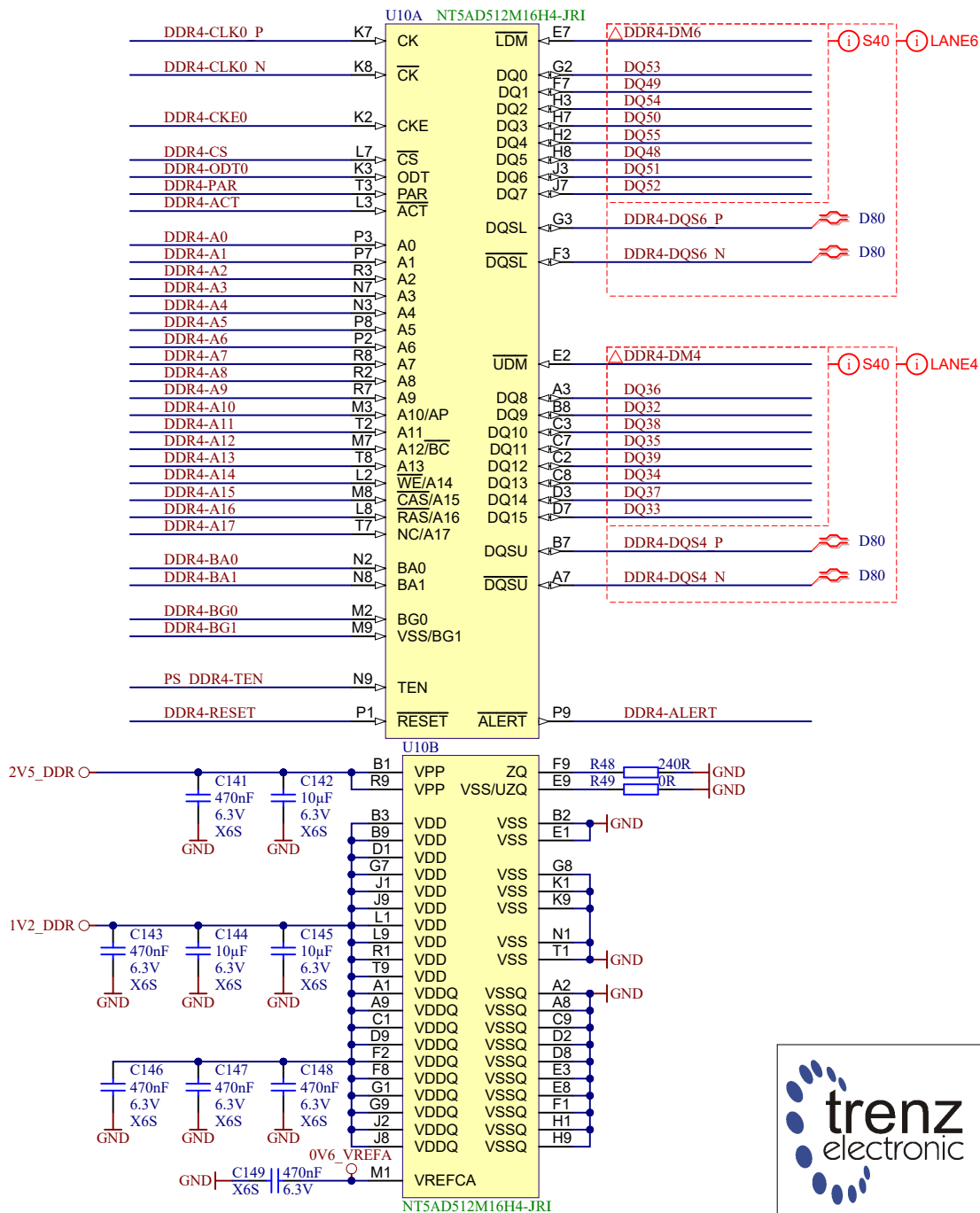
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|                             |                                  |               |
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| Date: 23.01.2026            | Copyright: Trenz Electronic GmbH | Page 22 of 35 |
| Filename: DDR4-RAM_2.SchDoc |                                  |               |

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A

B

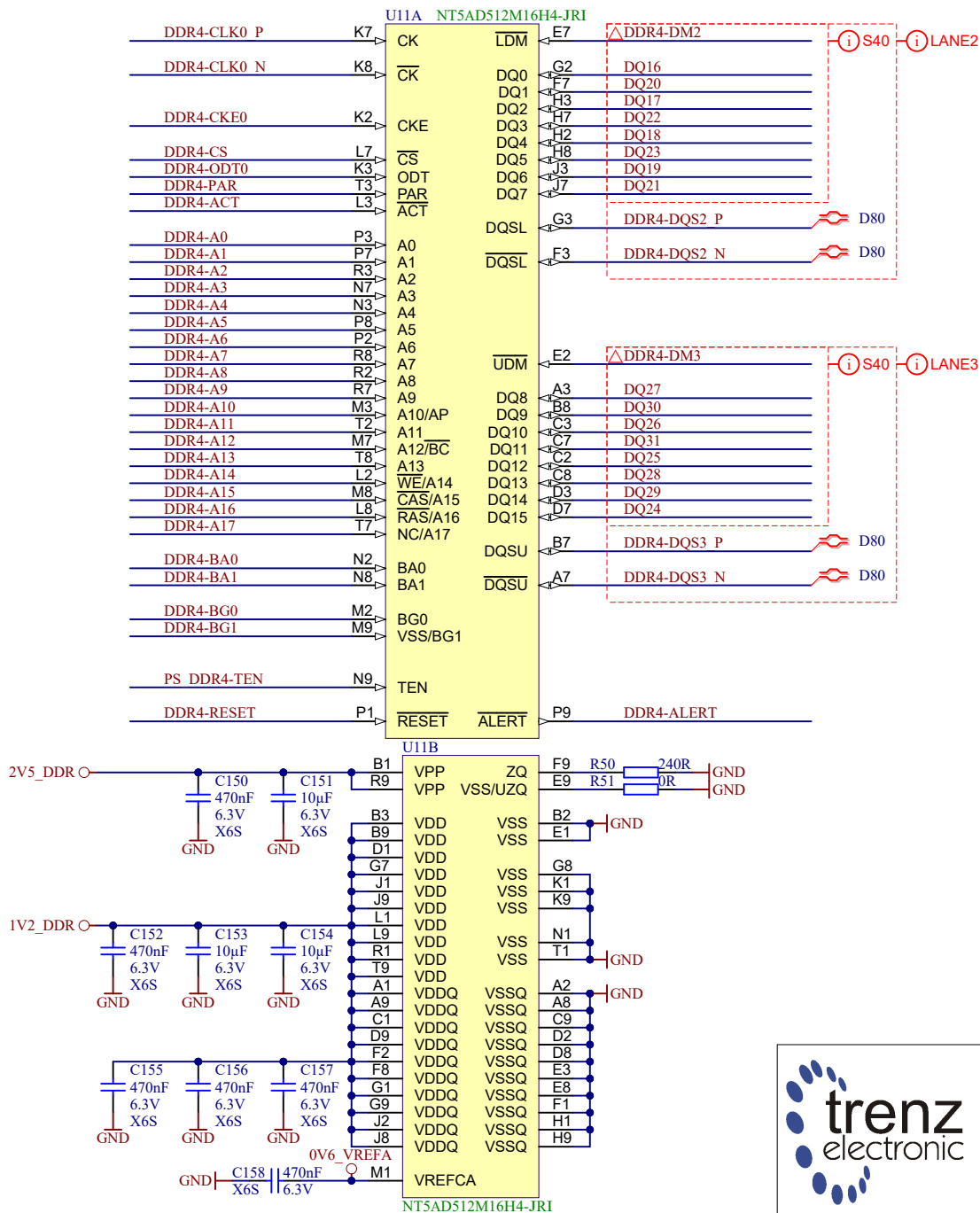
B

C

C

D

D



|                             |                                  |               |
|-----------------------------|----------------------------------|---------------|
| Title: TE0861 – DDR4-RAM_3  |                                  |               |
| A4                          | Number: TE0861 6BI51QA           | Rev. 01       |
| Date: 23.01.2026            | Copyright: Trenz Electronic GmbH | Page 23 of 35 |
| Filename: DDR4-RAM_3.SchDoc |                                  |               |

1

2

3

4

1

2

3

4

A

A

B

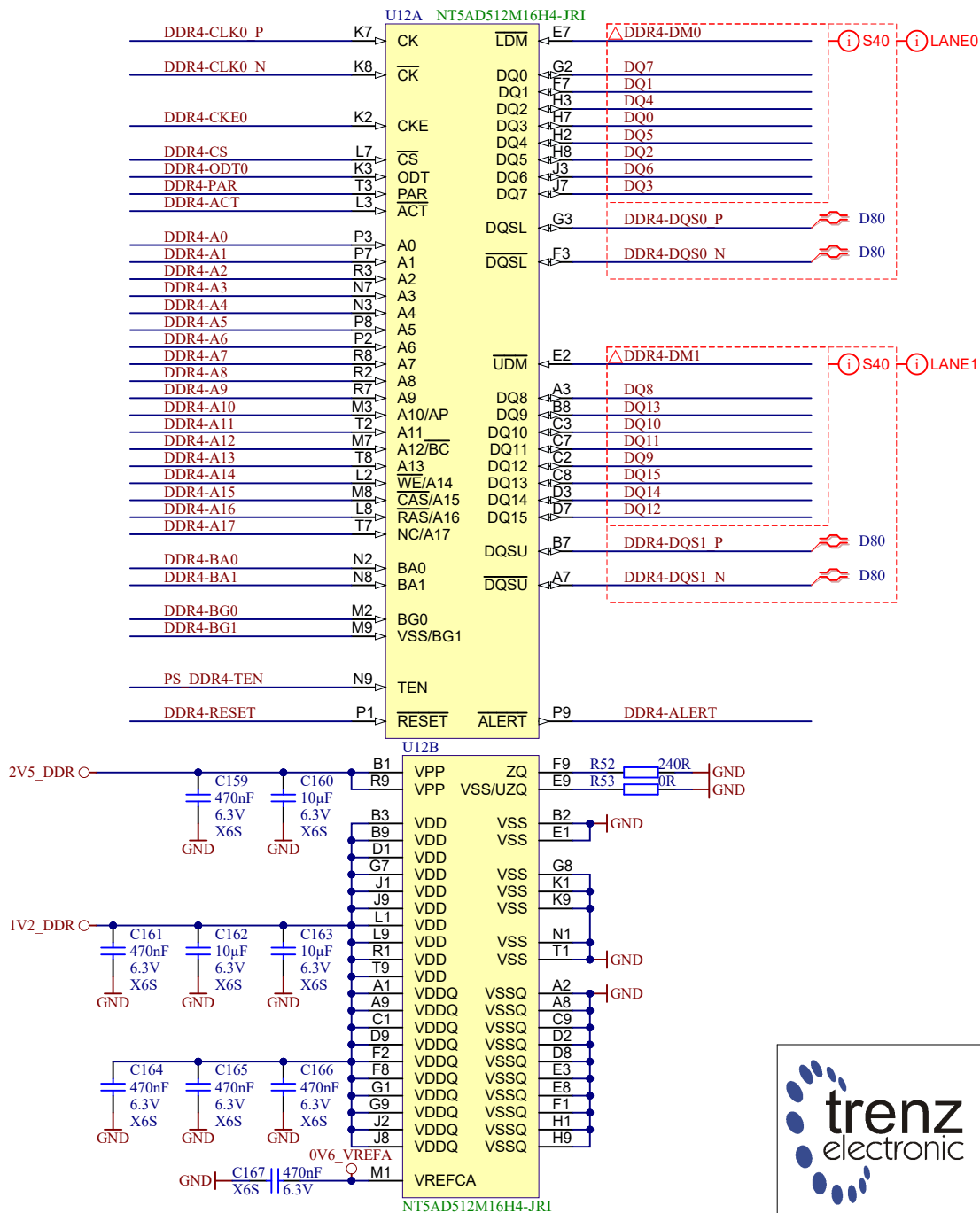
B

C

C

D

D



|                             |                                  |               |
|-----------------------------|----------------------------------|---------------|
| Title: TE0861 – DDR4-RAM_4  |                                  |               |
| A4                          | Number: TE0861 6BI51QA           | Rev. 01       |
| Date: 23.01.2026            | Copyright: Trenz Electronic GmbH | Page 24 of 35 |
| Filename: DDR4-RAM_4.SchDoc |                                  |               |

1

2

3

4

1

2

3

4

A

A

B

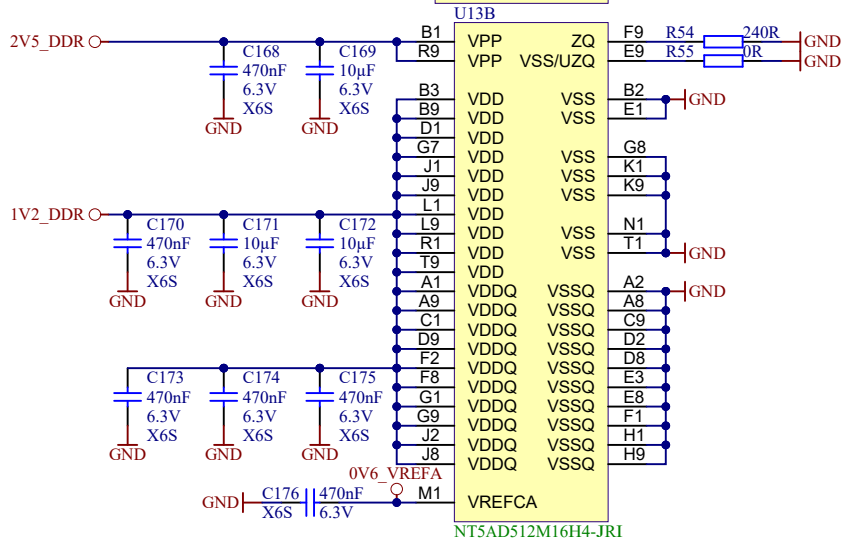
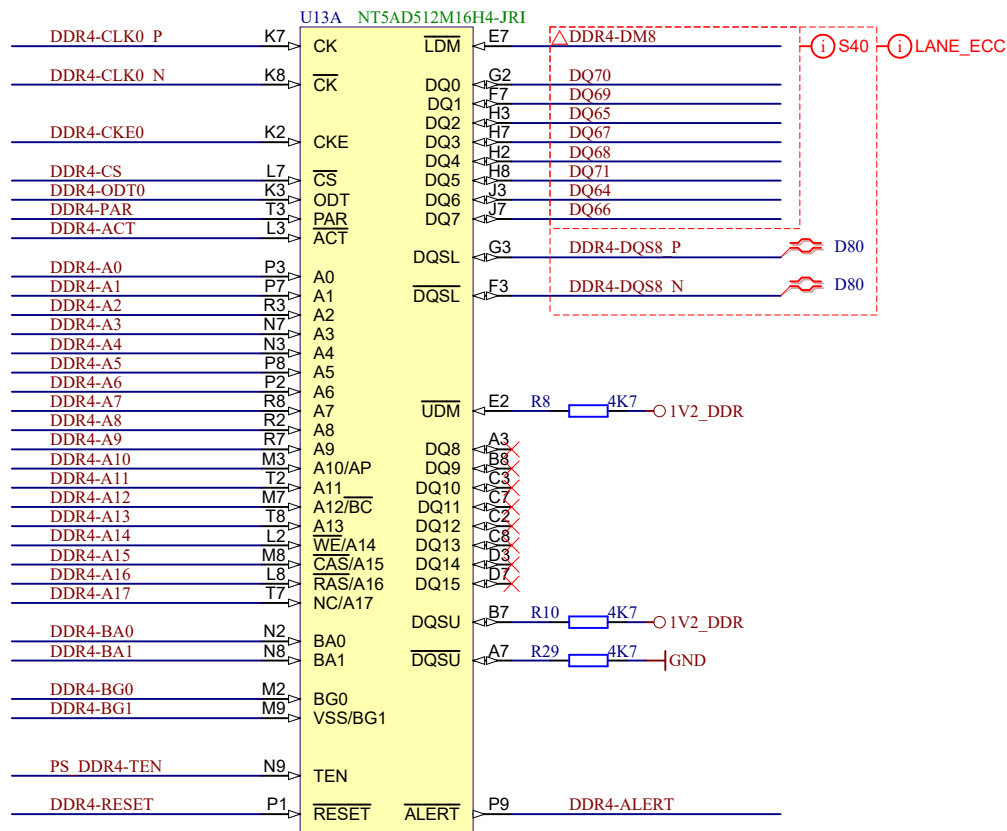
B

C

C

D

D



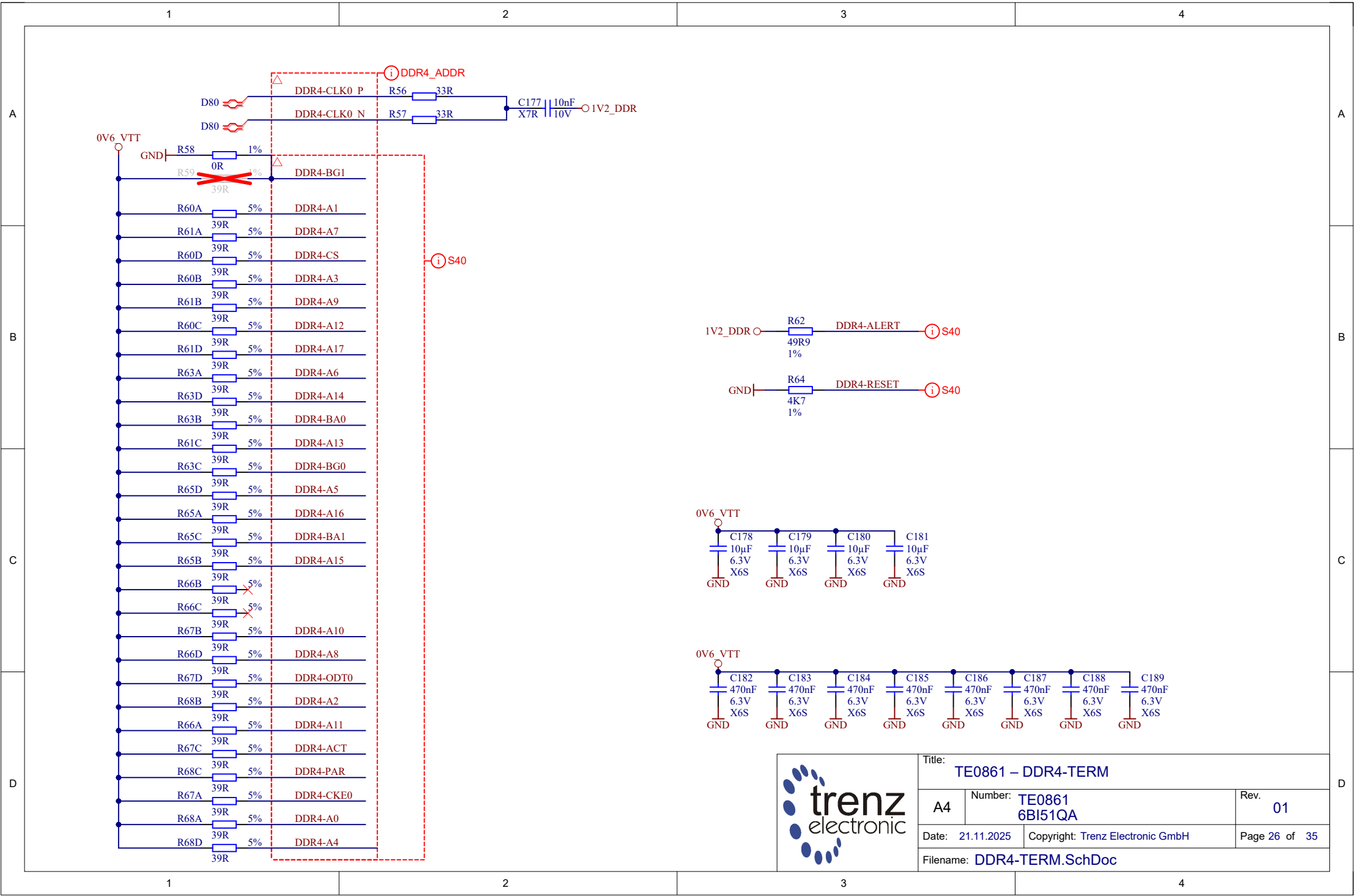
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| Title: TE0861 – DDR4-RAM_5  |                                  |               |
| A4                          | Number: TE0861 6BI51QA           | Rev. 01       |
| Date: 23.01.2026            | Copyright: Trenz Electronic GmbH | Page 25 of 35 |
| Filename: DDR4-RAM_5.SchDoc |                                  |               |

1

2

3

4



1

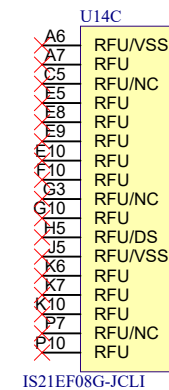
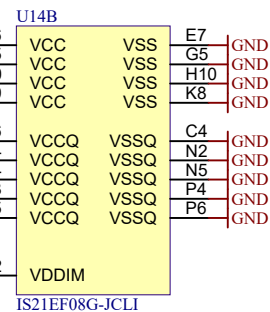
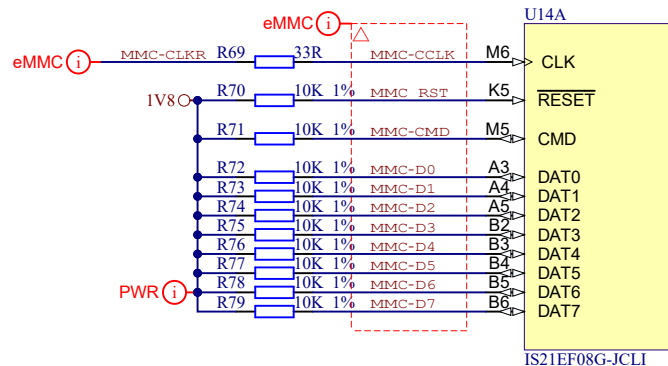
2

3

4

|      |    |    |     |
|------|----|----|-----|
| U14D |    |    |     |
| A1   | NC | NC | H2  |
| A2   | NC | NC | H3  |
| A8   | NC | NC | H12 |
| A9   | NC | NC | H13 |
| A10  | NC | NC | H14 |
| A11  | NC | NC | J1  |
| A12  | NC | NC | J2  |
| A13  | NC | NC | J3  |
| A14  | NC | NC | J12 |
| B1   | NC | NC | J13 |
| B7   | NC | NC | J14 |
| B8   | NC | NC | K1  |
| B9   | NC | NC | K2  |
| B10  | NC | NC | K3  |
| B11  | NC | NC | K12 |
| B12  | NC | NC | K13 |
| B13  | NC | NC | K14 |
| B14  | NC | NC | L1  |
| C1   | NC | NC | L2  |
| C3   | NC | NC | L3  |
| C7   | NC | NC | L12 |
| C8   | NC | NC | L13 |
| C9   | NC | NC | L14 |
| C10  | NC | NC | M1  |
| C11  | NC | NC | M2  |
| C12  | NC | NC | M3  |
| C13  | NC | NC | M7  |
| C14  | NC | NC | M8  |
| D1   | NC | NC | M9  |
| D2   | NC | NC | M10 |
| D3   | NC | NC | M11 |
| D4   | NC | NC | M12 |
| D12  | NC | NC | M13 |
| D13  | NC | NC | M14 |
| D14  | NC | NC | N1  |
| E1   | NC | NC | N3  |
| E2   | NC | NC | N6  |
| E3   | NC | NC | N7  |
| E12  | NC | NC | N8  |
| E13  | NC | NC | N9  |
| E14  | NC | NC | N10 |
| F1   | NC | NC | N11 |
| F2   | NC | NC | N12 |
| F3   | NC | NC | N13 |
| F12  | NC | NC | N14 |
| F13  | NC | NC | P1  |
| F14  | NC | NC | P2  |
| G1   | NC | NC | P8  |
| G2   | NC | NC | P9  |
| G12  | NC | NC | P11 |
| G13  | NC | NC | P12 |
| G14  | NC | NC | P13 |
| H1   | NC | NC | P14 |

IS21EF08G-JCLI



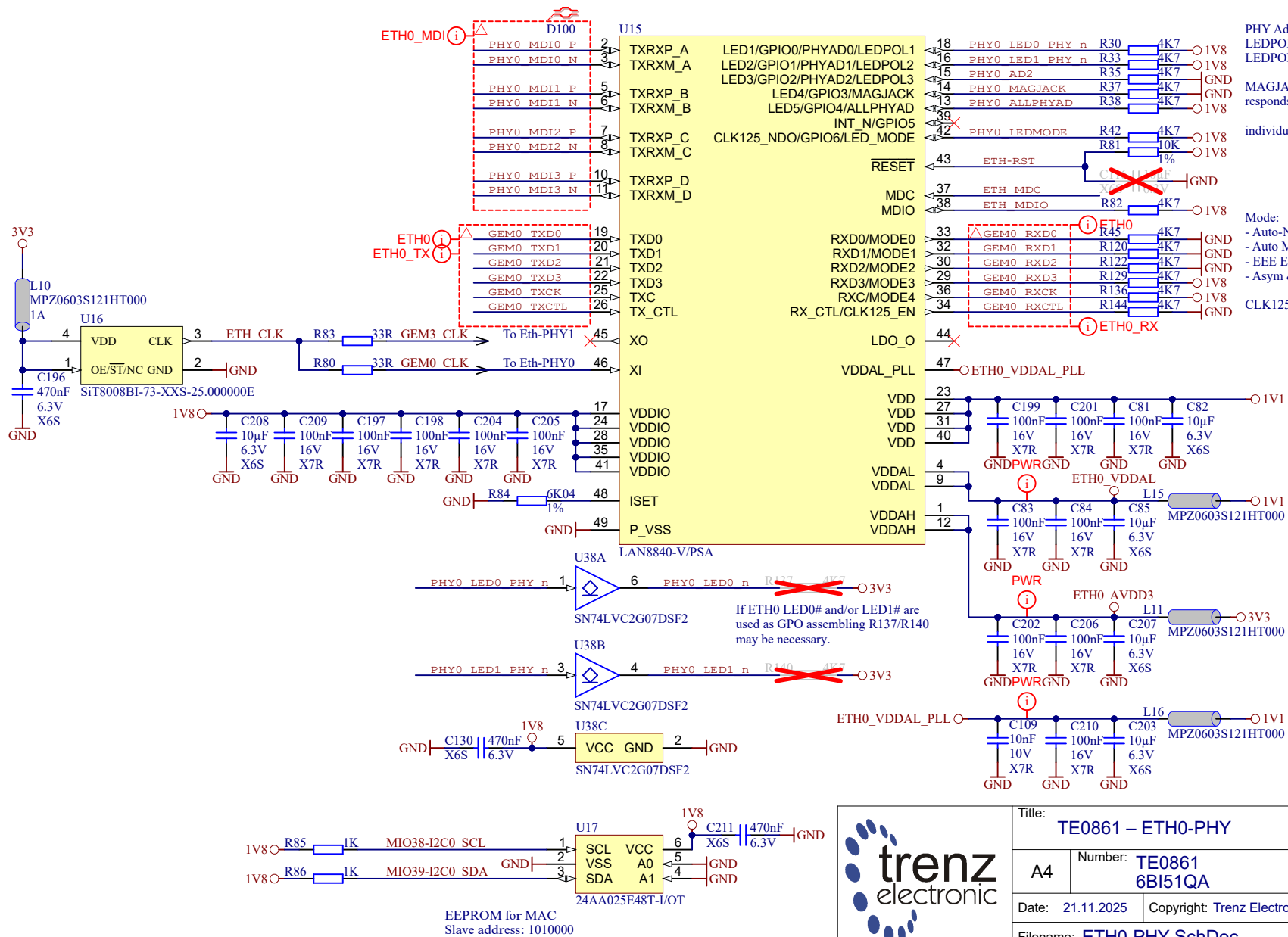
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|-----------------------|----------------------------------|---------------|
| Title: TE0861 – eMMC  |                                  |               |
| A4                    | Number: TE0861<br>6BI51QA        | Rev. 01       |
| Date: 21.11.2025      | Copyright: Trenz Electronic GmbH | Page 27 of 35 |
| Filename: eMMC.SchDoc |                                  |               |

1

2

3

4



PHY Addr: 0x03  
LEDPOL -> Active Low  
LEDPOL -> Active Low  
MAGJACK normal mode  
responds only to assigned PHY Addr.

individual LED mode

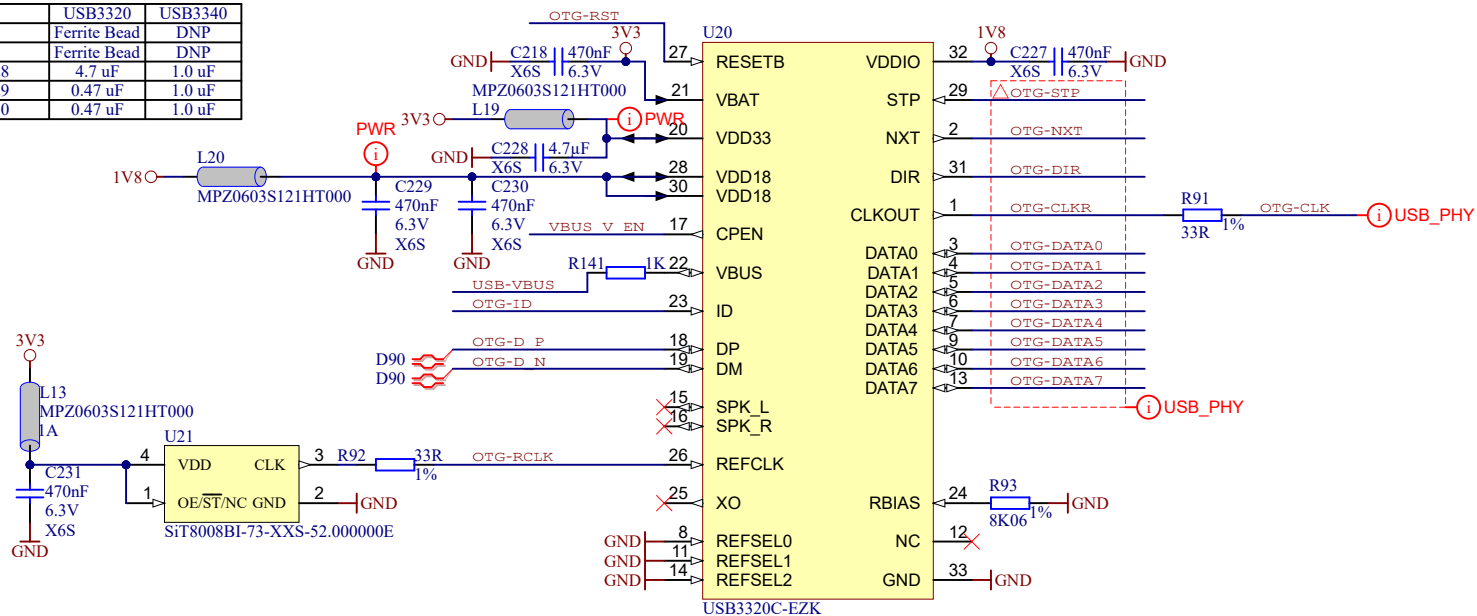
Mode:  
- Auto-Neg. Enabled  
- Auto MDIX Enabled  
- EEE Enabled  
- Asym & Sym Pause Advertised  
CLK125 disabled



|                           |                                  |               |
|---------------------------|----------------------------------|---------------|
| Title: TE0861 - ETH0-PHY  |                                  |               |
| A4                        | Number: TE0861 6BI51QA           | Rev. 01       |
| Date: 21.11.2025          | Copyright: Trenz Electronic GmbH | Page 28 of 35 |
| Filename: ETH0-PHY.SchDoc |                                  |               |



|      | USB3320      | USB3340 |
|------|--------------|---------|
| L19  | Ferrite Bead | DNP     |
| L20  | Ferrite Bead | DNP     |
| C228 | 4.7 uF       | 1.0 uF  |
| C229 | 0.47 uF      | 1.0 uF  |
| C230 | 0.47 uF      | 1.0 uF  |

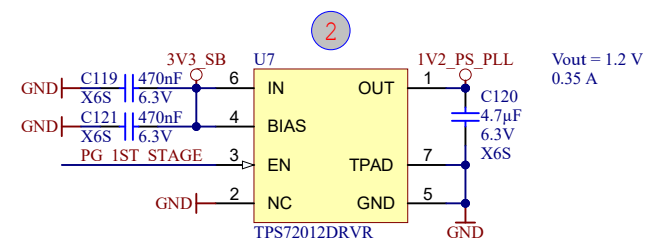
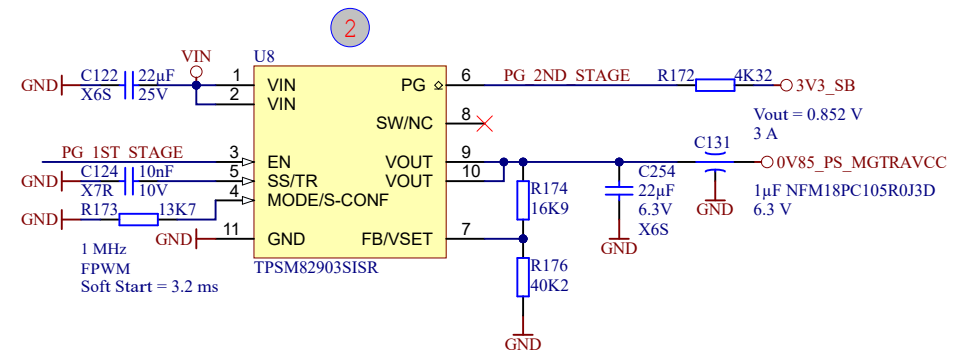
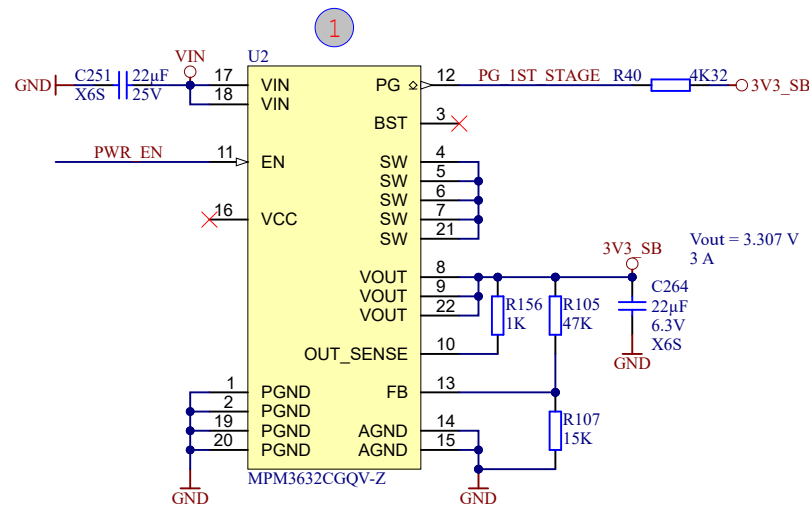
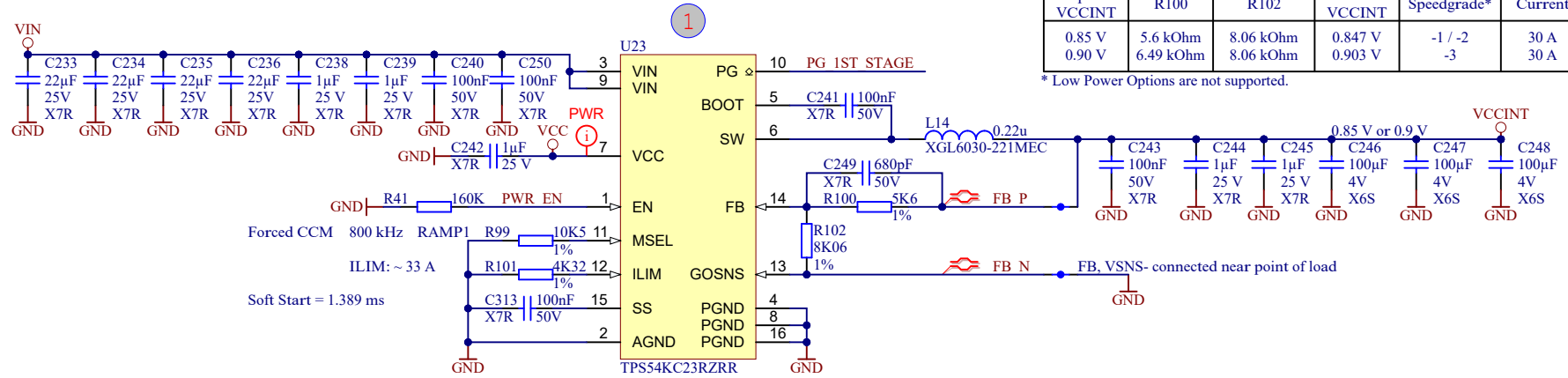


|                                                                                       |  |                                  |                |
|---------------------------------------------------------------------------------------|--|----------------------------------|----------------|
|  |  | Title: TE0861 – USB-PHY          |                |
|                                                                                       |  | A4 Number: <b>TE0861 6BI51QA</b> | Rev. <b>01</b> |
| Date: 21.11.2025                                                                      |  | Copyright: Trenz Electronic GmbH |                |
| Filename: USB-PHY.SchDoc                                                              |  | Page 30 of 35                    |                |



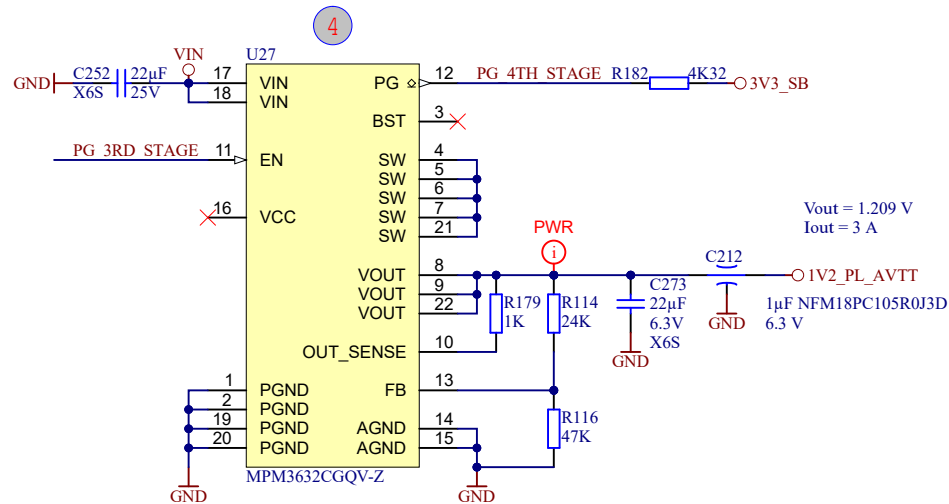
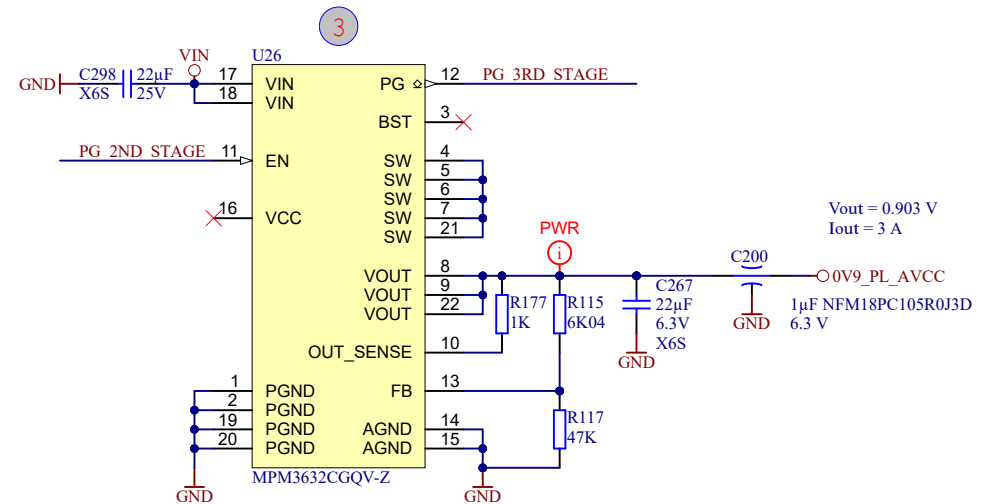
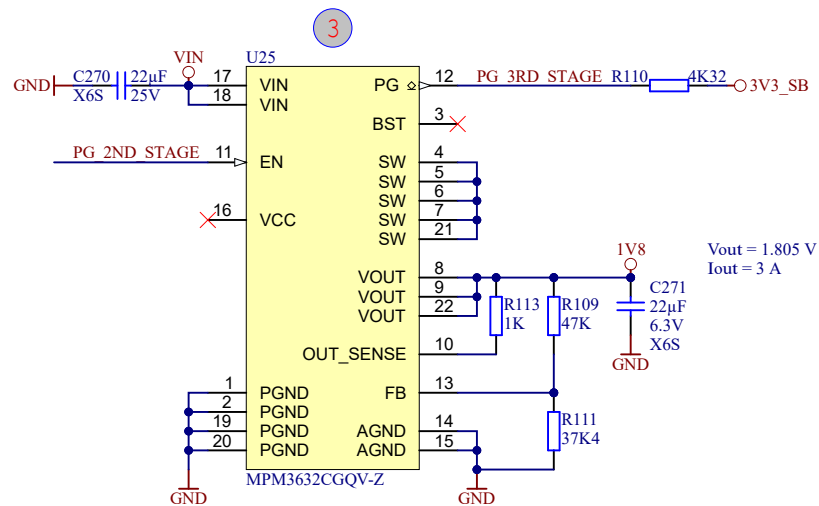
| Required VCCINT | R100      | R102      | Calculated VCCINT | Speedgrade* | Current |
|-----------------|-----------|-----------|-------------------|-------------|---------|
| 0.85 V          | 5.6 kOhm  | 8.06 kOhm | 0.847 V           | -1 / -2     | 30 A    |
| 0.90 V          | 6.49 kOhm | 8.06 kOhm | 0.903 V           | -3          | 30 A    |

\* Low Power Options are not supported.

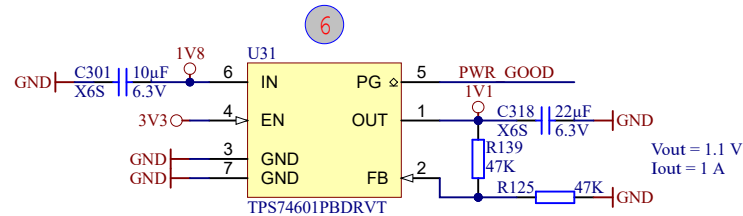
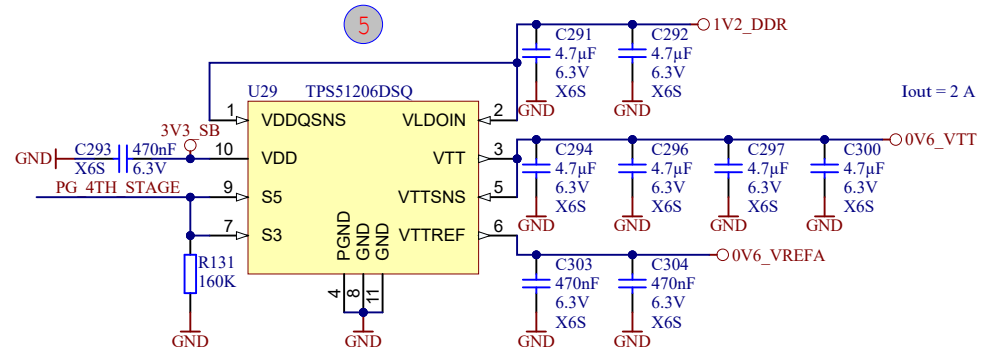
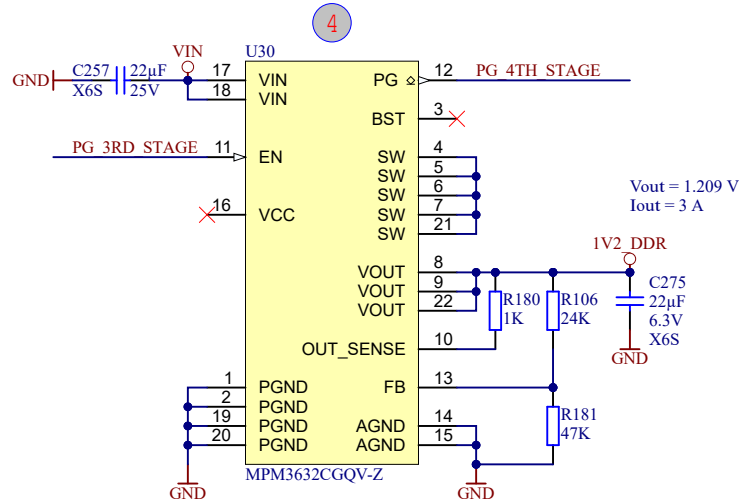
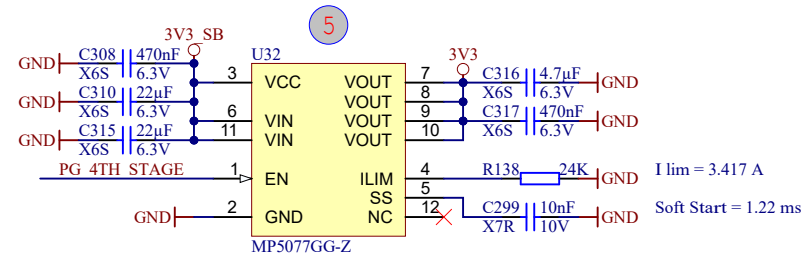
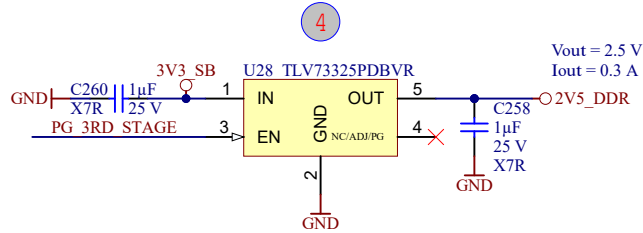


Title: **TE0861 – POWER 1**

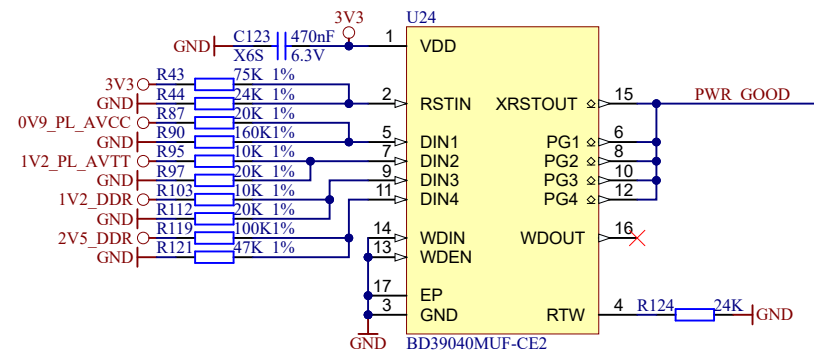
|                                 |                                  |                |
|---------------------------------|----------------------------------|----------------|
| A4                              | Number: <b>TE0861 6BI51QA</b>    | Rev. <b>01</b> |
| Date: 21.11.2025                | Copyright: Trenz Electronic GmbH |                |
| Filename: <b>POWER_1.SchDoc</b> |                                  | Page 32 of 35  |



|                          |                                  |               |
|--------------------------|----------------------------------|---------------|
| Title: TE0861 – POWER 2  |                                  |               |
| A4                       | Number: TE0861 6BI51QA           | Rev. 01       |
| Date: 21.11.2025         | Copyright: Trenz Electronic GmbH | Page 33 of 35 |
| Filename: POWER_2.SchDoc |                                  |               |



|                          |                                  |               |
|--------------------------|----------------------------------|---------------|
| Title: TE0861 – POWER 3  |                                  |               |
| A4                       | Number: TE0861 6BI51QA           | Rev. 01       |
| Date: 21.11.2025         | Copyright: Trenz Electronic GmbH | Page 34 of 35 |
| Filename: POWER_3.SchDoc |                                  |               |



| VCCINT | R34     | R39      |
|--------|---------|----------|
| 0.85 V | 10 kOhm | 160 kOhm |
| 0.90 V | 20 kOhm | 160 kOhm |

