

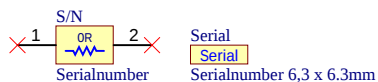
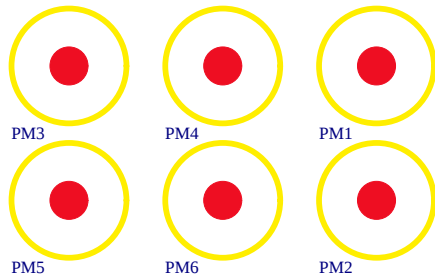
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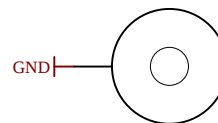
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U_SFP SFP.SchDoc	U_USBH USB3-HUB.SchDoc	U_POWER_OVERVIEW Power_overview.SchDoc
U_SSD SSD.SchDoc	U_CLK Clock.SchDoc	U_POWER_SEQ PowerUpSequence.SchDoc
U_FrontPanel FrontPanel.SchDoc	U_I2C I2C_system.schdoc	
U_USB USB.SchDoc	U_eMMC eMMC.SchDoc	
U_BackPanel BackPanel.SchDoc	U_DDRP DDR_PWR.SchDoc	
U_FMC PWR FMC PWR.SchDoc	U_DDR DDR_SODIMM.SchDoc	
U_ZU ZU.SchDoc	U_CLK2 Clock2.SchDoc	
U_SFP_CTRL SFP_CTRL.SchDoc	U_SC2 SC2.schdoc	
U_SC SC.schdoc	U_MISC MISC.schdoc	
U_UPWR USB3-HUB_PWR.SchDoc	U_PWR_IN PWR_IN.SchDoc	
U_FAN FAN_CON.schdoc	U_REVCH Revision_changes.SchDoc	



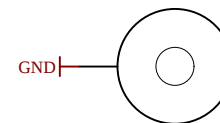
LOGO1

TE Logo PRINT Layer

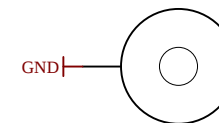
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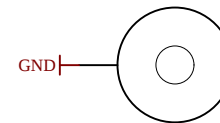
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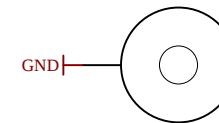
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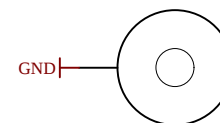
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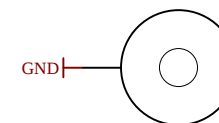
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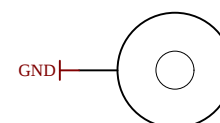
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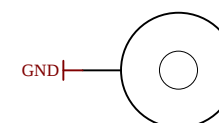
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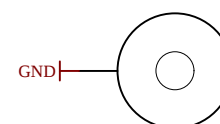
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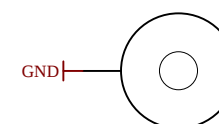
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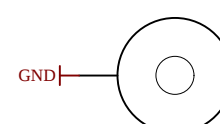
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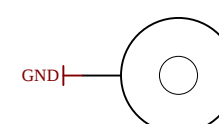
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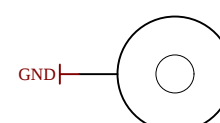
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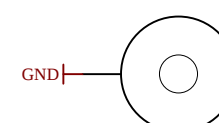
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Mount.Hole 3.2mm für Unterlegscheibe



Mount.Hole 3.2mm für Unterlegscheibe



Mount.Hole 3.2mm für Unterlegscheibe



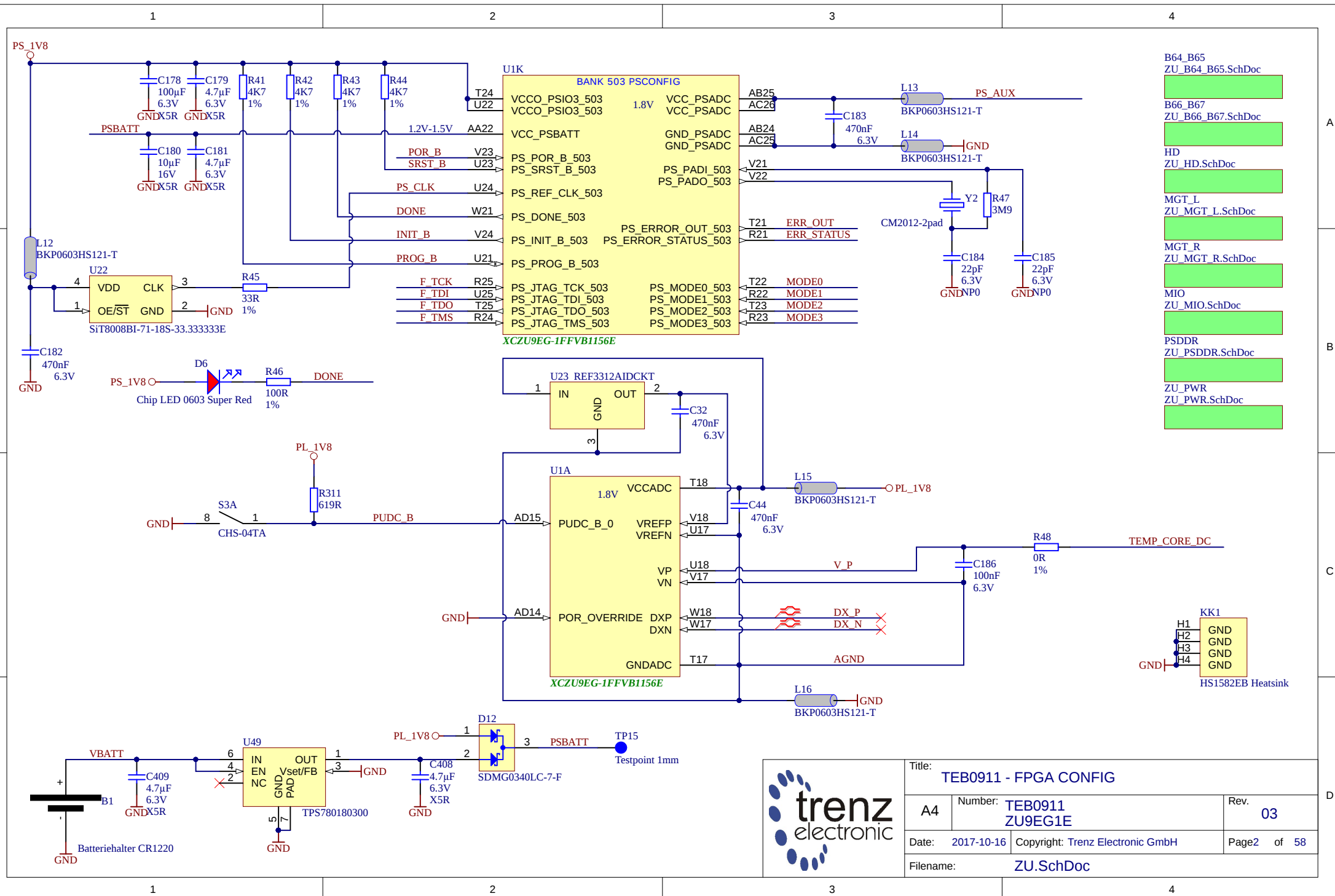
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A4	Number: TEB0911 ZU9EG1E	Rev. 03
Date: 2017-10-16	Copyright: Trenz Electronic GmbH	
Filename: TEB0911.SchDoc		Page1 of 58

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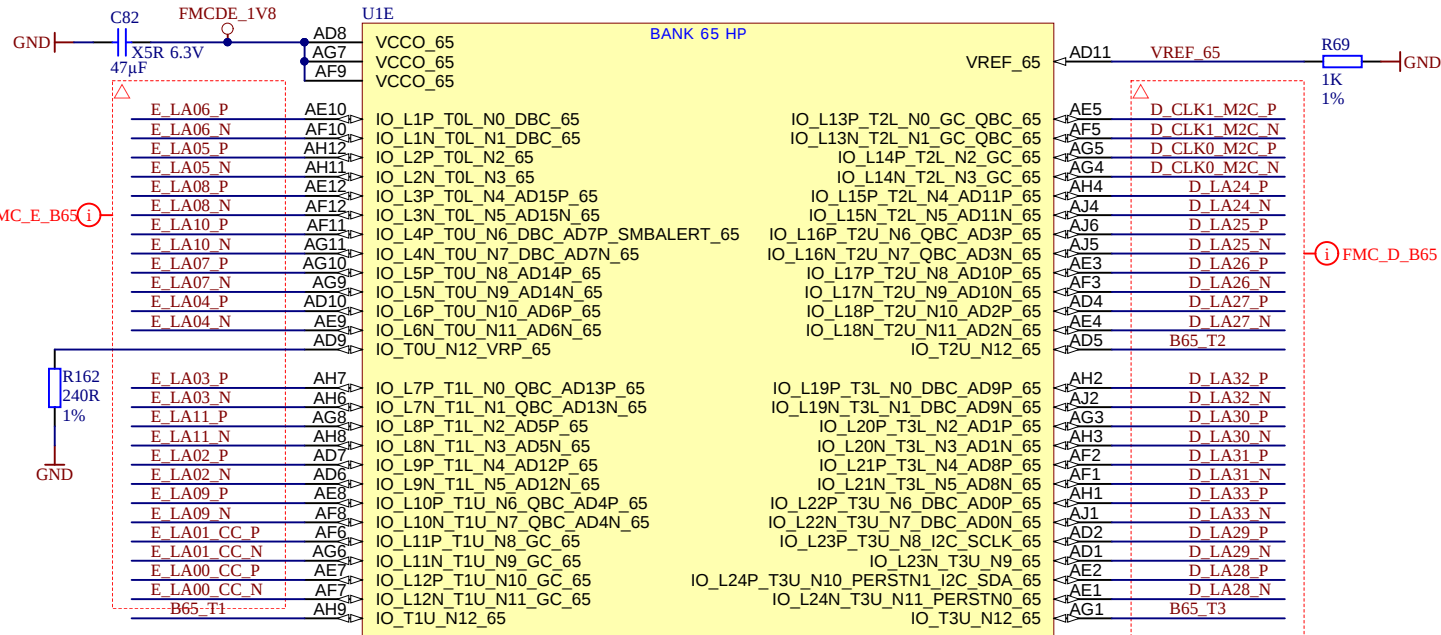
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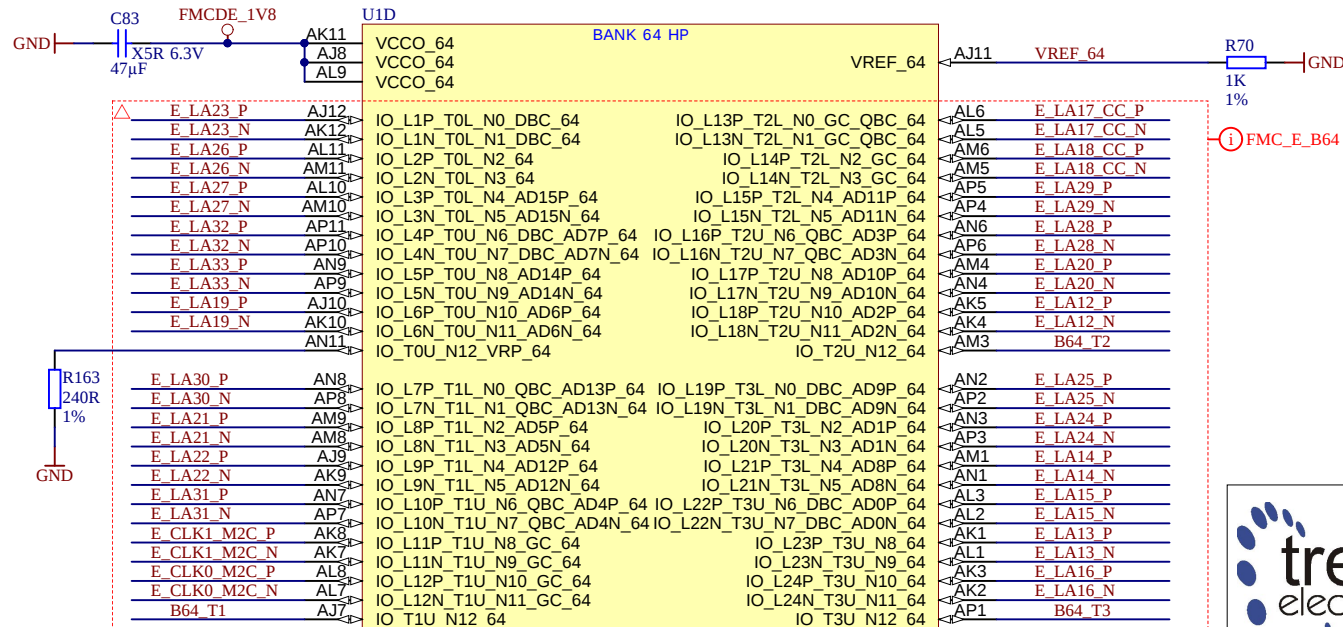
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XCZU9EG-1FFVB1156E



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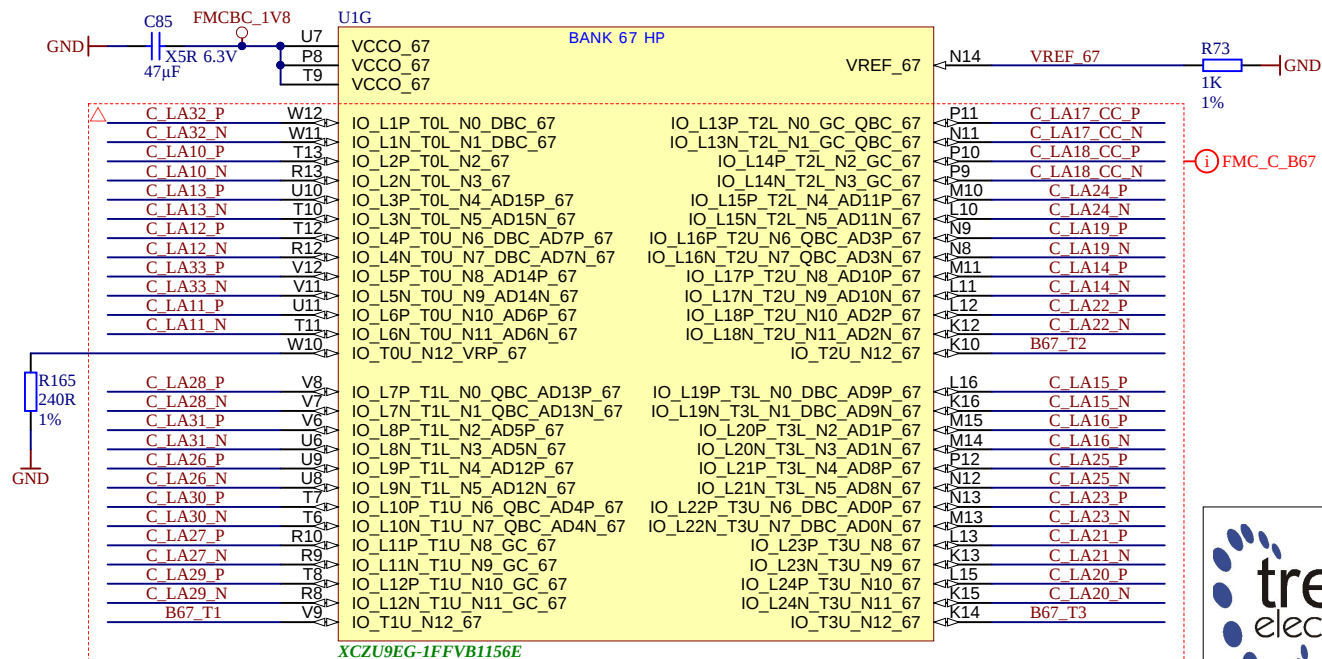
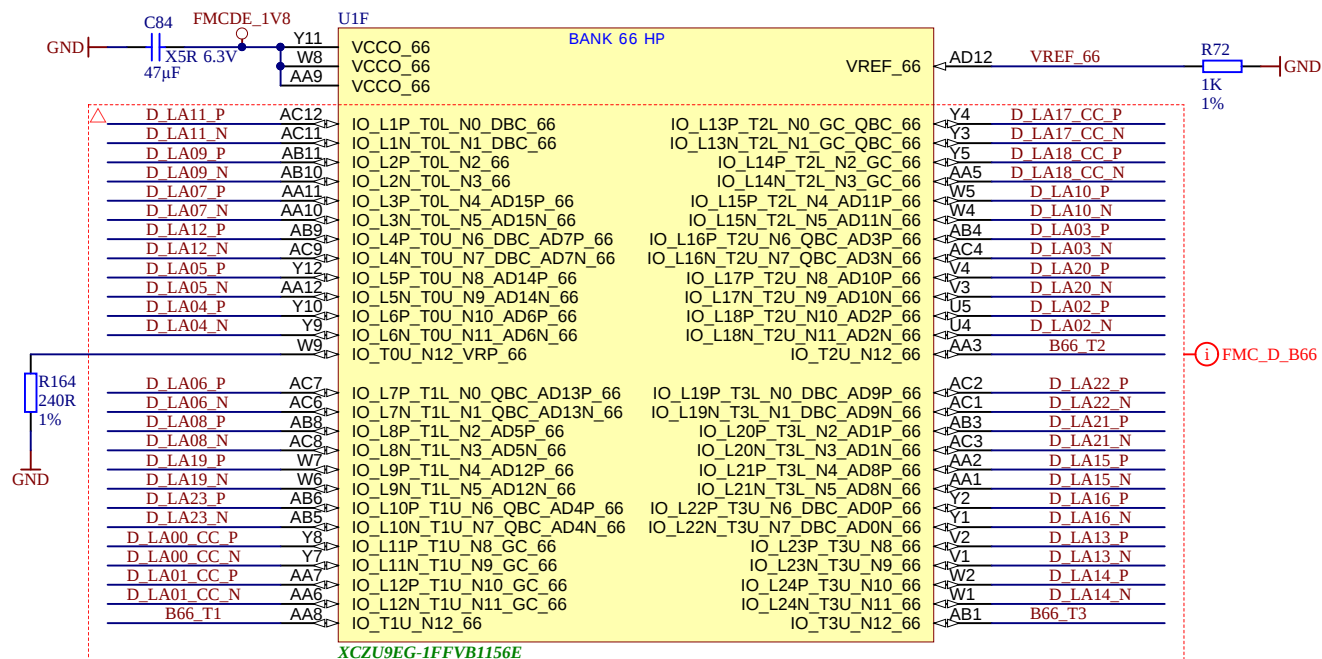
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Date: 2017-10-16	Copyright: Trenz Electronic GmbH		Page3 of 58
Filename: ZU B64 B65.SchDoc			

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Title: TEB0911 - FPGA B66 B67			
A4	Number: TEB0911 ZU9EG1E	Rev. 03	
Date: 2017-10-16	Copyright: Trenz Electronic GmbH	Page 4	of 58
Filename: ZU B66 B67.SchDoc			

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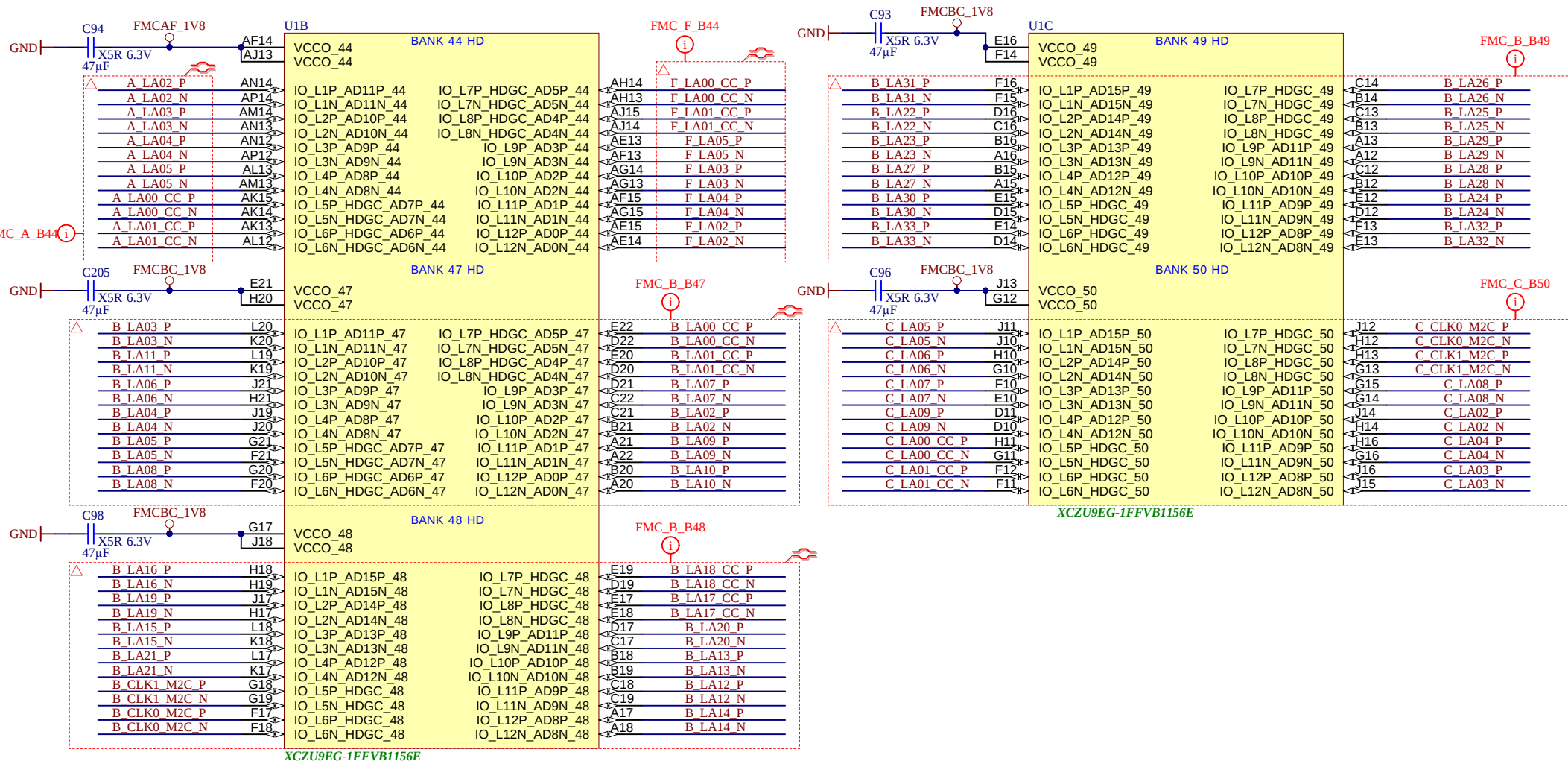
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XCZU9EG-1FFVB1156E



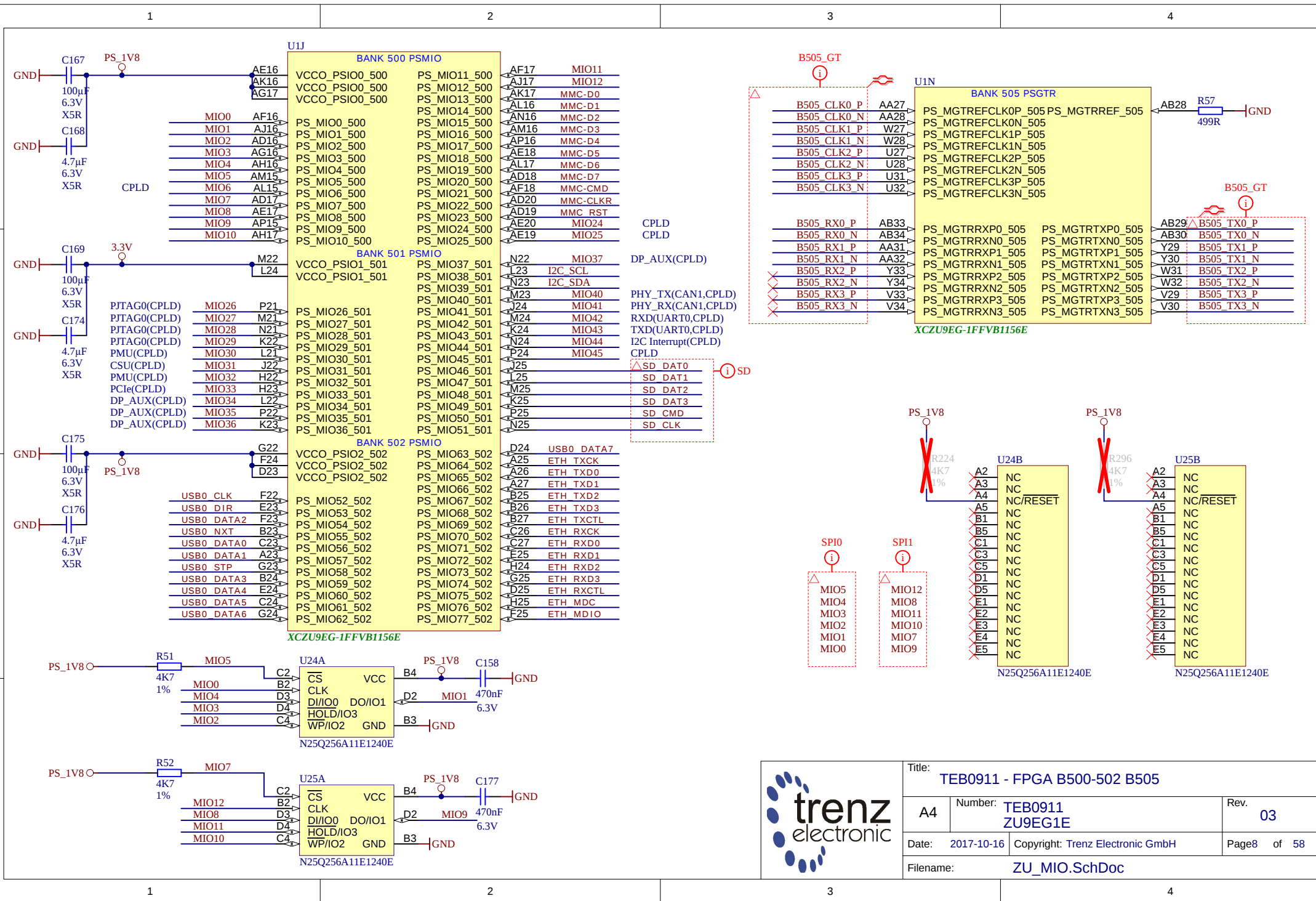
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Date: 2017-10-16	Copyright: Trenz Electronic GmbH		Page5 of 58
Filename: ZU HD.SchDoc			

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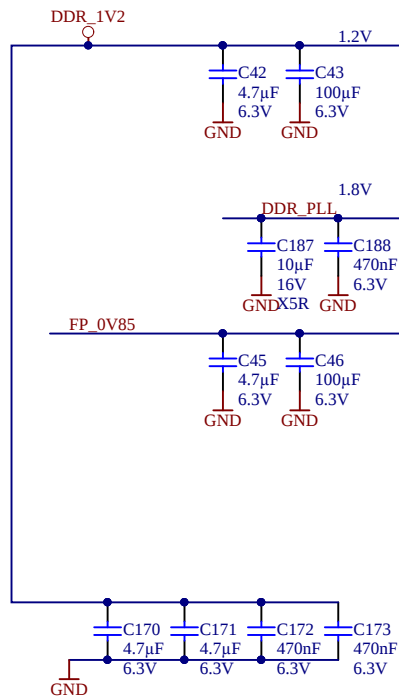
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A4	Number: TEB0911 ZU9EG1E		Rev. 03
Date: 2017-10-16	Copyright: Trenz Electronic GmbH		Page8 of 58
Filename: ZU_MIO.SchDoc			

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U1L

BANK 504 PSDDR

AN30	VCCO_PSDDR_504	PS_DDR_CK0_504	AN26	DDR4-CLK0_P
AG27	VCCO_PSDDR_504	PS_DDR_CK_N0_504	AN27	DDR4-CLK0_N
AN25	VCCO_PSDDR_504	PS_DDR_CKE0_504	AN29	DDR4-CKE0
AL29	VCCO_PSDDR_504			
AL24	VCCO_PSDDR_504	PS_DDR_CK1_504	AL26	DDR4-CLK1_P
AJ24	VCCO_PSDDR_504	PS_DDR_CK_N1_504	AL27	DDR4-CLK1_N
AJ28	VCCO_PSDDR_504	PS_DDR_CKE1_504	AJ27	DDR4-CKE1
	VCCO_PSDDR_504			
Y24	VCC_PSDDR_PLL	PS_DDR_A0_504	AP29	DDR4-A0
Y25	VCC_PSDDR_PLL	PS_DDR_A1_504	AP30	DDR4-A1
		PS_DDR_A2_504	AP26	DDR4-A2
AE25	VCC_PSINTFP_DDR	PS_DDR_A3_504	AP27	DDR4-A3
AE24	VCC_PSINTFP_DDR	PS_DDR_A4_504	AP25	DDR4-A4
AD24	VCC_PSINTFP_DDR	PS_DDR_A5_504	AN24	DDR4-A5
		PS_DDR_A6_504	AM29	DDR4-A6
		PS_DDR_A7_504	AM28	DDR4-A7
		PS_DDR_A8_504	AM26	DDR4-A8
		PS_DDR_A9_504	AM25	DDR4-A9
		PS_DDR_A10_504	AL28	DDR4-A10
		PS_DDR_A11_504	AK27	DDR4-A11
		PS_DDR_A12_504	AJ25	DDR4-A12
		PS_DDR_A13_504	AL25	DDR4-A13
		PS_DDR_A14_504	AK25	DDR4-A14
		PS_DDR_A15_504	AK24	DDR4-A15
		PS_DDR_A16_504	AM24	DDR4-A16
		PS_DDR_A17_504	AF25	DDR4-A17
		PS_DDR_CS_N0_504	AN28	DDR4-CS_N0
		PS_DDR_CS_N1_504	AL30	DDR4-CS_N1
		PS_DDR_BA0_504	AH26	DDR4-BA0
		PS_DDR_BA1_504	AG26	DDR4-BA1
		PS_DDR_BG0_504	AK28	DDR4-BG0
		PS_DDR_BG1_504	AH27	DDR4-BG1
		PS_DDR_PARITY_504	AF20	DDR4-PAR
		PS_DDR_RAM_RST_N_504	AF21	DDR4-RESET
		PS_DDR_ACT_N_504	AG25	DDR4-ACT
		PS_DDR_ALERT_N_504	AF22	DDR4-ALERT
		PS_DDR_ZQ_504	AF23	
		PS_DDR_ODT0_504	AM30	DDR4-ODT0
		PS_DDR_ODT1_504	AJ26	DDR4-ODT1

XCZU9EG-1FFVB1156E

ADDR_CMD_CTRL

DDR4-A[0..16]
DDR4-CS_N0
DDR4-CS_N1
DDR4-BA0
DDR4-BA1
DDR4-BG0
DDR4-BG1
DDR4-PAR
DDR4-ACT
DDR4-ODT0
DDR4-ODT1
DDR4-CKE0
DDR4-CKE1
DDR4-CLK0_P
DDR4-CLK0_N
DDR4-CLK1_P
DDR4-CLK1_N

LANE0

DO[0..7]
DDR4-DQS0_P
DDR4-DQS0_N
DDR4-DM0

LANE1

DO[8..15]
DDR4-DQS1_P
DDR4-DQS1_N
DDR4-DM1

LANE2

DO[16..23]
DDR4-DQS2_P
DDR4-DQS2_N
DDR4-DM2

LANE3

DO[24..31]
DDR4-DQS3_P
DDR4-DQS3_N
DDR4-DM3

LANE4

DO[32..39]
DDR4-DQS4_P
DDR4-DQS4_N
DDR4-DM4

LANE5

DO[40..47]
DDR4-DQS5_P
DDR4-DQS5_N
DDR4-DM5

LANE6

DO[48..55]
DDR4-DQS6_P
DDR4-DQS6_N
DDR4-DM6

LANE7

DO[56..63]
DDR4-DQS7_P
DDR4-DQS7_N
DDR4-DM7

LANE_ECC

CB[0..7]
DDR4-DQS8_P
DDR4-DQS8_N
DDR4-DM8

U1M

BANK 504 PSDDR

DQ0	AP20	PS_DDR_DQ0_504	PS_DDR_DQ32_504	AG31	DQ32
DQ1	AP18	PS_DDR_DQ1_504	PS_DDR_DQ33_504	AG30	DQ33
DQ2	AP19	PS_DDR_DQ2_504	PS_DDR_DQ34_504	AG29	DQ34
DQ3	AP17	PS_DDR_DQ3_504	PS_DDR_DQ35_504	AG28	DQ35
DQ4	AM20	PS_DDR_DQ4_504	PS_DDR_DQ36_504	AJ30	DQ36
DQ5	AM19	PS_DDR_DQ5_504	PS_DDR_DQ37_504	AK29	DQ37
DQ6	AM18	PS_DDR_DQ6_504	PS_DDR_DQ38_504	AK30	DQ38
DQ7	AL18	PS_DDR_DQ7_504	PS_DDR_DQ39_504	AJ29	DQ39
DQ8	AP22	PS_DDR_DQ8_504	PS_DDR_DQ40_504	AE27	DQ40
DQ9	AP21	PS_DDR_DQ9_504	PS_DDR_DQ41_504	AF28	DQ41
DQ10	AP24	PS_DDR_DQ10_504	PS_DDR_DQ42_504	AF30	DQ42
DQ11	AM23	PS_DDR_DQ11_504	PS_DDR_DQ43_504	AF31	DQ43
DQ12	AL21	PS_DDR_DQ12_504	PS_DDR_DQ44_504	AD28	DQ44
DQ13	AL22	PS_DDR_DQ13_504	PS_DDR_DQ45_504	AD27	DQ45
DQ14	AM23	PS_DDR_DQ14_504	PS_DDR_DQ46_504	AD29	DQ46
DQ15	AL23	PS_DDR_DQ15_504	PS_DDR_DQ47_504	AD30	DQ47
DQ16	AL20	PS_DDR_DQ16_504	PS_DDR_DQ48_504	AH33	DQ48
DQ17	AK20	PS_DDR_DQ17_504	PS_DDR_DQ49_504	AJ34	DQ49
DQ18	AJ20	PS_DDR_DQ18_504	PS_DDR_DQ50_504	AH34	DQ50
DQ19	AK18	PS_DDR_DQ19_504	PS_DDR_DQ51_504	AH32	DQ51
DQ20	AG20	PS_DDR_DQ20_504	PS_DDR_DQ52_504	AK34	DQ52
DQ21	AH18	PS_DDR_DQ21_504	PS_DDR_DQ53_504	AK33	DQ53
DQ22	AG19	PS_DDR_DQ22_504	PS_DDR_DQ54_504	AL32	DQ54
DQ23	AK18	PS_DDR_DQ23_504	PS_DDR_DQ55_504	AL31	DQ55
DQ24	AG21	PS_DDR_DQ24_504	PS_DDR_DQ56_504	AG33	DQ56
DQ25	AH21	PS_DDR_DQ25_504	PS_DDR_DQ57_504	AG34	DQ57
DQ26	AG24	PS_DDR_DQ26_504	PS_DDR_DQ58_504	AF32	DQ58
DQ27	AG23	PS_DDR_DQ27_504	PS_DDR_DQ59_504	AF33	DQ59
DQ28	AK22	PS_DDR_DQ28_504	PS_DDR_DQ60_504	AD31	DQ60
DQ29	AJ21	PS_DDR_DQ29_504	PS_DDR_DQ61_504	AD32	DQ61
DQ30	AJ22	PS_DDR_DQ30_504	PS_DDR_DQ62_504	AD34	DQ62
DQ31	AK23	PS_DDR_DQ31_504	PS_DDR_DQ63_504	AD33	DQ63
		PS_DDR_DQ64_504	PS_DDR_DQ65_504	AN31	CB0
DDR4-DQS0_P	AN18	PS_DDR_DQS_P0_504	PS_DDR_DQ66_504	AP31	CB1
DDR4-DQS0_N	AN19	PS_DDR_DQS_N0_504	PS_DDR_DQ67_504	AP32	CB2
DDR4-DQS1_P	AN21	PS_DDR_DQS_P1_504	PS_DDR_DQ68_504	AP33	CB3
DDR4-DQS1_N	AN22	PS_DDR_DQS_N1_504	PS_DDR_DQ69_504	AM31	CB4
DDR4-DQS2_P	AH19	PS_DDR_DQS_P2_504	PS_DDR_DQ70_504	AM33	CB5
DDR4-DQS2_N	AJ19	PS_DDR_DQS_N2_504	PS_DDR_DQ71_504	AM34	CB6
DDR4-DQS3_P	AH22	PS_DDR_DQS_P3_504		AL33	CB7
DDR4-DQS3_N	AH23	PS_DDR_DQS_N3_504			
DDR4-DQS4_P	AH28	PS_DDR_DQS_P4_504			
DDR4-DQS4_N	AH29	PS_DDR_DQS_N4_504	PS_DDR_DM0_504	AN17	DDR4-DM0
DDR4-DQS5_P	AE28	PS_DDR_DQS_P5_504	PS_DDR_DM1_504	AM21	DDR4-DM1
DDR4-DQS5_N	AE29	PS_DDR_DQS_N5_504	PS_DDR_DM2_504	AK19	DDR4-DM2
DDR4-DQS6_P	AJ32	PS_DDR_DQS_P6_504	PS_DDR_DM3_504	AH24	DDR4-DM3
DDR4-DQS6_N	AK32	PS_DDR_DQS_N6_504	PS_DDR_DM4_504	AH31	DDR4-DM4
DDR4-DQS7_P	AE32	PS_DDR_DQS_P7_504	PS_DDR_DM5_504	AE30	DDR4-DM5
DDR4-DQS7_N	AE33	PS_DDR_DQS_N7_504	PS_DDR_DM6_504	AJ31	DDR4-DM6
DDR4-DQS8_P	AN32	PS_DDR_DQS_P8_504	PS_DDR_DM7_504	AE34	DDR4-DM7
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XCZU9EG-1FFVB1156E



Title:

TEB0911 - FPGA B504

A4

Number:

TEB0911
ZU9EG1E

Rev.

03

Date:

2017-10-16

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Filename:

ZU_PSDDR.SchDoc

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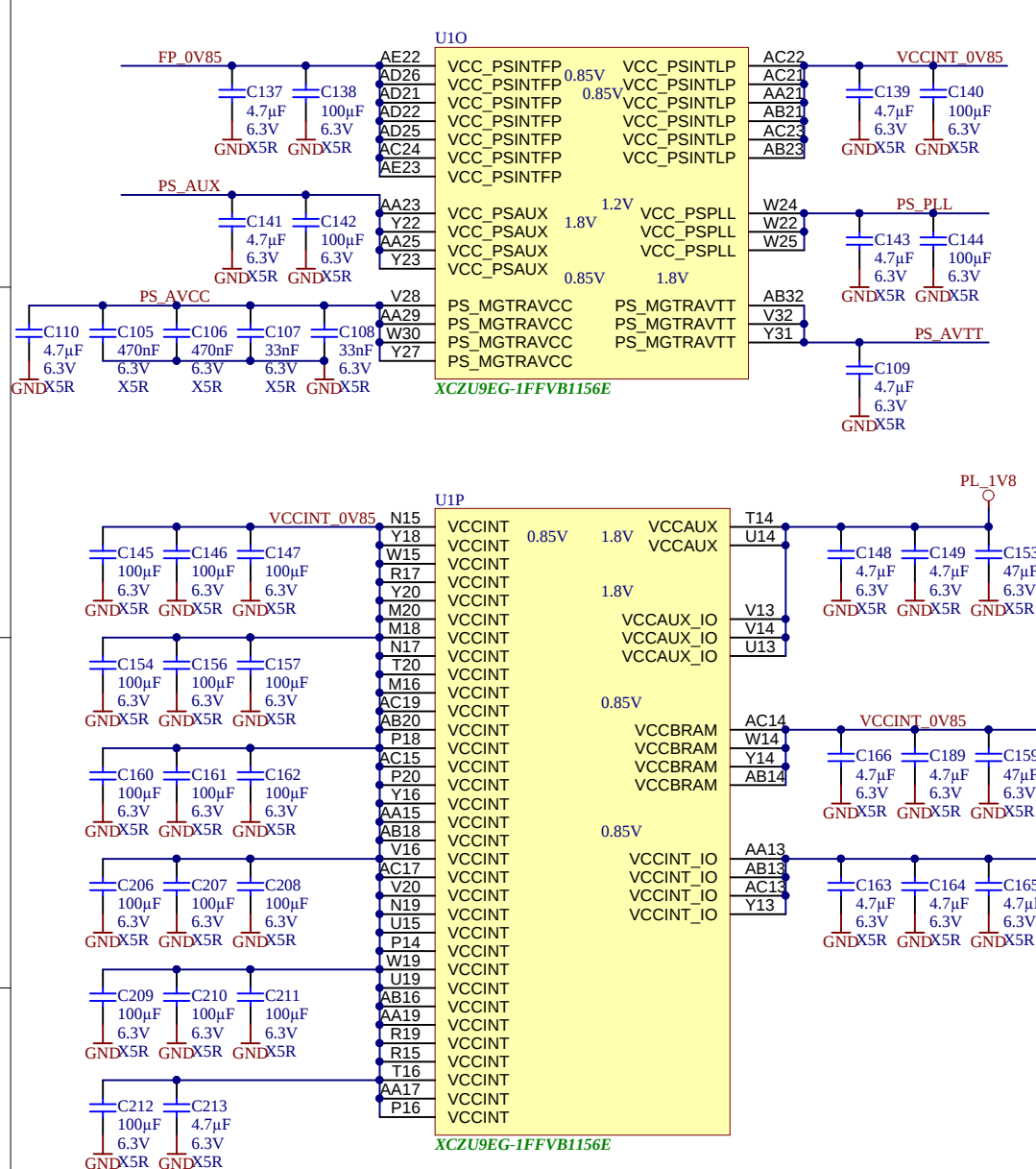
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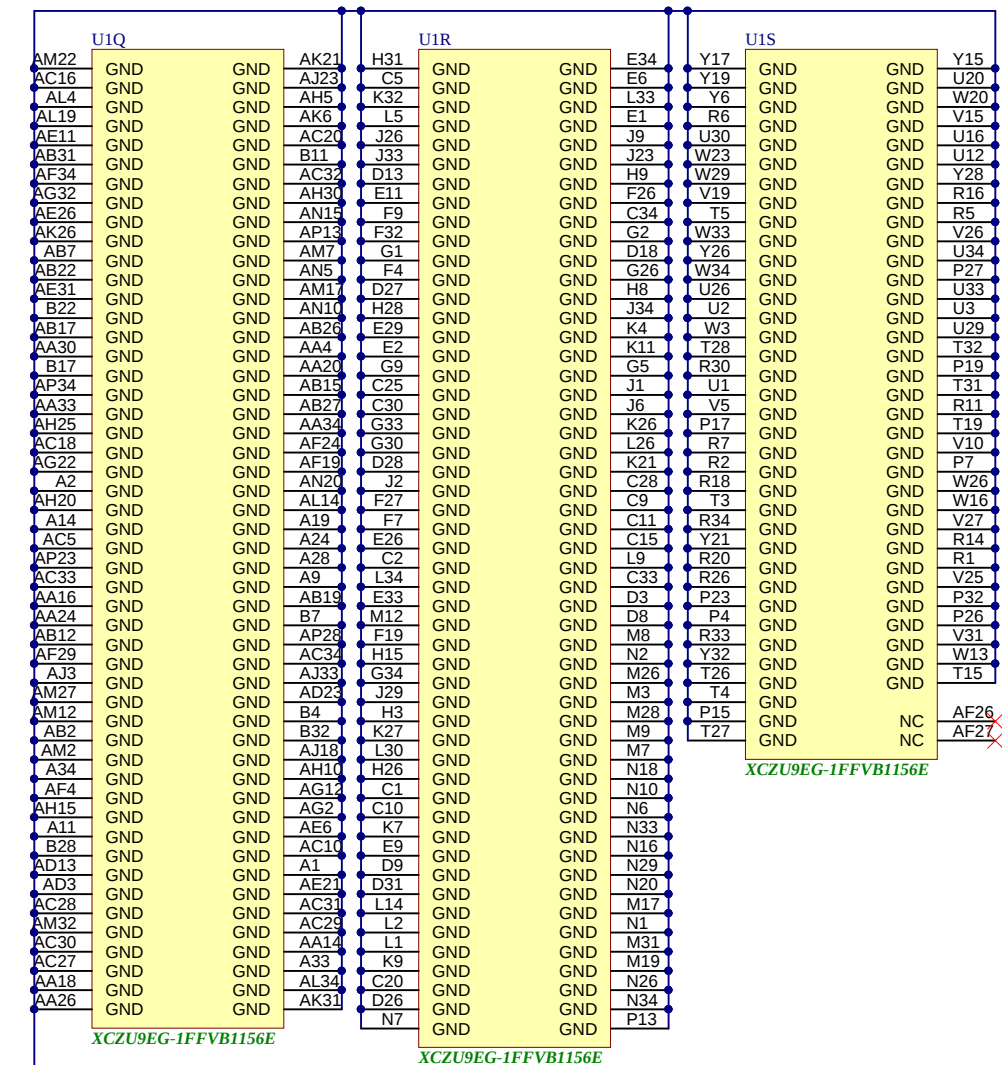
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Capacitors suitable for ZU9EG, ZU15EG



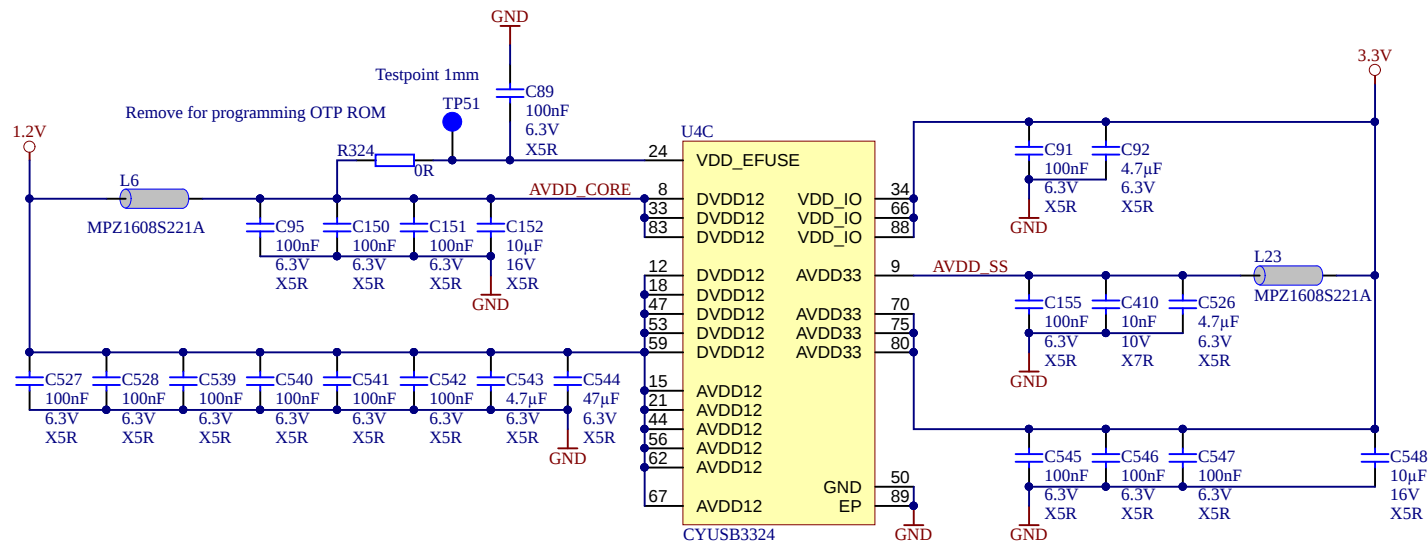
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Date: 2017-10-16	Copyright: Trenz Electronic GmbH	
Filename: ZU_PWR.SchDoc		Page10 of 58

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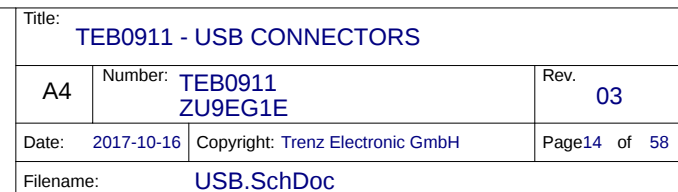
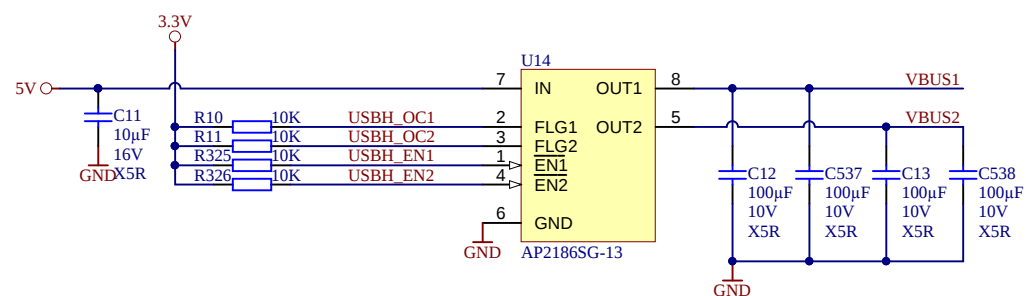
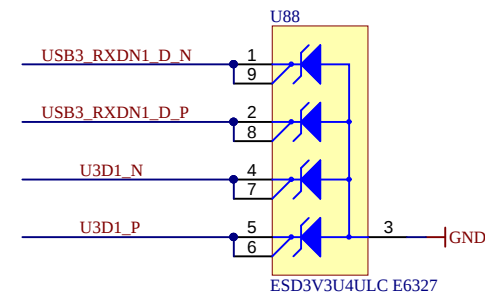
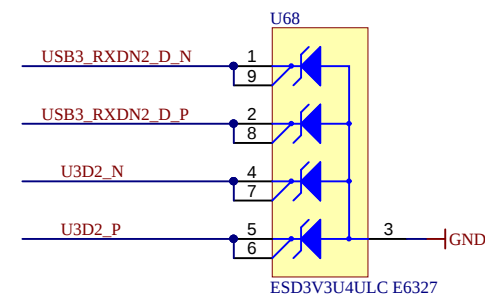
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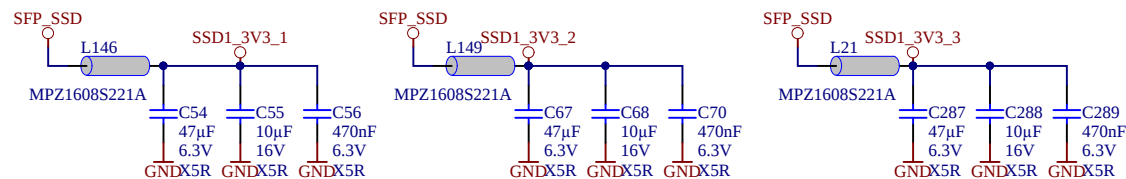
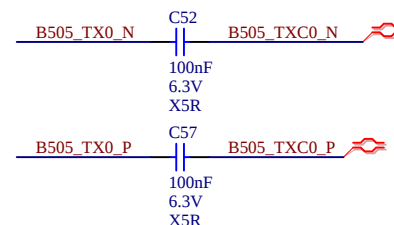
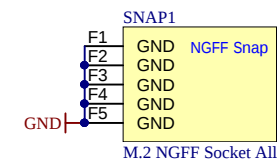
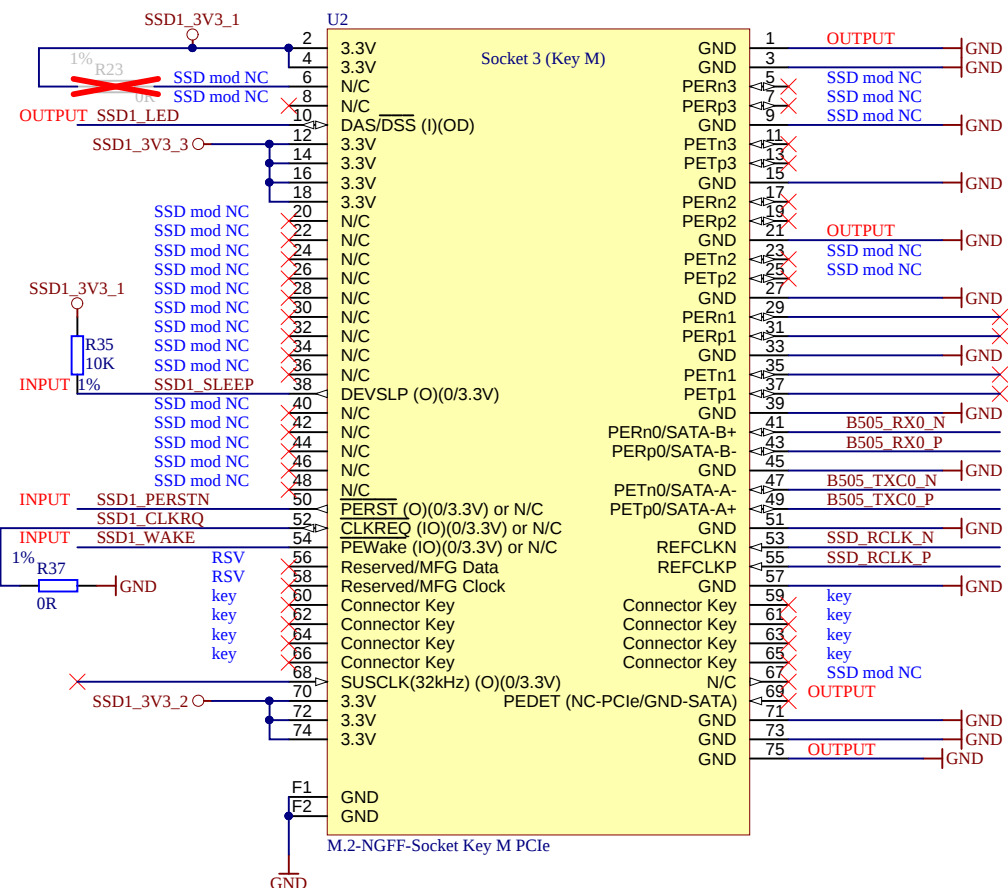
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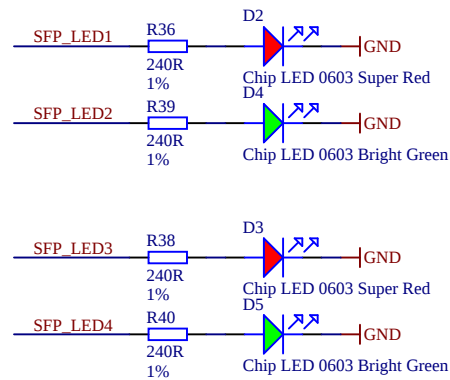
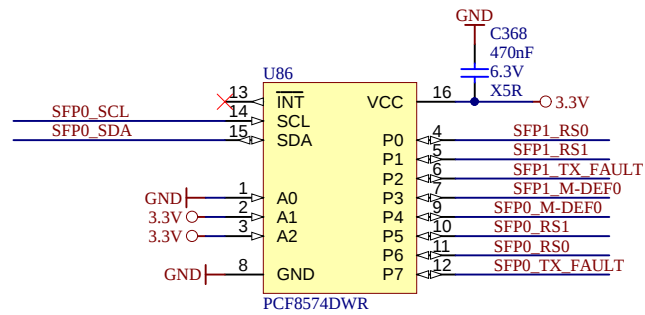


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		A4	Rev. 03
Date: 2017-10-16	Copyright: Trenz Electronic GmbH		Page13 of 58
Filename: USB3-HUB_PWR.SchDoc			

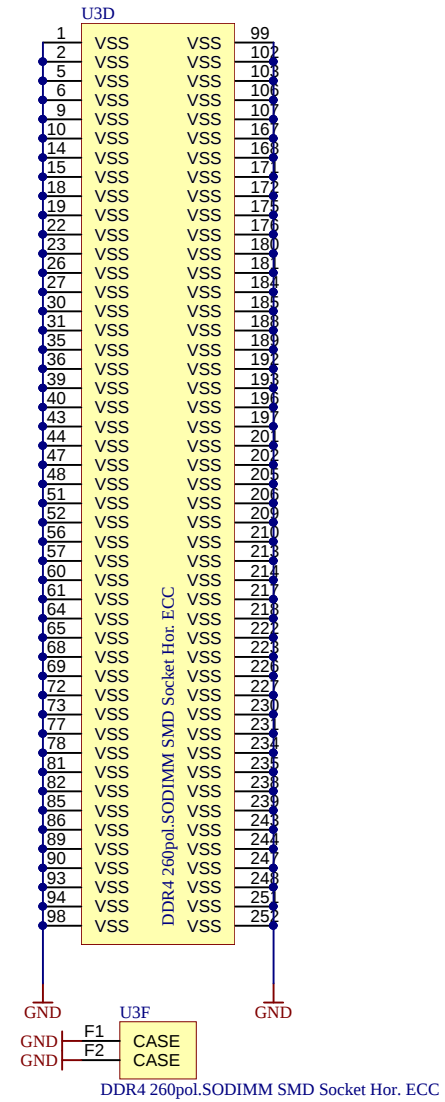
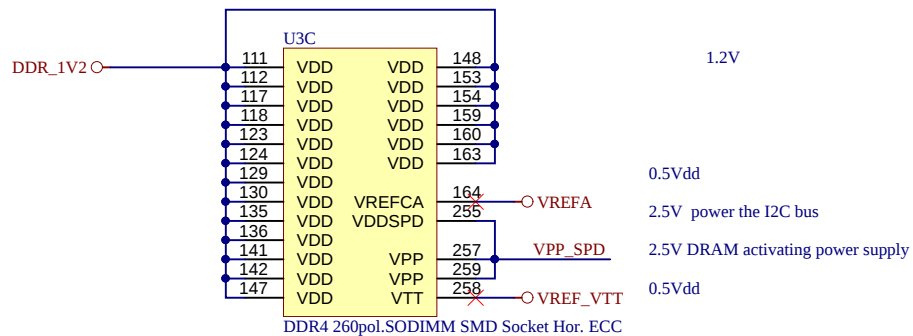
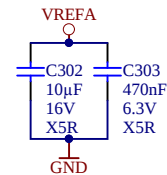
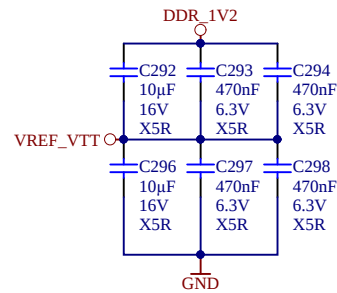
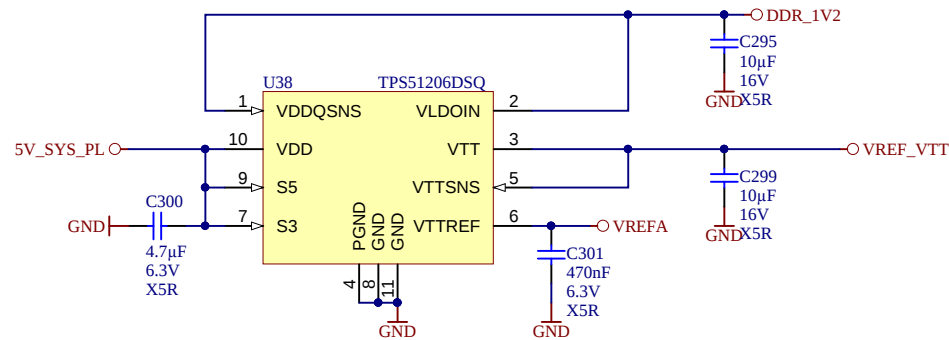
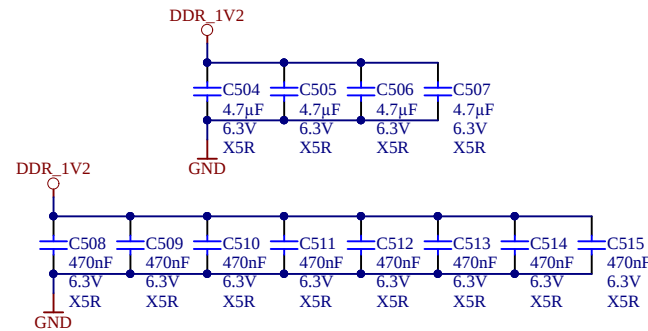
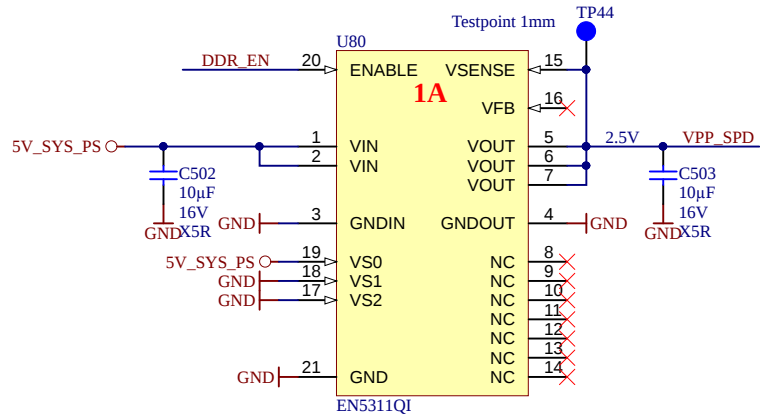




Title: TEB0911 - SSD		
A4	Number: TEB0911 ZU9EG1E	Rev. 03
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Filename: SSD.SchDoc		



	Title: TEB0911 - SFP		
	A4	Number: TEB0911 ZU9EG1E	Rev. 03
	Date: 2017-10-16	Copyright: Trenz Electronic GmbH	Page 18 of 58
	Filename: SFP_CTRL.SchDoc		



		Title: TEB0911 - DDR POWER	
		A4	Number: TEB0911 ZU9EG1E
Date: 2017-10-16	Copyright: Trenz Electronic GmbH		Rev. 03
Filename: DDR_PWR.SchDoc		Page 19 of 58	

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U3A

DDR4-A0	144	A0	DQS0_T	13	DDR4-DQS7_P
DDR4-A1	133	A1	DQS0_C	11	DDR4-DQS7_N
DDR4-A2	132	A2			
DDR4-A3	131	A3	DQS1_T	34	DDR4-DQS6_P
DDR4-A4	128	A4	DQS1_C	32	DDR4-DQS6_N
DDR4-A5	126	A5			
DDR4-A6	127	A6	DQS2_T	55	DDR4-DQS5_P
DDR4-A7	122	A7	DQS2_C	53	DDR4-DQS5_N
DDR4-A8	125	A8			
DDR4-A9	121	A9	DQS3_T	76	DDR4-DQS4_P
DDR4-A10	146	A10/AP	DQS3_C	74	DDR4-DQS4_N
DDR4-A11	120	A11			
DDR4-A12	119	A12	DQS4_T	179	DDR4-DQS3_P
DDR4-A13	158	A13	DQS4_C	177	DDR4-DQS3_N
DDR4-A14	151	WE/A14			
DDR4-A15	156	CAS/A15	DQS5_T	200	DDR4-DQS2_P
DDR4-A16	152	RAS/A16	DQS5_C	198	DDR4-DQS2_N

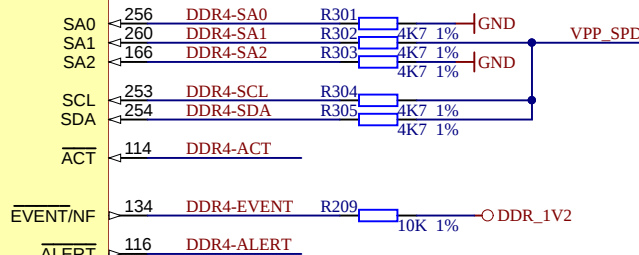
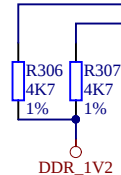
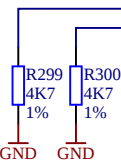
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DDR4-BA1	145	BA1	DQS6_C	219	DDR4-DQS1_N
DDR4-BG0	115	BG0	DQS7_T	242	DDR4-DQS0_P
DDR4-BG1	113	BG1	DQS7_C	240	DDR4-DQS0_N

DDR4-CKE0	109	CKE0	DQS8_T	97	DDR4-DQS8_P
DDR4-CKE1	110	CKE1/NC	DQS8_C	95	DDR4-DQS8_N

DDR4-CLK0_P	137	CK0_T	DM0/DBI0	12	DDR4-DM7
DDR4-CLK0_N	139	CK0_C	DM1/DBI1	33	DDR4-DM6
DDR4-CLK1_P	138	CK1_T/NF	DM2/DBI2	54	DDR4-DM5
DDR4-CLK1_N	140	CK1_C/NF	DM3/DBI3	75	DDR4-DM4
			DM4/DBI4	178	DDR4-DM3
			DM5/DBI5	199	DDR4-DM2
			DM6/DBI6	220	DDR4-DM1
			DM7/DBI7	241	DDR4-DM0
			DM8/DBI/NC	96	DDR4-DM8

DDR4-CS_N0	149	CS0			
DDR4-CS_N1	157	CS1/NC			
	162	C0/CS2/NC			
	165	C1/CS3/ NC			

DDR4-ODT0	155	ODT0	SA0	256	DDR4-SA0
DDR4-ODT1	161	ODT1/NC	SA1	260	DDR4-SA1
			SA2	166	DDR4-SA2
DDR4-RESET	108	RESET	SCL	253	DDR4-SCL
DDR4-PAR	143	PARITY	SDA	254	DDR4-SDA



DDR4 260pol.SODIMM SMD Socket Hor. ECC

U3B

DQ62	8	DQ0	DQ32	174	DQ31
DQ60	7	DQ1	DQ33	173	DQ28
DQ59	20	DQ2	DQ34	187	DQ27
DQ56	21	DQ3	DQ35	186	DQ25
DQ63	4	DQ4	DQ36	170	DQ26
DQ61	3	DQ5	DQ37	169	DQ30
DQ58	16	DQ6	DQ38	183	DQ29
DQ57	17	DQ7	DQ39	182	DQ24
		DQ8	DQ40	195	DQ20
DQ50	28	DQ9	DQ41	194	DQ16
DQ51	29	DQ10	DQ42	207	DQ23
DQ54	41	DQ11	DQ43	208	DQ22
DQ55	42	DQ12	DQ44	191	DQ18
DQ48	24	DQ13	DQ45	190	DQ17
DQ49	25	DQ14	DQ46	203	DQ21
DQ52	38	DQ15	DQ47	204	DQ19
DQ53	37	DQ16	DQ48	216	DQ11
DQ47	50	DQ17	DQ49	215	DQ15
DQ43	49	DQ18	DQ50	228	DQ8
DQ45	62	DQ19	DQ51	229	DQ13
DQ41	63	DQ20	DQ52	211	DQ12
DQ44	46	DQ21	DQ53	212	DQ14
DQ46	45	DQ22	DQ54	224	DQ10
DQ42	58	DQ23	DQ55	225	DQ9
DQ40	59	DQ24	DQ56	237	DQ1
		DQ25	DQ57	236	DQ2
DQ38	70	DQ26	DQ58	249	DQ7
DQ36	71	DQ27	DQ59	250	DQ5
DQ34	83	DQ28	DQ60	232	DQ0
DQ35	84	DQ29	DQ61	233	DQ4
DQ33	66	DQ30	DQ62	245	DQ6
DQ32	67	DQ31	DQ63	246	DQ3
DQ39	79				
DQ37	80				

DDR4 260pol.SODIMM SMD Socket Hor. ECC

CB6	92	CB0/NC
CB3	91	CB1/NC
CB4	101	CB2/NC
CB0	105	CB3/NC
CB5	88	CB4/NC
CB7	87	CB5/NC
CB1	100	CB6/NC
CB2	104	CB7/NC

DDR4 260pol.SODIMM SMD Socket Hor. ECC

DDR4-DQS7_P
DDR4-DQS7_N
DDR4-DQS6_P
DDR4-DQS6_N
DDR4-DQS5_P
DDR4-DQS5_N
DDR4-DQS4_P
DDR4-DQS4_N
DDR4-DQS3_P
DDR4-DQS3_N
DDR4-DQS2_P
DDR4-DQS2_N
DDR4-DQS1_P
DDR4-DQS1_N
DDR4-DQS0_P
DDR4-DQS0_N
DDR4-DQS8_P
DDR4-DQS8_N
DDR4-CLK0_P
DDR4-CLK0_N
DDR4-CLK1_P
DDR4-CLK1_N

DO[0..63]
CB[0..7]
DDR4-DM[0..8]
DDR4-ACT
DDR4-ALERT
DDR4-AI[0..16]
DDR4-BA0
DDR4-BA1
DDR4-BG0
DDR4-BG1
DDR4-CKE0
DDR4-CKE1
DDR4-CS_N0
DDR4-CS_N1
DDR4-ODT0
DDR4-ODT1
DDR4-PAR



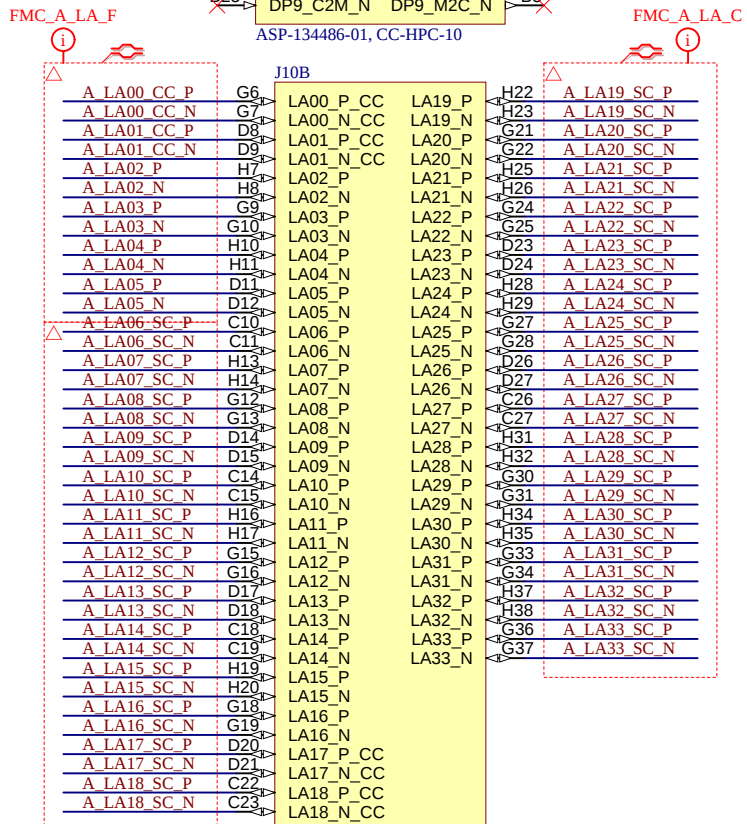
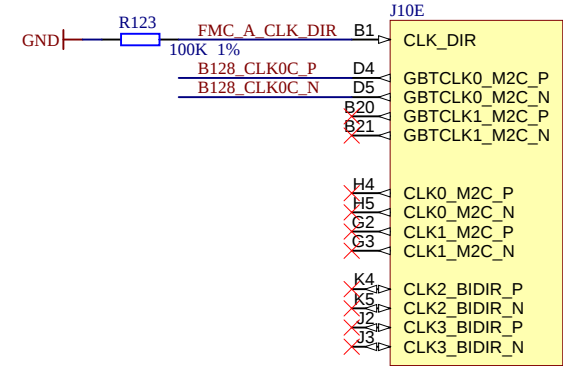
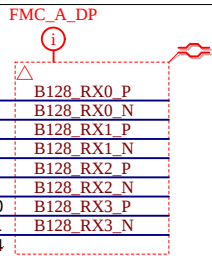
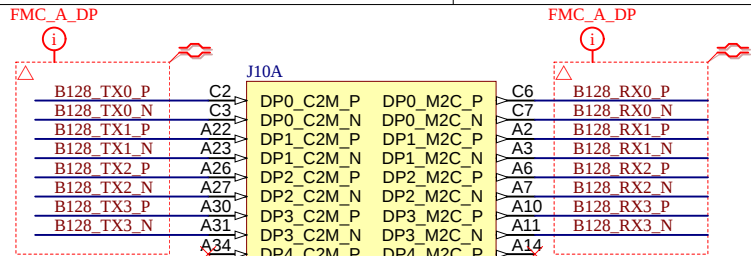
Title: TEB0911 - DDR4 SODIMM			
A4	Number: TEB0911 ZU9EG1E	Rev. 03	
Date: 2017-10-16	Copyright: Trenz Electronic GmbH		Page20 of 58
Filename: DDR_SODIMM.SchDoc			

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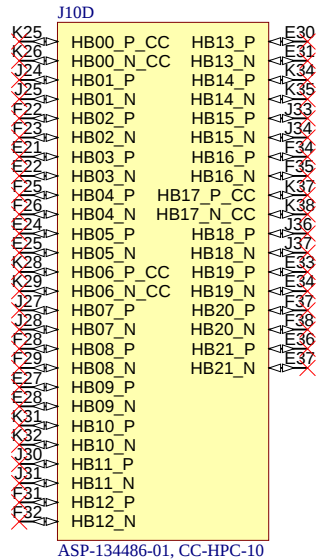
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ASP-134486-01, CC-HPC-10

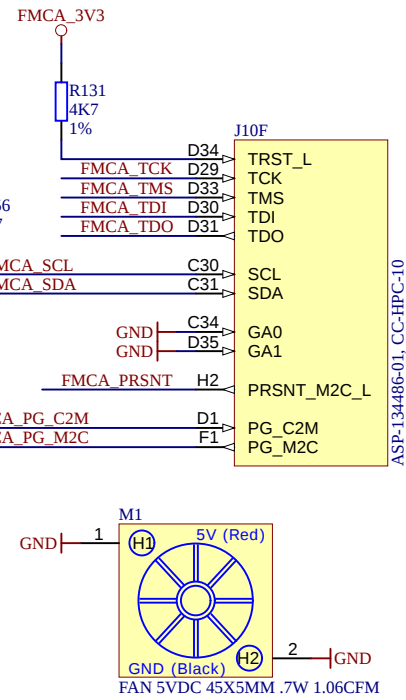


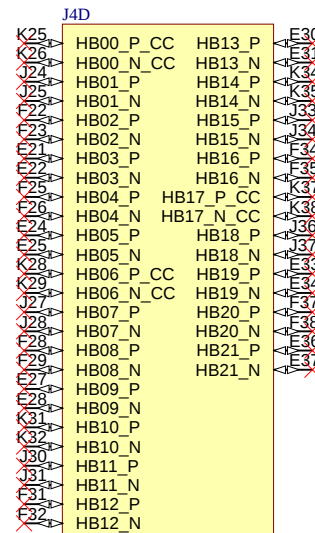
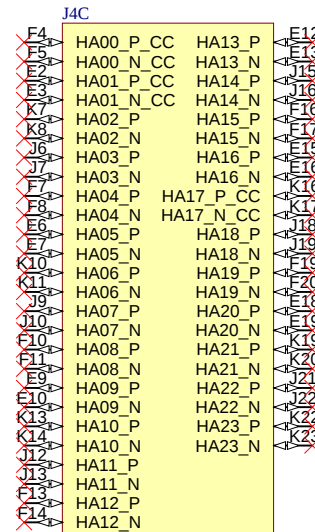
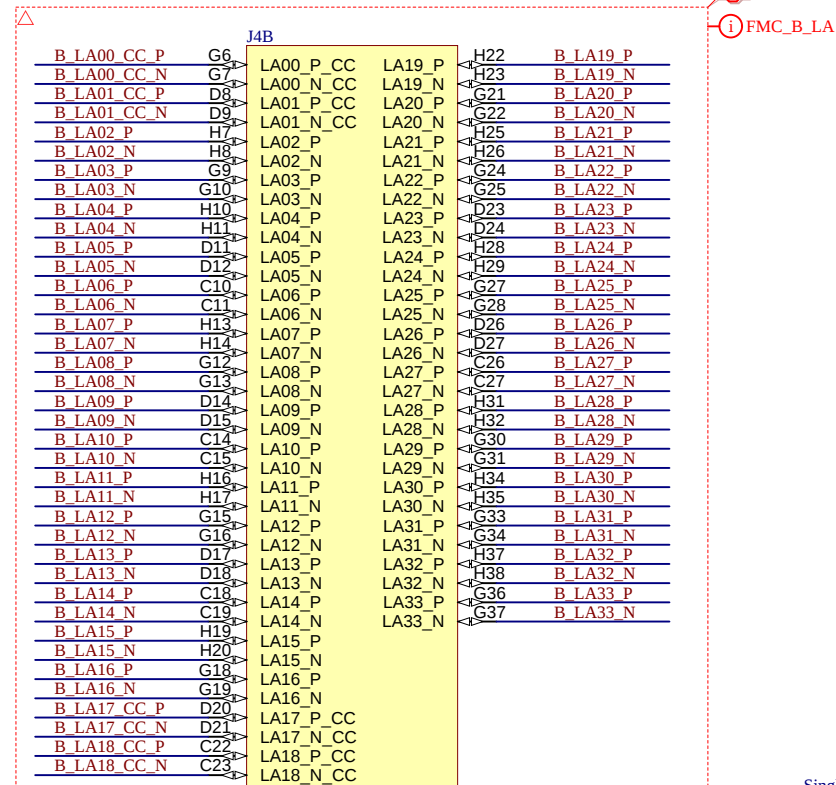
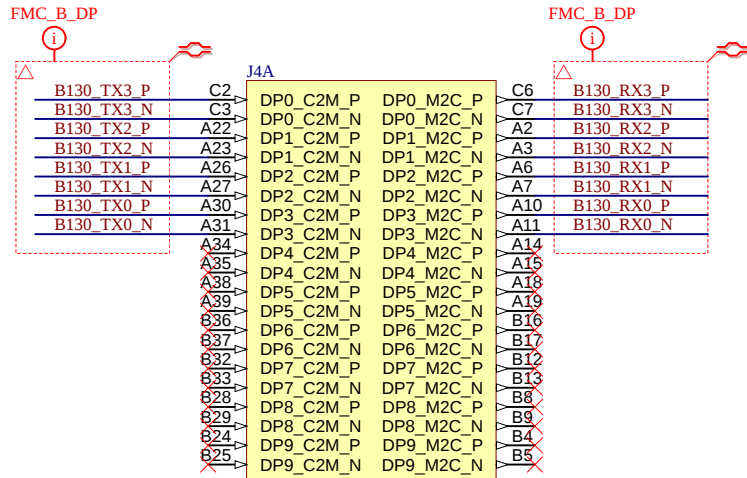
U10
SNAP

Single FMC Commercial Baseboard Snap



Title: TEB0911 - FMC A		
A4	Number: TEB0911 ZU9EG1E	Rev. 03
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Filename: FMC_A.SchDoc	Page21 of 58	





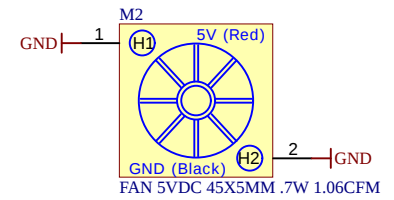
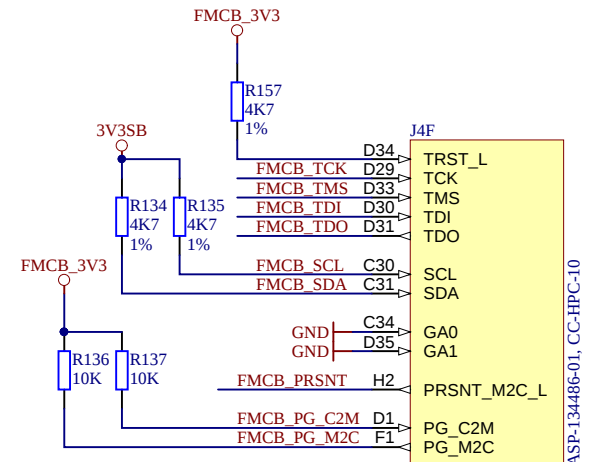
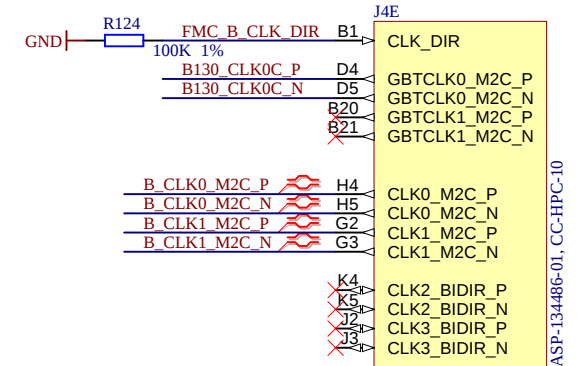
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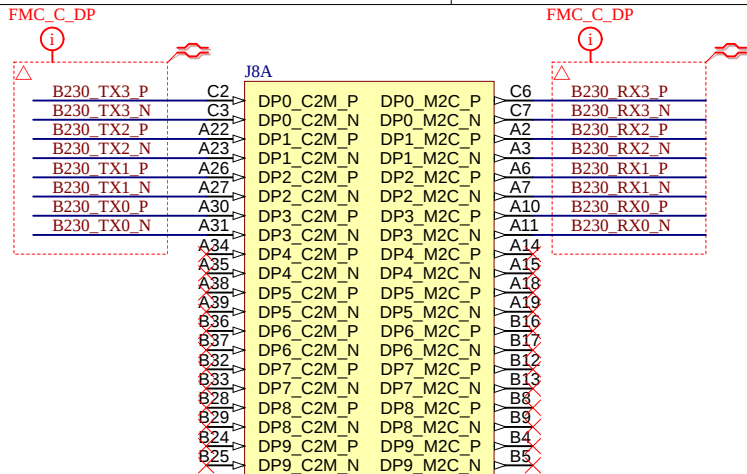
SNAP

Single FMC Commercial Baseboard Snap

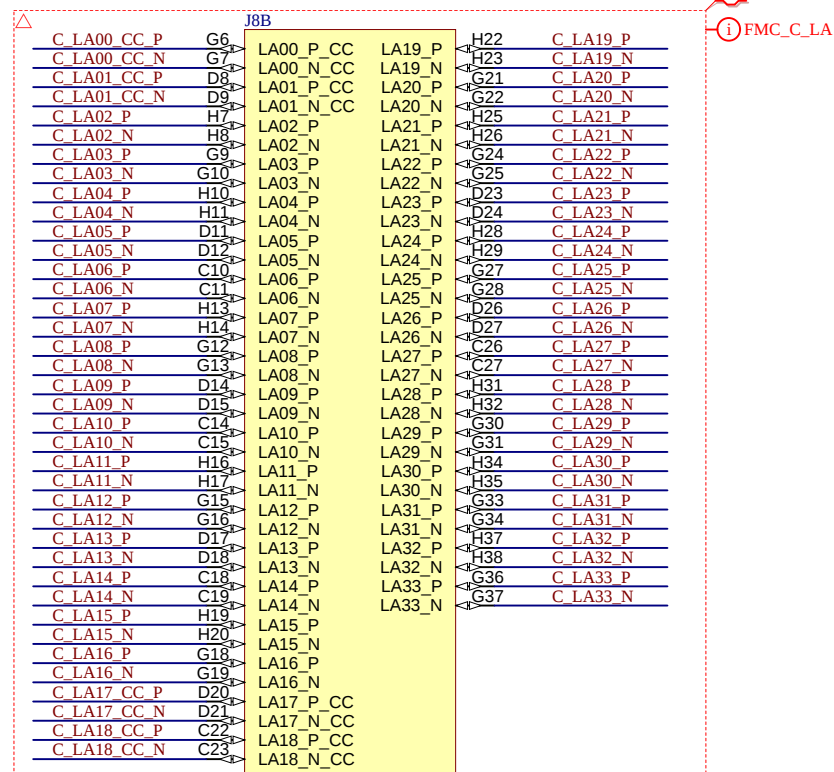


Title: TEB0911 - FMC B		
A4	Number: TEB0911 ZU9EG1E	Rev. 03
Date: 2017-10-16	Copyright: Trenz Electronic GmbH	
Filename: FMC_B.SchDoc	Page22 of 58	

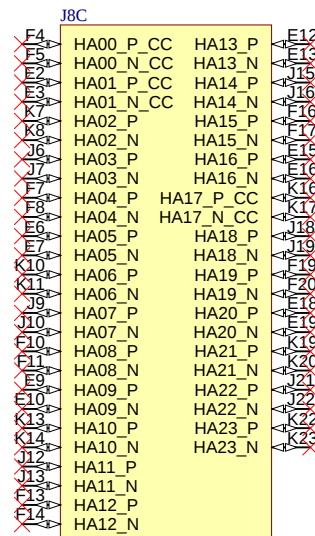




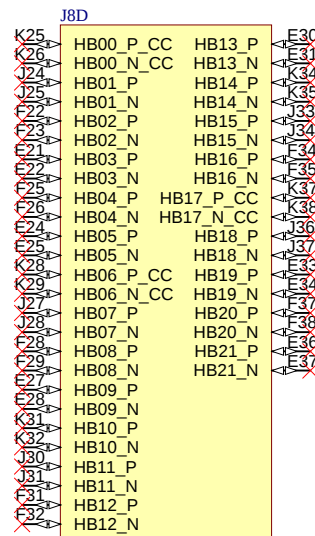
ASP-134486-01, CC-HPC-10



ASP-134486-01, CC-HPC-10



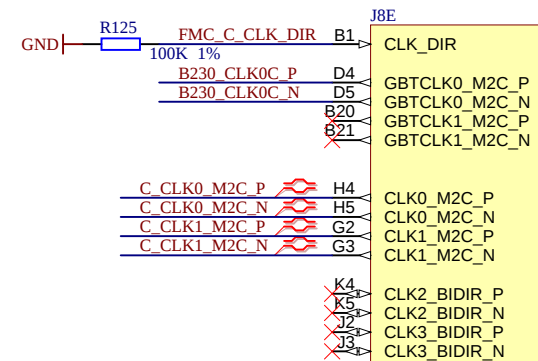
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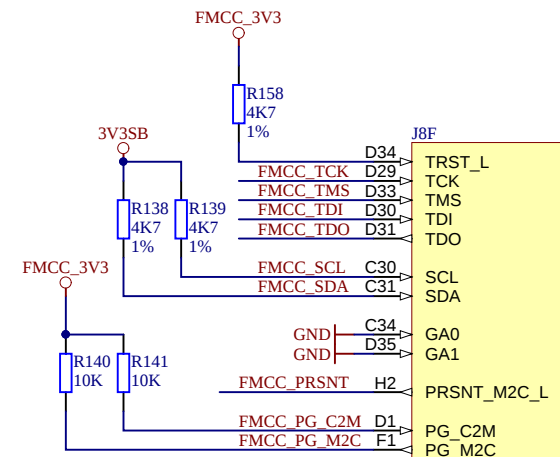
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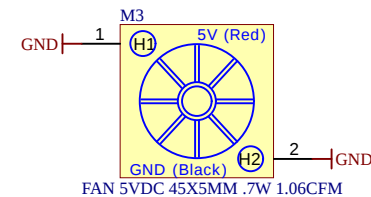
Single FMC Commercial Baseboard Snap



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ASP-134486-01, CC-HPC-10



Title: TEB0911 - FMC C		
A4	Number: TEB0911 ZU9EG1E	Rev. 03
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FMC_D_DP

FMC_D_DP

J7A

B229_TX3_P	C2	DP0_C2M_P	DP0_M2C_P	C6	B229_RX3_P
B229_TX3_N	C3	DP0_C2M_N	DP0_M2C_N	C7	B229_RX3_N
B229_TX2_P	A22	DP1_C2M_P	DP1_M2C_P	A2	B229_RX2_P
B229_TX2_N	A23	DP1_C2M_N	DP1_M2C_N	A3	B229_RX2_N
B229_TX1_P	A26	DP2_C2M_P	DP2_M2C_P	A6	B229_RX1_P
B229_TX1_N	A27	DP2_C2M_N	DP2_M2C_N	A7	B229_RX1_N
B229_TX0_P	A30	DP3_C2M_P	DP3_M2C_P	A10	B229_RX0_P
B229_TX0_N	A31	DP3_C2M_N	DP3_M2C_N	A11	B229_RX0_N
	A34	DP4_C2M_P	DP4_M2C_P	A14	
	A35	DP4_C2M_N	DP4_M2C_N	A15	
	A38	DP5_C2M_P	DP5_M2C_P	A18	
	A39	DP5_C2M_N	DP5_M2C_N	A19	
	B36	DP6_C2M_P	DP6_M2C_P	B16	
	B37	DP6_C2M_N	DP6_M2C_N	B17	
	B32	DP7_C2M_P	DP7_M2C_P	B12	
	B33	DP7_C2M_N	DP7_M2C_N	B13	
	B28	DP8_C2M_P	DP8_M2C_P	B8	
	B29	DP8_C2M_N	DP8_M2C_N	B9	
	B24	DP9_C2M_P	DP9_M2C_P	B4	
	B25	DP9_C2M_N	DP9_M2C_N	B5	

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J7C

HA00_P_CC	HA13_P	E12
HA00_N_CC	HA13_N	E13
HA01_P_CC	HA14_P	E15
HA01_N_CC	HA14_N	E16
HA02_P	HA15_P	E17
HA02_N	HA15_N	E18
HA03_P	HA16_P	E19
HA03_N	HA16_N	E20
HA04_P	HA17_P_CC	E21
HA04_N	HA17_N_CC	E22
HA05_P	HA18_P	E23
HA05_N	HA18_N	E24
HA06_P	HA19_P	E25
HA06_N	HA19_N	E26
HA07_P	HA20_P	E27
HA07_N	HA20_N	E28
HA08_P	HA21_P	E29
HA08_N	HA21_N	E30
HA09_P	HA22_P	E31
HA09_N	HA22_N	E32
HA10_P	HA23_P	E33
HA10_N	HA23_N	E34
HA11_P		E35
HA11_N		E36
HA12_P		E37
HA12_N		E38

ASP-134486-01, CC-HPC-10

J7D

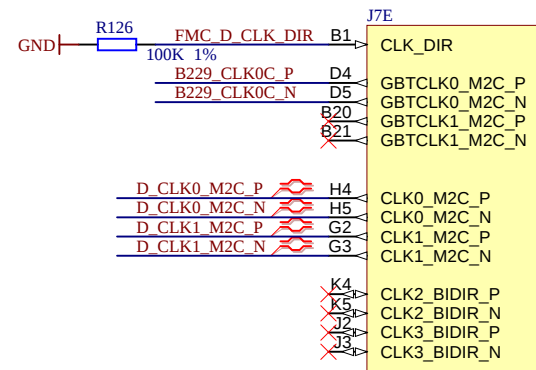
HB00_P_CC	HB13_P	E30
HB00_N_CC	HB13_N	E31
HB01_P	HB14_P	E32
HB01_N	HB14_N	E33
HB02_P	HB15_P	E34
HB02_N	HB15_N	E35
HB03_P	HB16_P	E36
HB03_N	HB16_N	E37
HB04_P	HB17_P_CC	E38
HB04_N	HB17_N_CC	E39
HB05_P	HB18_P	E40
HB05_N	HB18_N	E41
HB06_P_CC	HB19_P	E42
HB06_N_CC	HB19_N	E43
HB07_P	HB20_P	E44
HB07_N	HB20_N	E45
HB08_P	HB21_P	E46
HB08_N	HB21_N	E47
HB09_P		E48
HB09_N		E49
HB10_P		E50
HB10_N		E51
HB11_P		E52
HB11_N		E53
HB12_P		E54
HB12_N		E55

ASP-134486-01, CC-HPC-10

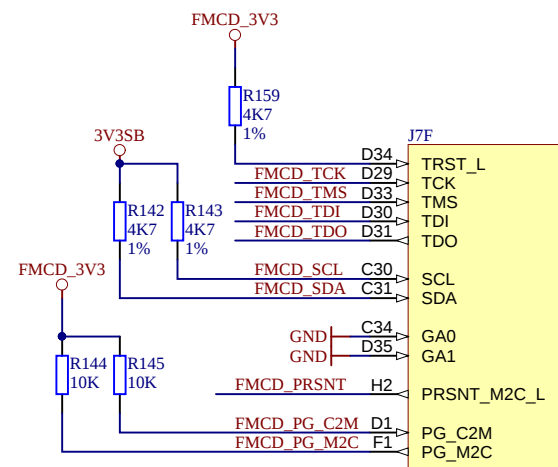
U6

SNAP

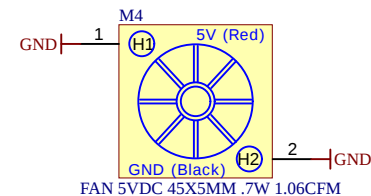
Single FMC Commercial Baseboard Snap



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Title: TEB0911 - FMC D

A4 Number: TEB0911 ZU9EG1E

Rev. 03

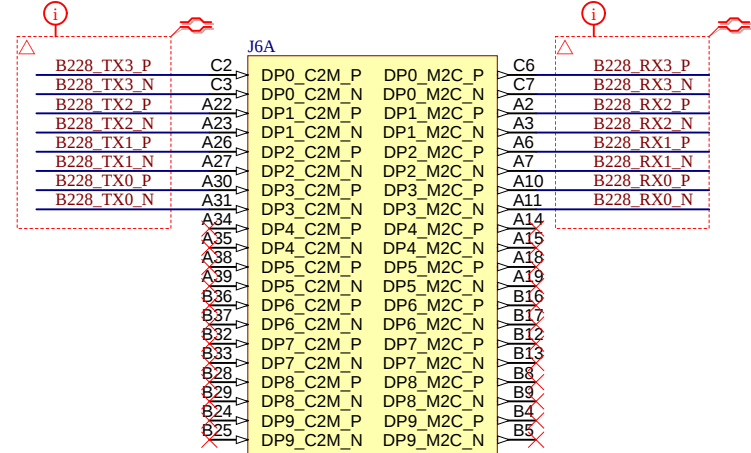
Date: 2017-10-16 Copyright: Trenz Electronic GmbH

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Filename: FMC_D.SchDoc

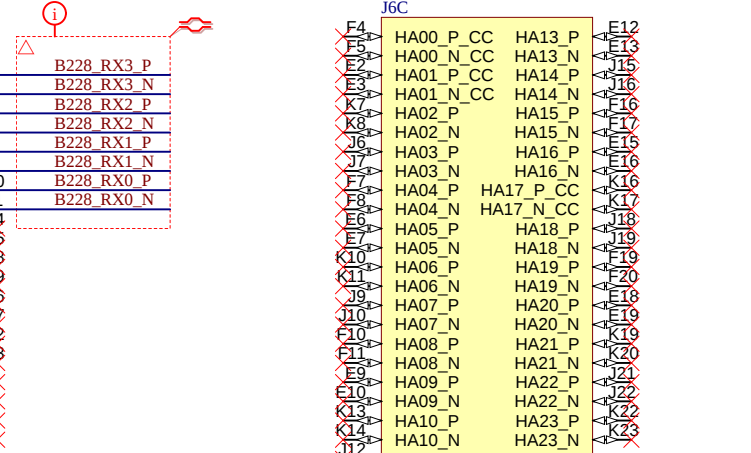


FMC_E_DP

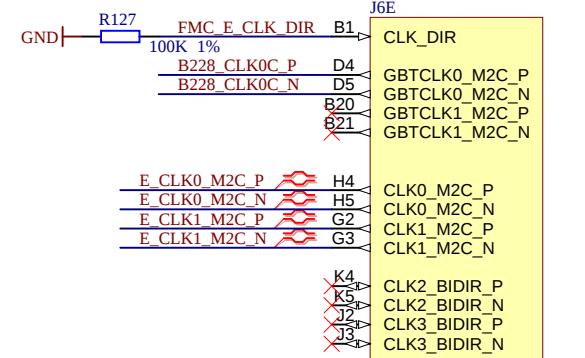


ASP-134486-01, CC-HPC-10

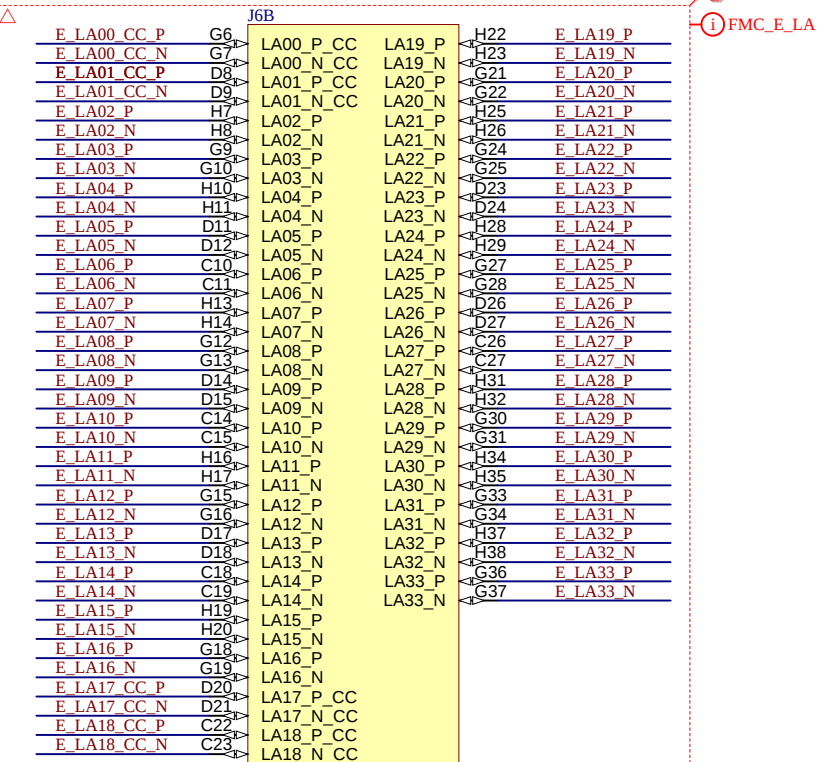
FMC_E_DP



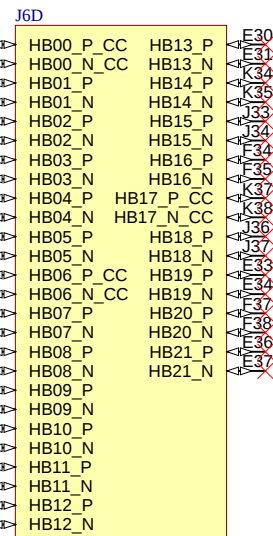
ASP-134486-01, CC-HPC-10



ASP-134486-01, CC-HPC-10



ASP-134486-01, CC-HPC-10

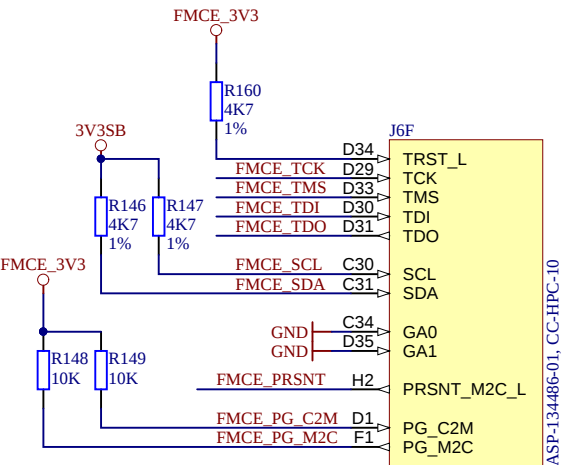


ASP-134486-01, CC-HPC-10

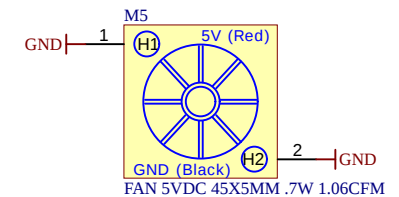
U7

SNAP

Single FMC Commercial Baseboard Snap



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Title: TEB0911 - FMC E		
A4	Number: TEB0911 ZU9EG1E	Rev. 03
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FMC_F_DP

FMC_F_DP

J21A

B129_TX0_P	C2	DP0_C2M_P	DP0_M2C_P	C6	B129_RX0_P
B129_TX0_N	C3	DP0_C2M_N	DP0_M2C_N	C7	B129_RX0_N
B129_TX1_P	A22	DP1_C2M_P	DP1_M2C_P	A2	B129_RX1_P
B129_TX1_N	A23	DP1_C2M_N	DP1_M2C_N	A3	B129_RX1_N
	A26	DP2_C2M_P	DP2_M2C_P	A6	
	A27	DP2_C2M_N	DP2_M2C_N	A7	
	A30	DP3_C2M_P	DP3_M2C_P	A10	
	A31	DP3_C2M_N	DP3_M2C_N	A11	
	A34	DP4_C2M_P	DP4_M2C_P	A14	
	A35	DP4_C2M_N	DP4_M2C_N	A15	
	A38	DP5_C2M_P	DP5_M2C_P	A18	
	A39	DP5_C2M_N	DP5_M2C_N	A19	
	B36	DP6_C2M_P	DP6_M2C_P	B16	
	B37	DP6_C2M_N	DP6_M2C_N	B17	
	B32	DP7_C2M_P	DP7_M2C_P	B12	
	B33	DP7_C2M_N	DP7_M2C_N	B13	
	B28	DP8_C2M_P	DP8_M2C_P	B8	
	B29	DP8_C2M_N	DP8_M2C_N	B9	
	B24	DP9_C2M_P	DP9_M2C_P	B4	
	B25	DP9_C2M_N	DP9_M2C_N	B5	

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FMC_F_LA_F

FMC_F_LA_C

J21B

F LA00_CC_P	G6	LA00_P_CC	LA19_P	H22	F LA19_SC_P
F LA00_CC_N	G7	LA00_N_CC	LA19_N	H23	F LA19_SC_N
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F LA01_CC_N	D9	LA01_N_CC	LA20_N	G22	F LA20_SC_N
F LA02_P	H7	LA02_P	LA21_P	H25	F LA21_SC_P
F LA02_N	H8	LA02_N	LA21_N	H26	F LA21_SC_N
F LA03_P	G9	LA03_P	LA22_P	G24	F LA22_SC_P
F LA03_N	G10	LA03_N	LA22_N	G25	F LA22_SC_N
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F LA05_P	D12	LA05_P	LA24_P	H28	F LA24_SC_P
F LA05_N	D12	LA05_N	LA24_N	H29	F LA24_SC_N
F LA06_SC_P	C10	LA06_P	LA25_P	G27	F LA25_SC_P
F LA06_SC_N	C11	LA06_N	LA25_N	G28	F LA25_SC_N
F LA07_SC_P	H13	LA07_P	LA26_P	D26	F LA26_SC_P
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F LA08_SC_P	G12	LA08_P	LA27_P	C26	F LA27_SC_P
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F LA17_SC_N	D21	LA17_N_CC			
F LA18_SC_P	C22	LA18_P_CC			
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J21C

HA00_P_CC	HA13_P	E12
HA00_N_CC	HA13_N	E13
HA01_P_CC	HA14_P	E15
HA01_N_CC	HA14_N	E16
HA02_P	HA15_P	E17
HA02_N	HA15_N	E18
HA03_P	HA16_P	E19
HA03_N	HA16_N	E20
HA04_P	HA17_P_CC	K16
HA04_N	HA17_N_CC	K17
HA05_P	HA18_P	J18
HA05_N	HA18_N	J19
HA06_P	HA19_P	F20
HA06_N	HA19_N	F18
HA07_P	HA20_P	E19
HA07_N	HA20_N	K19
HA08_P	HA21_P	J21
HA08_N	HA21_N	K20
HA09_P	HA22_P	J22
HA09_N	HA22_N	K22
HA10_P	HA23_P	K23
HA10_N	HA23_N	K23
HA11_P		
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HA12_P		
HA12_N		

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J21D

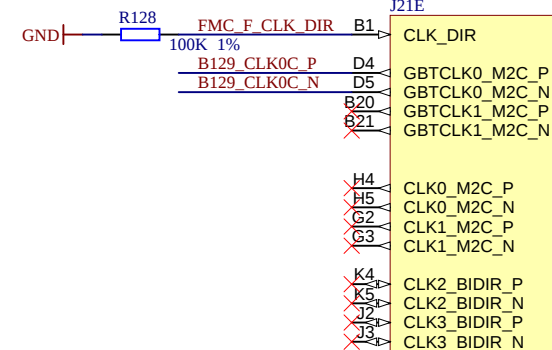
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HB02_N	HB15_N	F34
HB03_P	HB16_P	F35
HB03_N	HB16_N	K37
HB04_P	HB17_P_CC	K38
HB04_N	HB17_N_CC	K38
HB05_P	HB18_P	J36
HB05_N	HB18_N	J37
HB06_P_CC	HB19_P	E33
HB06_N_CC	HB19_N	E34
HB07_P	HB20_P	F37
HB07_N	HB20_N	F38
HB08_P	HB21_P	E36
HB08_N	HB21_N	E37
HB09_P		
HB09_N		
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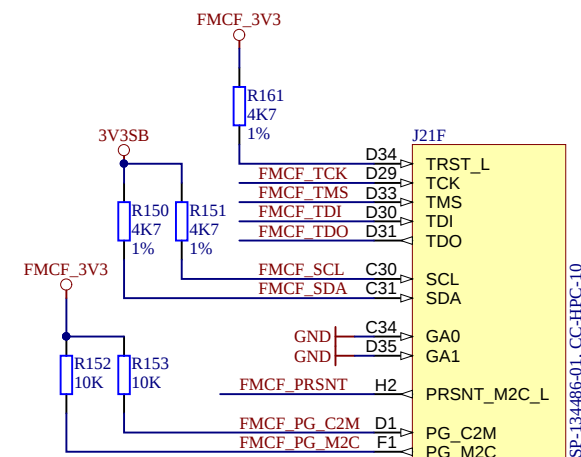
U19

SNAP

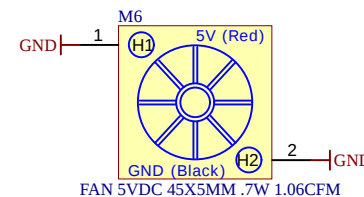
Single FMC Commercial Baseboard Snap



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FAN 5VDC 45X55MM .7W 1.06CFM



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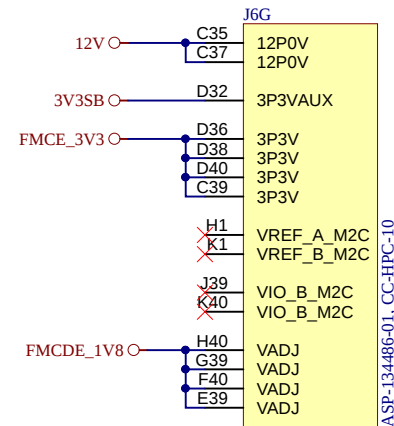
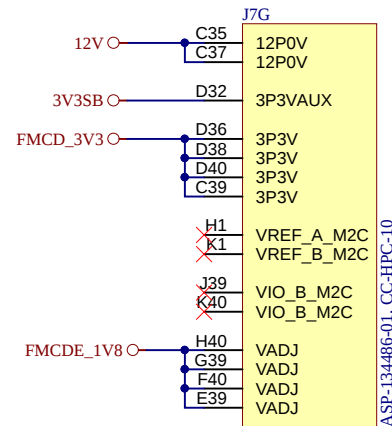
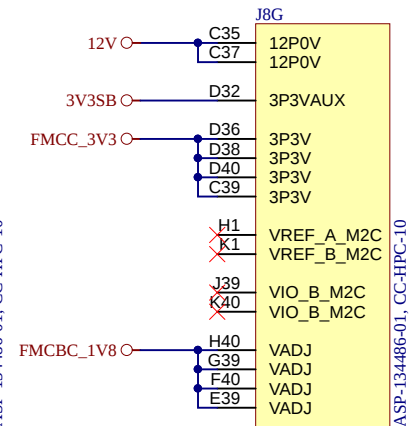
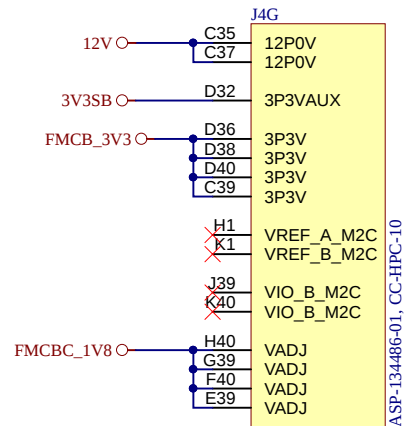
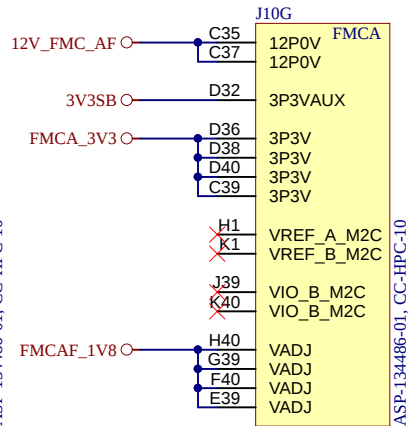
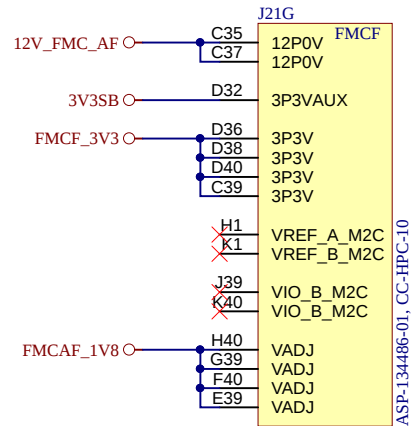
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FMC_A
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FMC_B
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FMC_C
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FMC_D
FMC_D.SchDoc
FMC_E
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FMC_F
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FMC_MECH.SchDoc



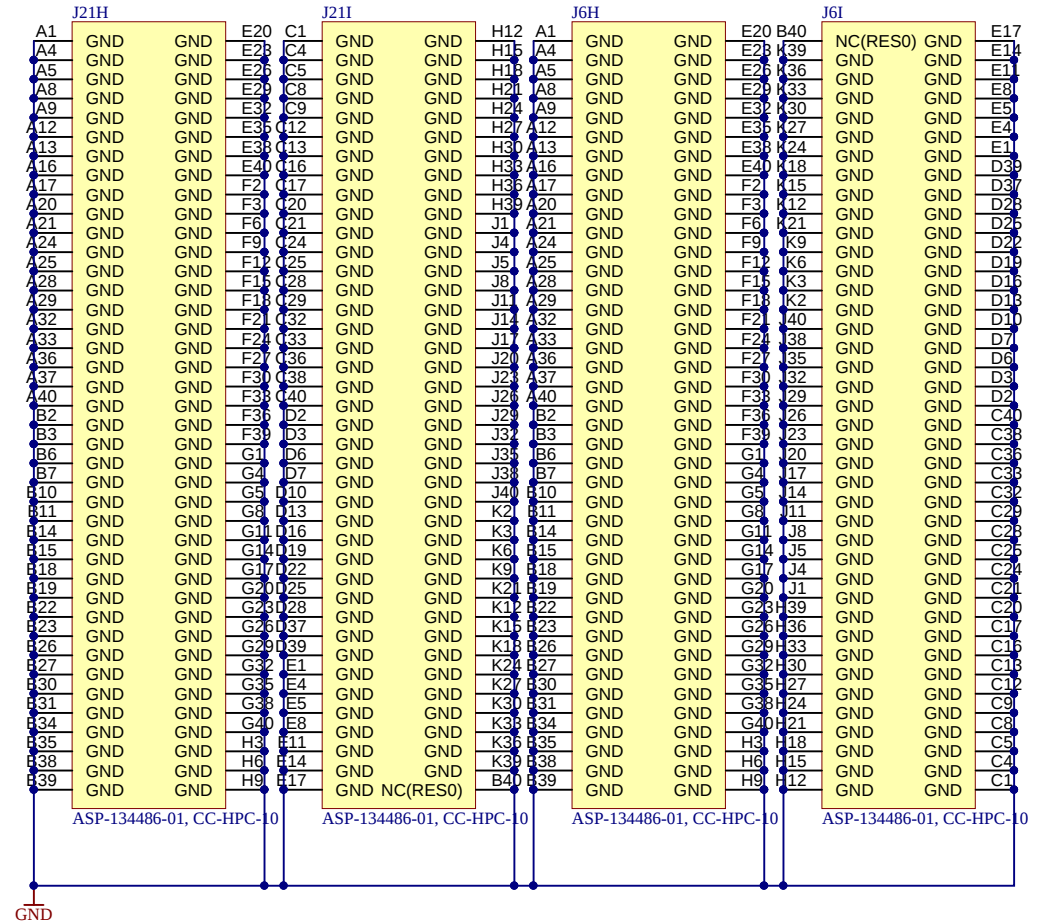
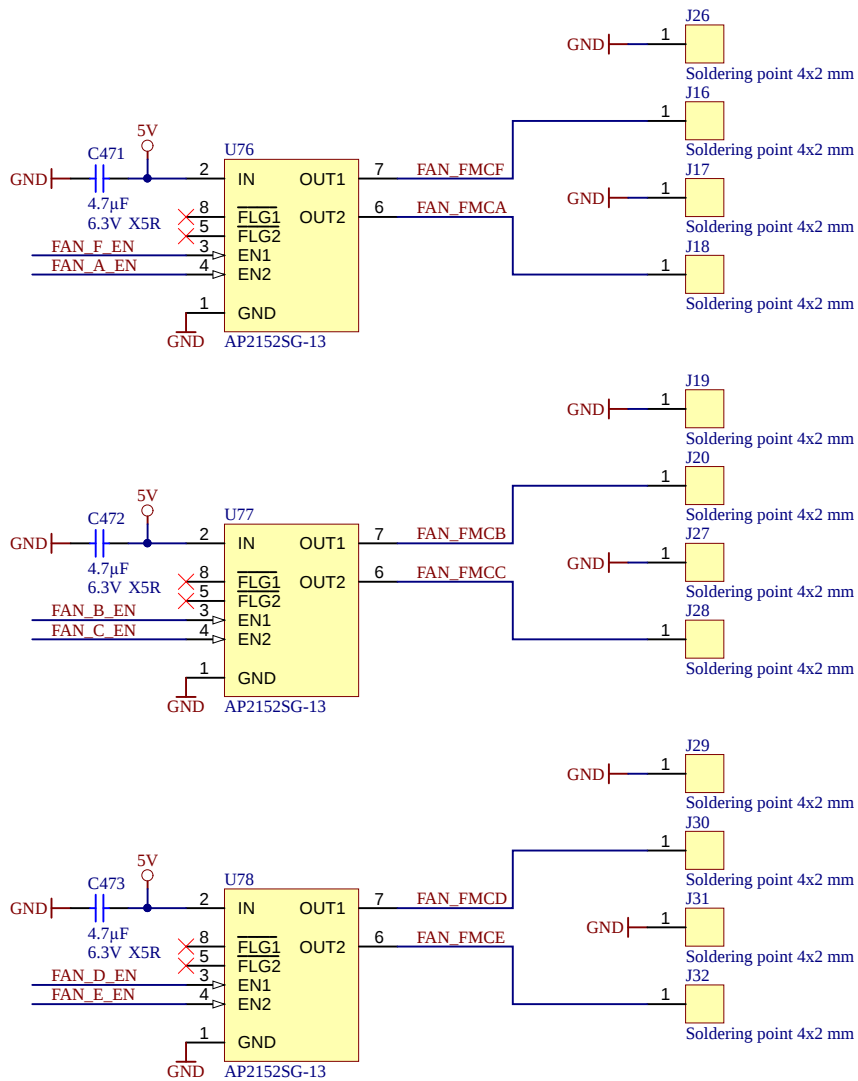
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A4	Number: TEB0911 ZU9EG1E	Rev. 03
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Filename: FMC PWR.SchDoc		

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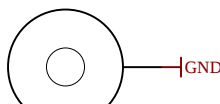
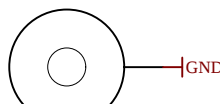
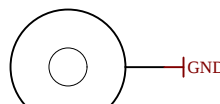
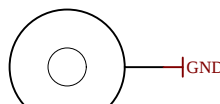
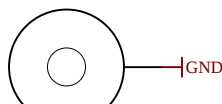
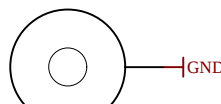
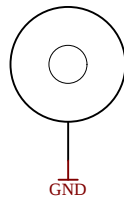
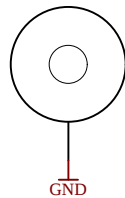
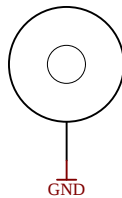
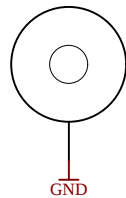
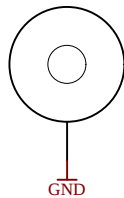
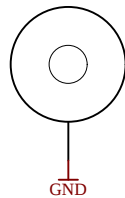
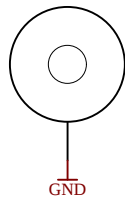
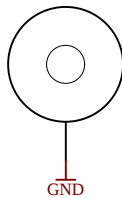
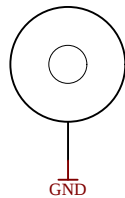
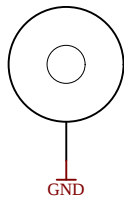
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Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm



Steel Spacer M2.5 2mm SMD

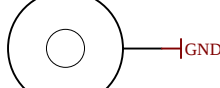
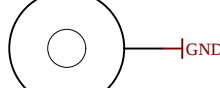
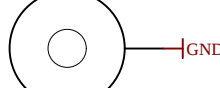
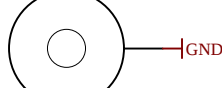
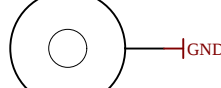
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Steel Spacer M2.5 2mm SMD

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Steel Spacer M2.5 2mm SMD

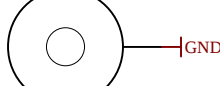
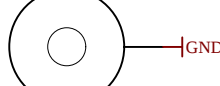
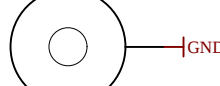
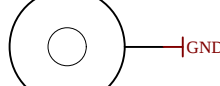
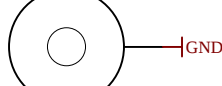
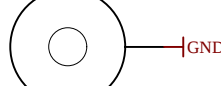
Steel Spacer M2.5 2mm SMD

Steel Spacer M2.5 2mm SMD

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Steel Spacer M2.5 2mm SMD

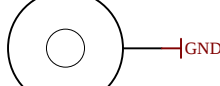
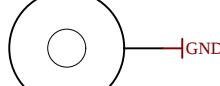
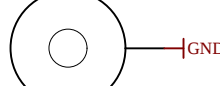
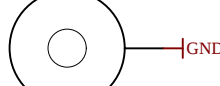
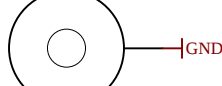
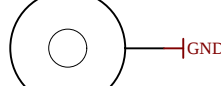
Steel Spacer M2.5 2mm SMD

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Steel Spacer M2.5 2mm SMD



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U27B

FMCAF_V18 ○ M15
SC_B1
N15
N19
P15
U21

VCCIO1 "PB2000&4000/7000"
VCCIO1
VCCIO1 x02 - 2000 & 4000 have same pinouts
VCCIO1
VCCIO1
VCCIO1

VCCIO1
VCCIO1
VCCIO1
VCCIO1
VCCIO1

L15 ○ FMCAF_V18
K19
K15
J15
F21

BANK1

A LA15 SC N D19
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A LA27 SC N K20
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F LA18 SC N L16
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A LA11 SC N K22
A LA13 SC P L21
A LA13 SC N L22
A LA17 SC P N22
A LA17 SC N N21

NC/PR12C_T
NC/PR12D_C
PR2C/2C_T
PR2D/2D_C
PR3A/3A_T-DQ0 R_GPLLT_IN
NC/PR15C_T
PR3B/3B_C-DQ0 R_GPLLT_IN
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PR3C/3C_T
PR3D/3D_C
PR4A/4A_T
PR4B/4B_C
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PR5C/6C_T
PR5D/6D_C
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PR6D/7D_C
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PR7D/8D_C
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NC/PR9D_C
NC/PR10A_T
NC/PR10B_C
PR8C/10C_T
PR8D/10D_C
PR9A/11A_T-DQ0
PR9B/11B_C-DQ0
PR9C/11C_T
PR9D/11D_C
PR10A/12A_T-DQ0 || PCLKT1_0
PR10B/12B_C-DQ0 || PCLKC1_0

NC/PR12C_T
NC/PR12D_C
PR10C/15A_T
PR10D/15B_C
NC/PR15C_T
NC/PR15D_C
PR13A/16A_T-DQS1
PR13B/16B_C-DQS1N
PR13C/16C_T
PR13D/16D_C
PR14A/17A_T-DQ1
PR14B/17B_C-DQ1
PR14C/17C_T
PR14D/17D_C
PR15A/18A_T-DQ1
PR15B/18B_C-DQ1
NC/PR18C_T
NC/PR18D_C
NC/PR19A_T
NC/PR19B_C
PR15C/19C_T-DQ1
PR15D/19D_C-DQ1
PR16A/21A_T-DQ1
PR16B/21B_C-DQ1
PR16C/21C_T
PR16D/21D_C
PR17A/22A_T
PR17B/22B_C
PR17C/22C_T
PR17D/22D_C
PR18A/23A_T-DQ1
PR18B/23B_C-DQ1
PR18C/23C_T
PR18D/23D_C
PR19A/24A_T-DQ1
PR19B/24B_C-DQ1
PR19C/24C_T
PR19D/24D_C
PR20A/25A_T
PR20B/25B_C
PR20C/25C_T
PR20D/25D_C

M22 A LA16 SC P
M21 A LA16 SC N
N20 F LA07 SC P
P21 F LA07 SC N
M16 F LA19 SC N
M17 F LA19 SC P
P20 A LA24 SC P
R22 A LA24 SC N
M19 F LA06 SC N
M18 F LA06 SC P
R21 F LA10 SC P
R20 F LA10 SC N
M20 F LA08 SC P
N18 F LA08 SC N
T22 A LA29 SC N
T21 A LA29 SC P
N16 F LA17 SC N
N17 F LA17 SC P
T20 A LA28 SC P
U22 A LA28 SC N
P19 F LA12 SC P
P18 F LA12 SC N
U20 A LA31 SC P
V22 A LA31 SC N
R19 F LA09 SC P
R18 F LA09 SC N
W22 A LA30 SC N
V21 A LA30 SC P
T19 F LA11 SC P
U19 F LA11 SC N
Y22 A LA33 SC P
AA22 A LA33 SC N
T18 F LA14 SC P
R17 F LA14 SC N
W20 F LA16 SC P
V19 F LA16 SC N
R16 F LA15 SC N
T17 F LA15 SC P
Y21 A LA32 SC P
Y20 A LA32 SC N
U18 F LA13 SC P
U17 F LA13 SC N

LCMX02-7000HC-4FG484C

U27D

BANK3

SC_B3 FMCAF_V18 ○ U2
N8

VCCIO3 "PB2000&4000/7000"
VCCIO3
VCCIO3 x02 - 2000 & 4000 have same pinouts

F LA33 SC N R2
F LA33 SC P R3
F LA32 SC N M3
F LA32 SC P N5
F LA30 SC P U3
F LA30 SC N V1
F LA20 SC P N7
F LA20 SC N N6
F LA28 SC P W1
F LA28 SC N V2
F LA23 SC P P7
F LA23 SC N P6
F LA31 SC N V3
F LA31 SC P W2

NC/PL22C_T
NC/PL22D_C
PL15C/18C_T
PL15D/18D_C
PL16A/19A_T
PL16B/19B_C
PL16C/19C_T
PL16D/19D_C
NC/PL21A_T
NC/PL21B_C
PL17C/21C_T
PL17D/21D_C
PL17A/22A_T || PCLKT3_0
PL17B/22B_C || PLCKC3_0

T4 F LA26 SC P
U4 F LA26 SC N
Y1 F LA29 SC P
AA1 F LA29 SC N
T5 F LA22 SC N
R6 F LA22 SC P
W3 F LA24 SC N
V4 F LA24 SC P
R7 F LA21 SC P
T6 F LA21 SC N
Y2 F LA25 SC N
W4 F LA25 SC P
U5 F LA27 SC N
T7 F LA27 SC P

LCMX02-7000HC-4FG484C

U27E

BANK4

SC_B4 PS_V18 ○ K4
L8

VCCIO4 "PB2000&4000/7000"
VCCIO4
VCCIO4 x02 - 2000 & 4000 have same pinouts

ERR_OUT H1
INIT_B J3
PHY_LED0 L5
PLL_RST L4
ERR_STATUS J2
PG_GT_L K3
ETH_RST L6
MR L7
PHY_CLK125M K2
PHY_LED2 K1
I2C_RST L2
PHY_LED1 L1

PL9A/10A_T
PL9B/10B_C
PL9C/10C_T
PL9D/10D_C
PL10A/11A_T
PL10B/11B_C
NC/PL11C_T
NC/PL11D_C
PL10D/12A_T || PCLKT4_0
PL10C/12B_C || PCLKC4_0
NC/PL12C_T
NC/PL12D_C

LCMX02-7000HC-4FG484C

N4 PS_V18 ○ M8

N1
N2
M2
N3
P2
M7
M6
P3
F1
M4
M5

USB0_RST

DP_EN

U27F

BANK5

SC_B5 PS_V18 ○ F2
J8
K8

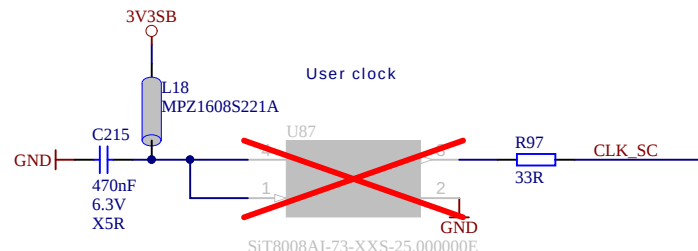
VCCIO5 "PB2000&4000/7000"
VCCIO5
VCCIO5 x02 - 2000 & 4000 have same pinouts

B64 T2 C3
B65 T1 C2
MIO6 F6
MIO24 F5
B65 T2 E4
B64 T1 D3
XMOD2 E G6
B64 T3 B1
B65 T3 C1
XMOD2 G H6
MIO25 G5
PROG_B E2
B66 T1 D1
B66 T2 F4
DONE G4

PL2A/2A_T
PL2B/2B_C
PL2C/2C_T
PL2D/2D_C
PL3A/3A_T || L_GPLLT_FB
PL3B/3B_C || L_GPLLC_FB
PL3C/3C_T
PL3D/3D_C
PL4A/4A_T || L_GPLLT_IN
PL4B/4B_C || LGPLLC_IN
PL4C/4C_T
PL4D/4D_C
PL5A/5A_T
PL5B/5B_C
PL5C/5C_T
PL5D/5D_C

LCMX02-7000HC-4FG484C

F1 SI5345_CLK
F3 B66 T3
H5
H4 B67 T3
F1 B67 T1
G3 B67 T2
J5
J4
G2 MODE2
G1 MODE3
K6 XMOD2_B
K7 XMOD2_A
H3 MODE0
H2 MODE1
K5
K3



Title: TEB0911 - CPLD			
A4	Number: TEB0911 ZU9EG1E	Rev. 03	
Date: 2017-10-16	Copyright: Trenz Electronic GmbH		Page32 of 58
Filename: SC2.schdoc			

1

2

3

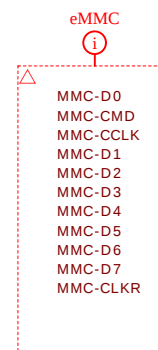
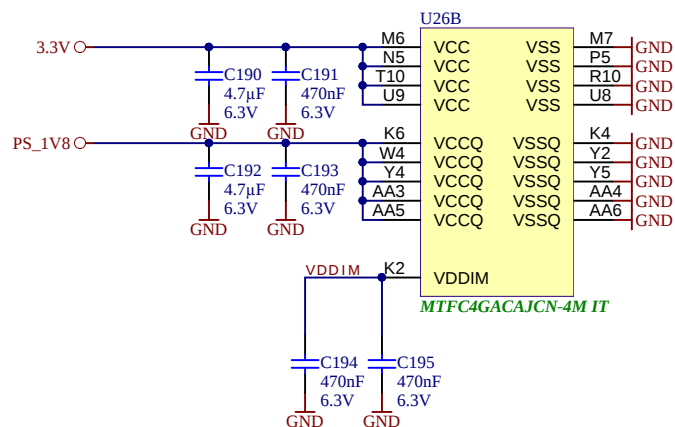
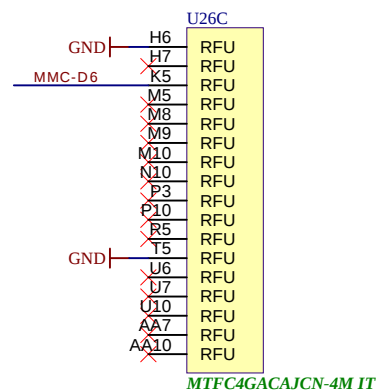
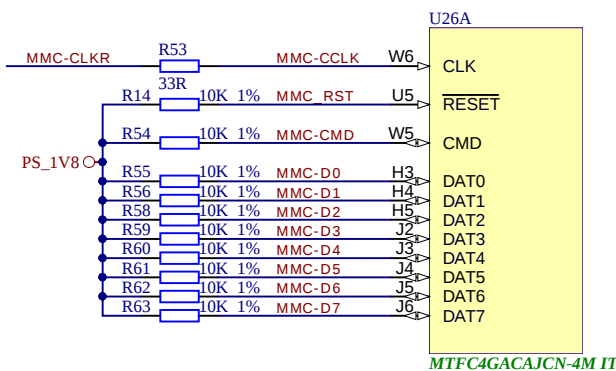
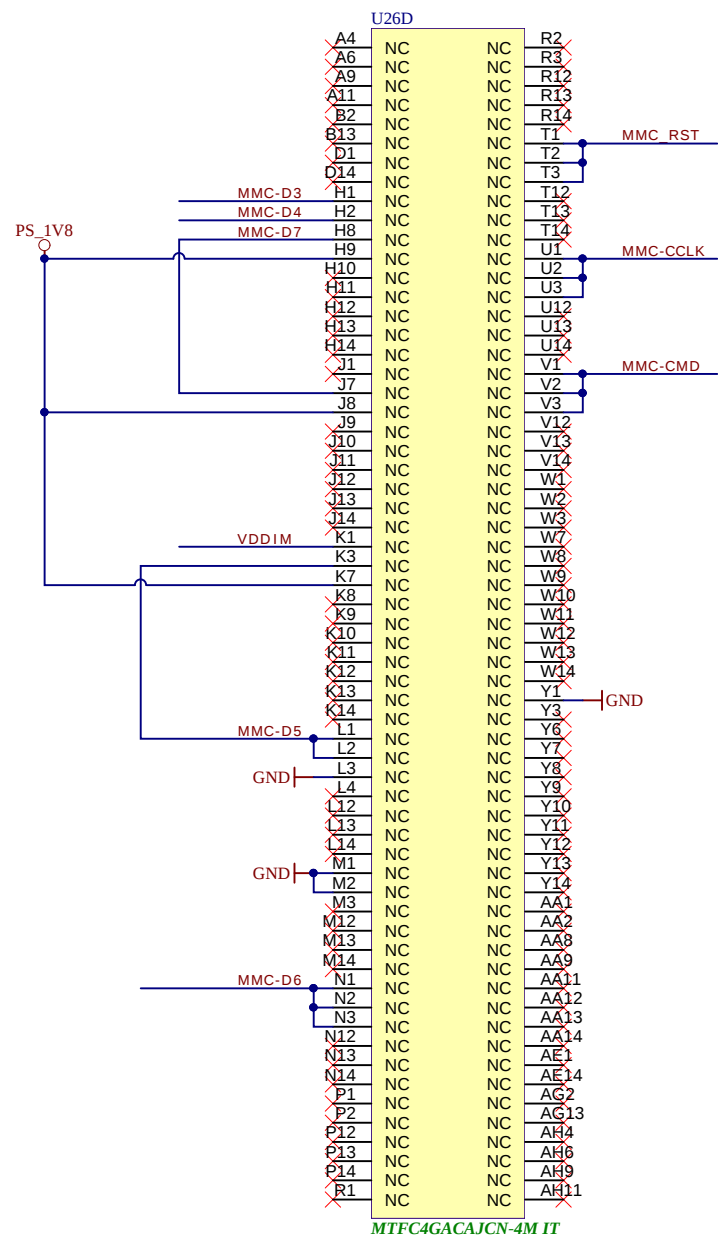
4

1

2

3

4



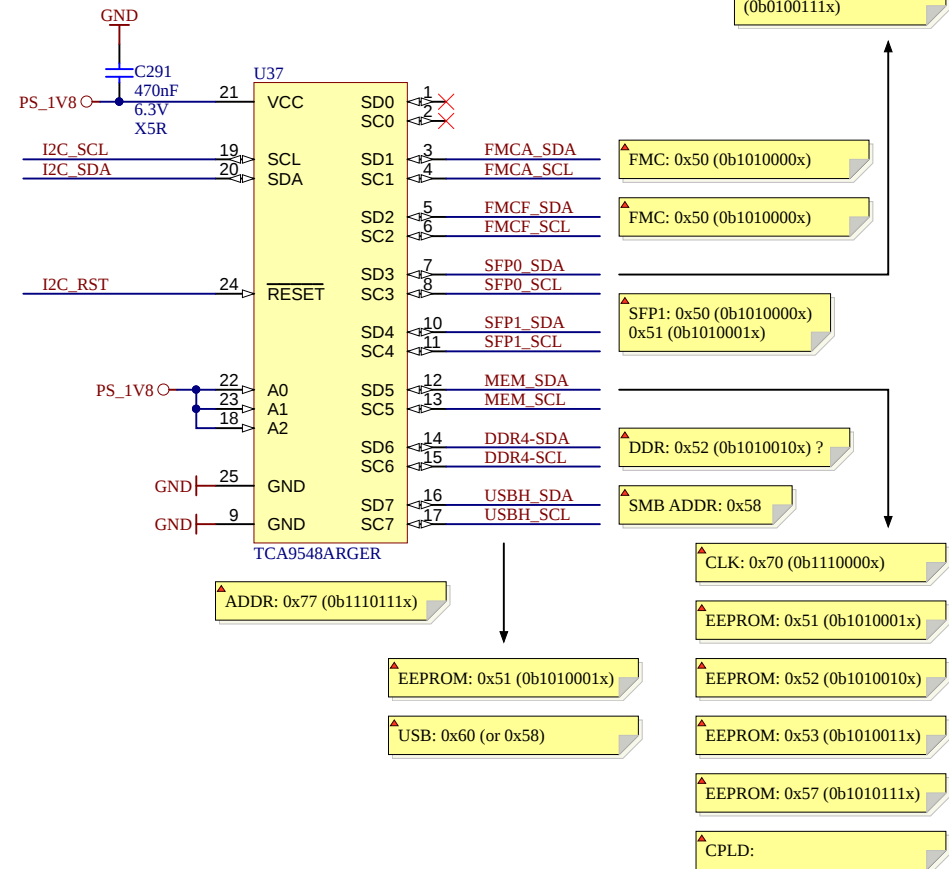
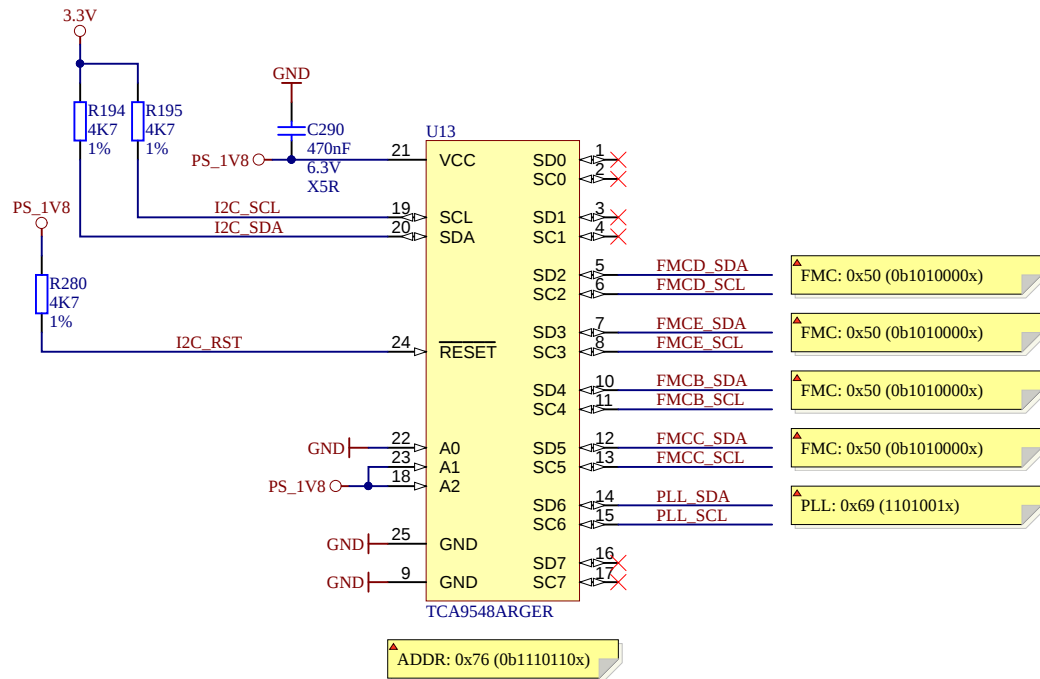
Title: TEB0911 - eMMC		
A4	Number: TEB0911 ZU9EG1E	Rev. 03
Date: 2017-10-16	Copyright: Trenz Electronic GmbH	Page34 of 58
Filename: eMMC.SchDoc		

1

2

3

4



Title: TEB0911 - I2C System			
A4	Number: TEB0911 ZU9EG1E	Rev. 03	
Date: 2017-10-16	Copyright: Trenz Electronic GmbH		Page 35 of 58
Filename: I2C_system.schdoc			

1

2

3

4

A

A

B

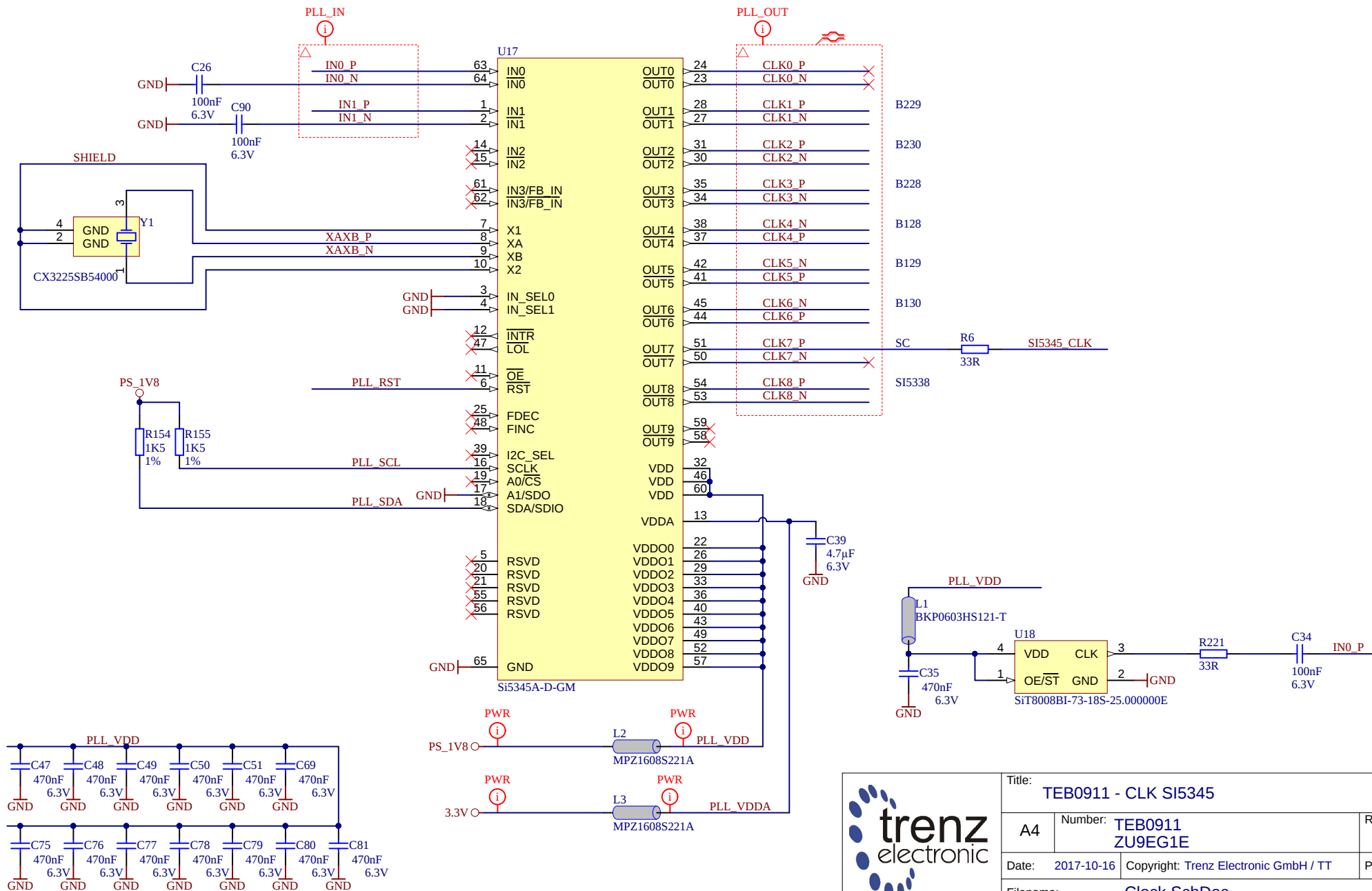
B

C

C

D

D



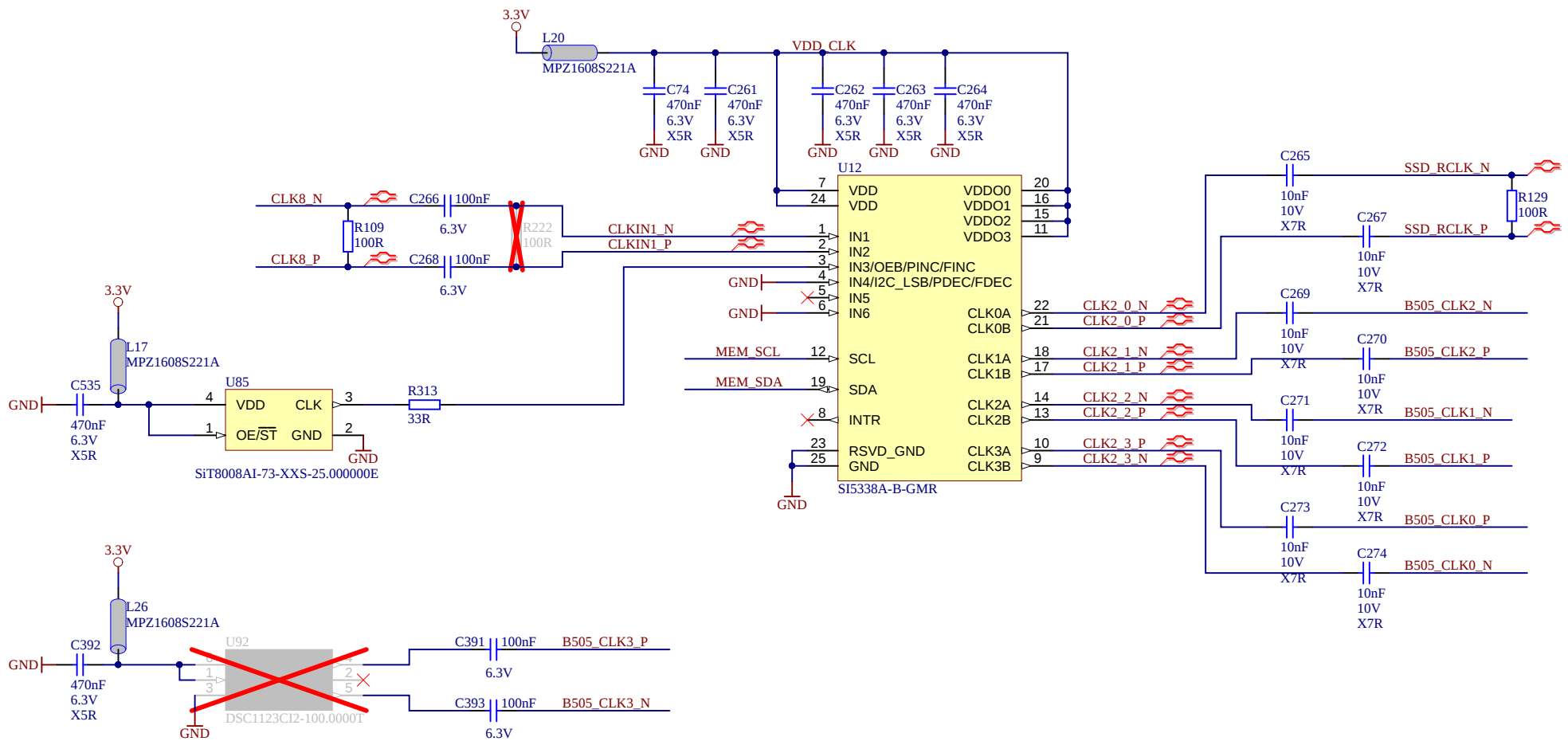
Title: TEB0911 - CLK SI5345			
A4	Number: TEB0911 ZU9EG1E	Rev. 03	
Date: 2017-10-16	Copyright: Trenz Electronic GmbH / TT		Page 36 of 58
Filename: Clock.SchDoc			

1

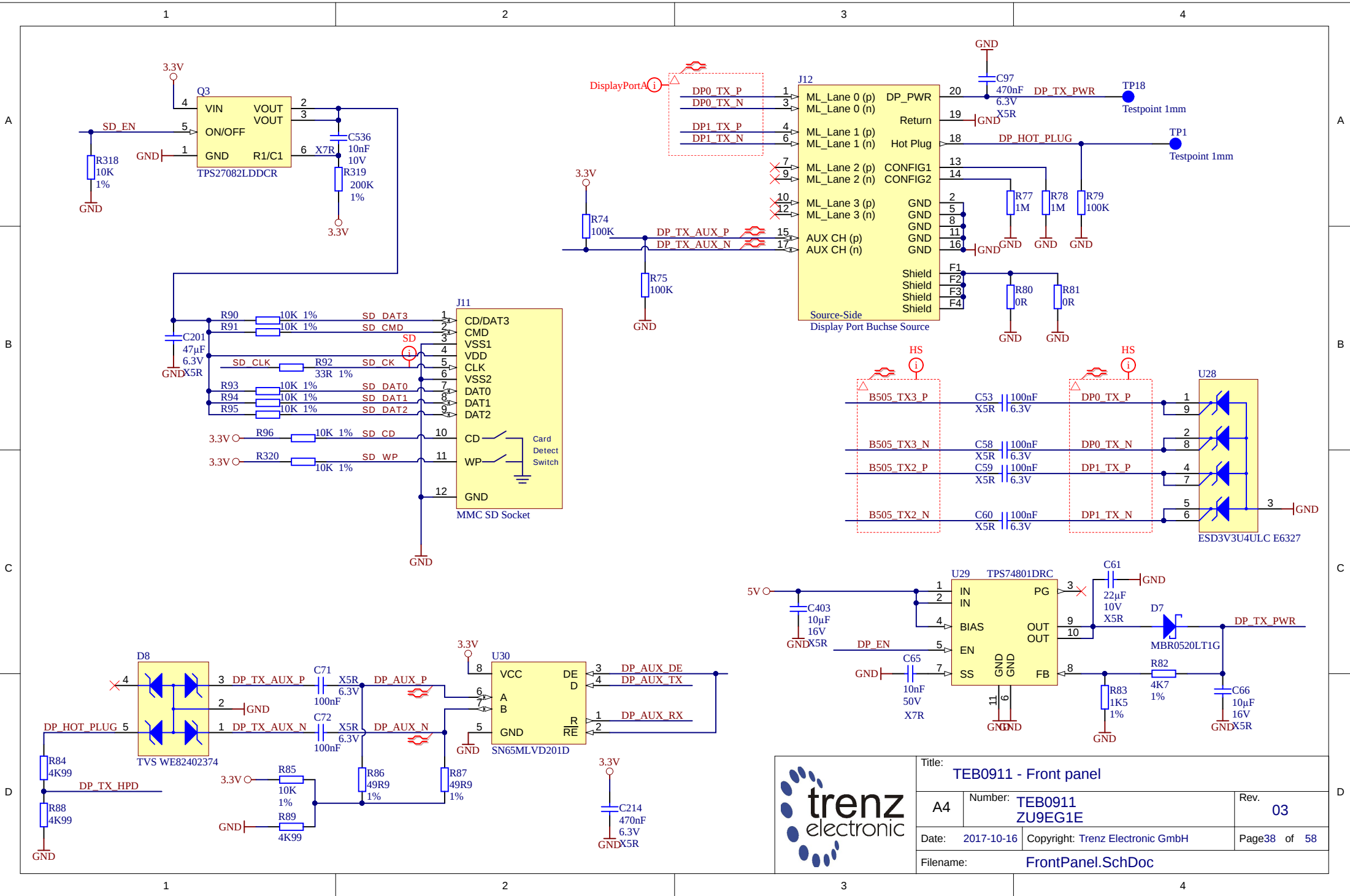
2

3

4



	Title: TEB0911 - CLK SI5338	
	A4	Number: TEB0911 ZU9EG1E
	Date: 2017-10-16	Copyright: Trenz Electronic GmbH
	Filename: Clock2.SchDoc	Page 37 of 58
Rev. 03		



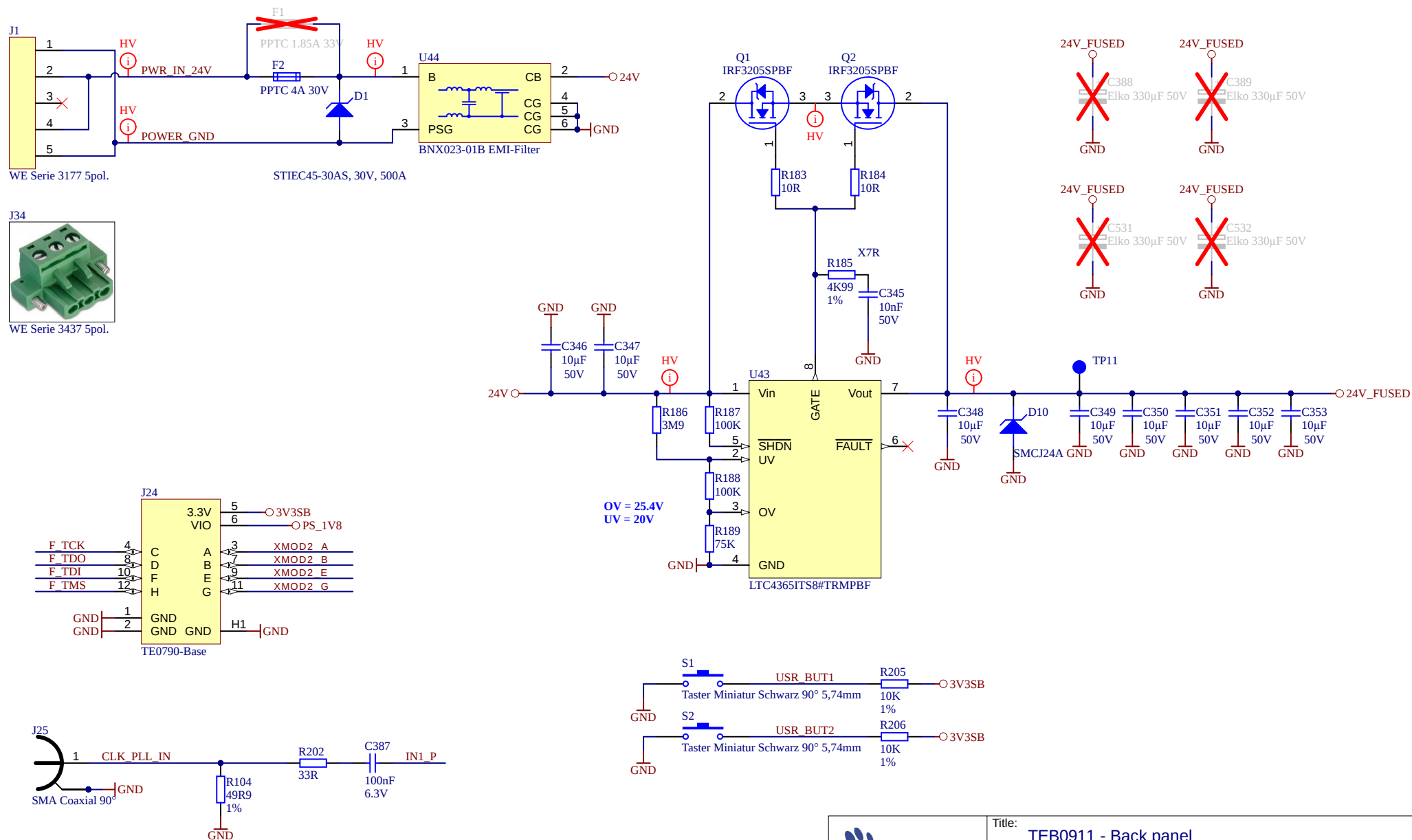
Title: TEB0911 - Front panel		
A4	Number: TEB0911 ZU9EG1E	Rev. 03
Date: 2017-10-16	Copyright: Trenz Electronic GmbH	
Filename: FrontPanel.SchDoc		Page38 of 58

1

2

3

4



Title: TEB0911 - Back panel		
A4	Number: TEB0911 ZU9EG1E	Rev. 03
Date: 2017-10-16	Copyright: Trenz Electronic GmbH	Page 39 of 58
Filename: BackPanel.SchDoc		

1

2

3

4

1

2

3

4

A

A

B

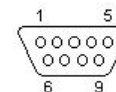
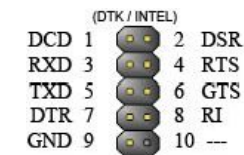
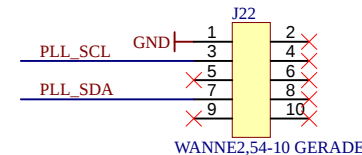
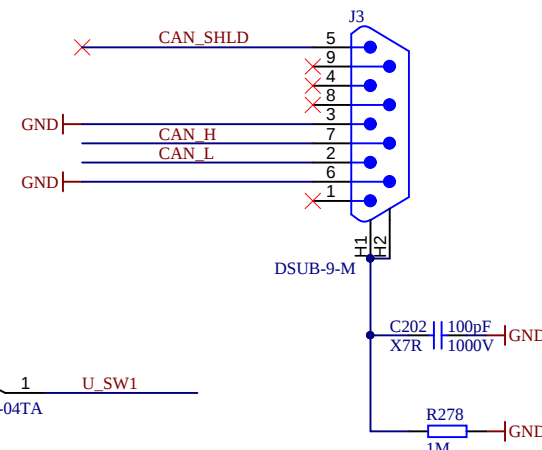
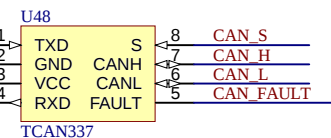
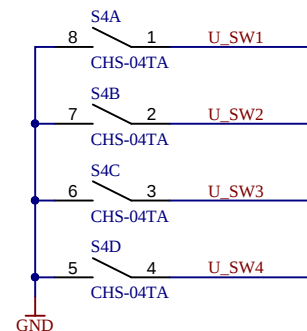
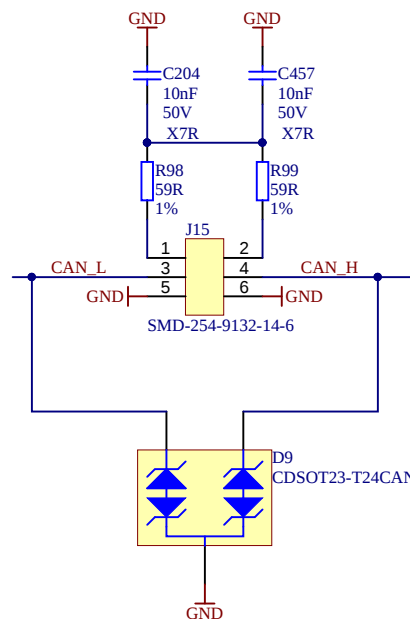
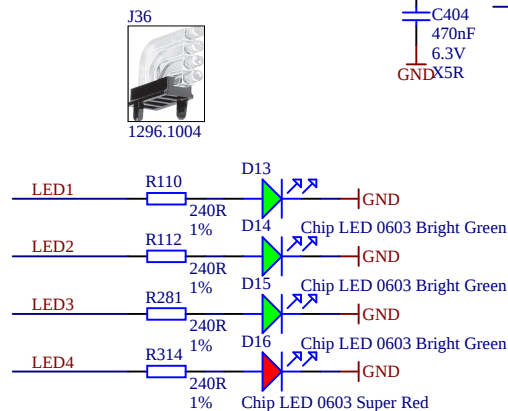
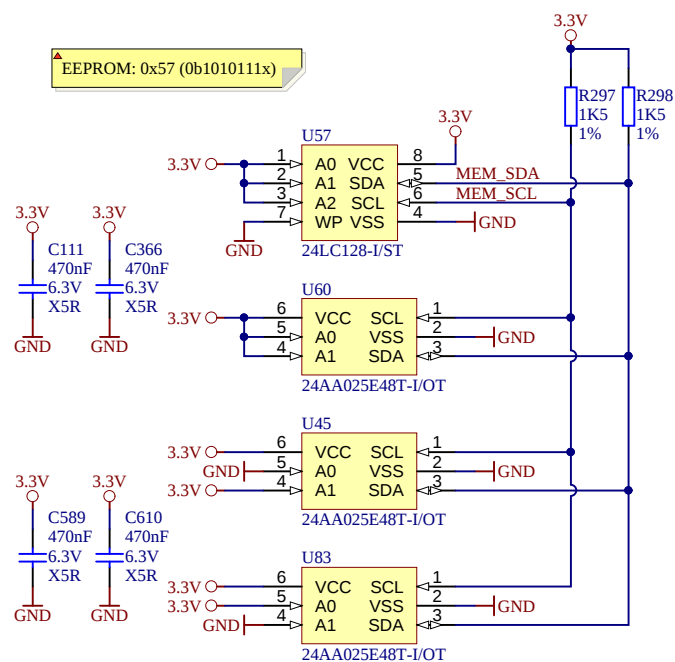
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C

C

D

D



1-1

2-3

3-5

4-7

5-9

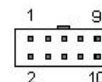
6-2

7-4

8-6

9-8

10--



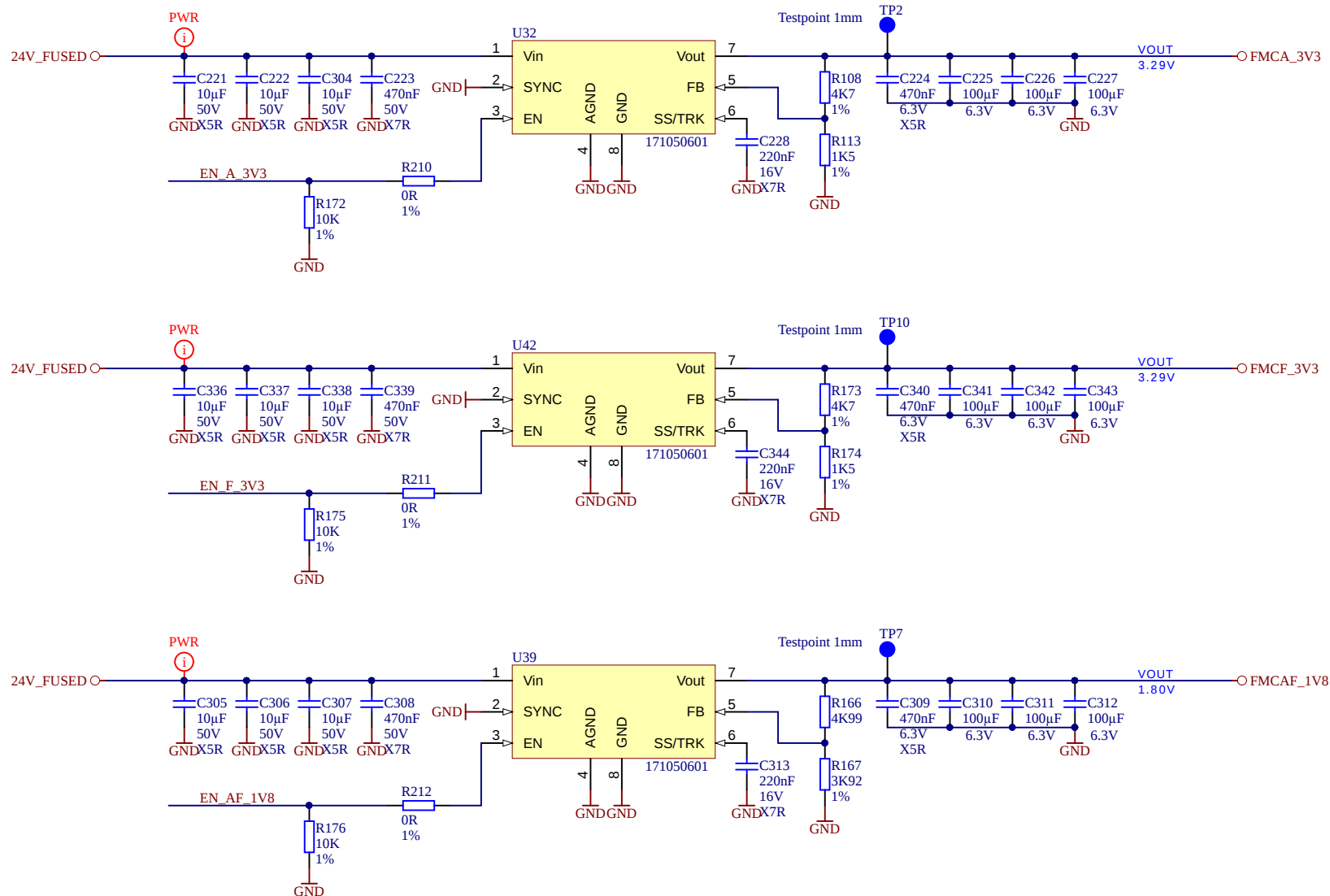
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A4	Number: TEB0911 ZU9EG1E	Rev. 03
Date: 2017-10-16	Copyright: Trenz Electronic GmbH	
Filename: MISC.schdoc	Page 40 of 58	

1

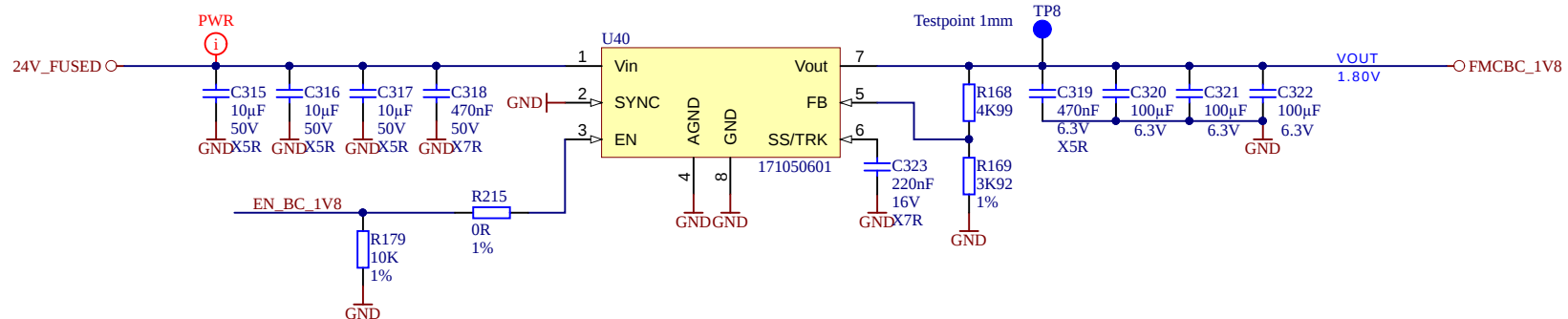
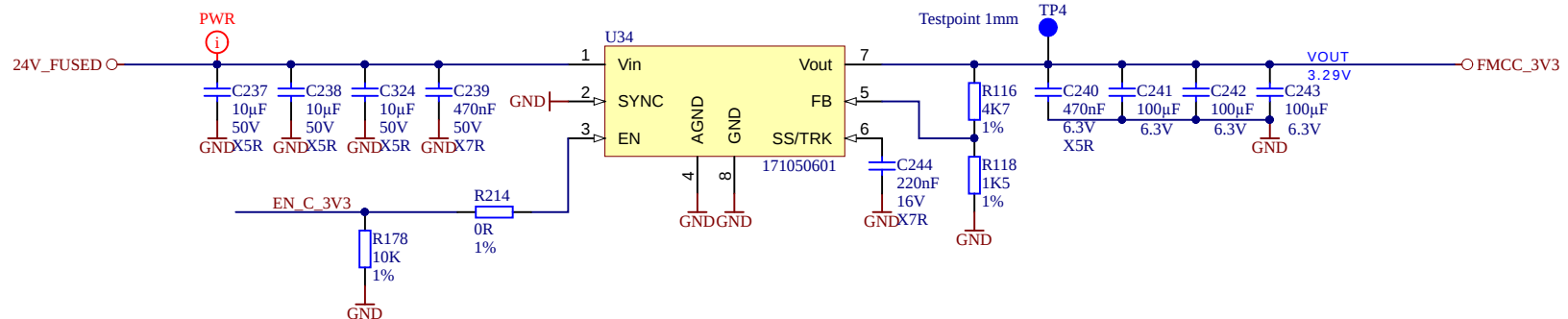
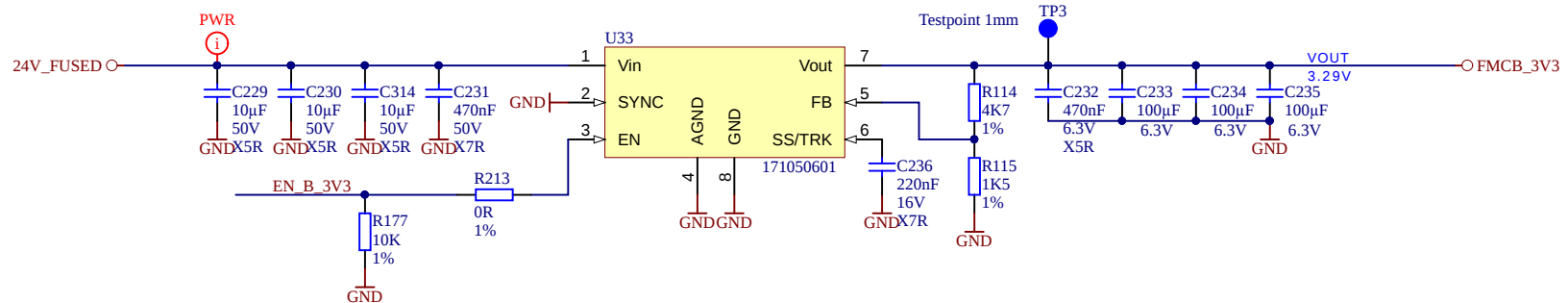
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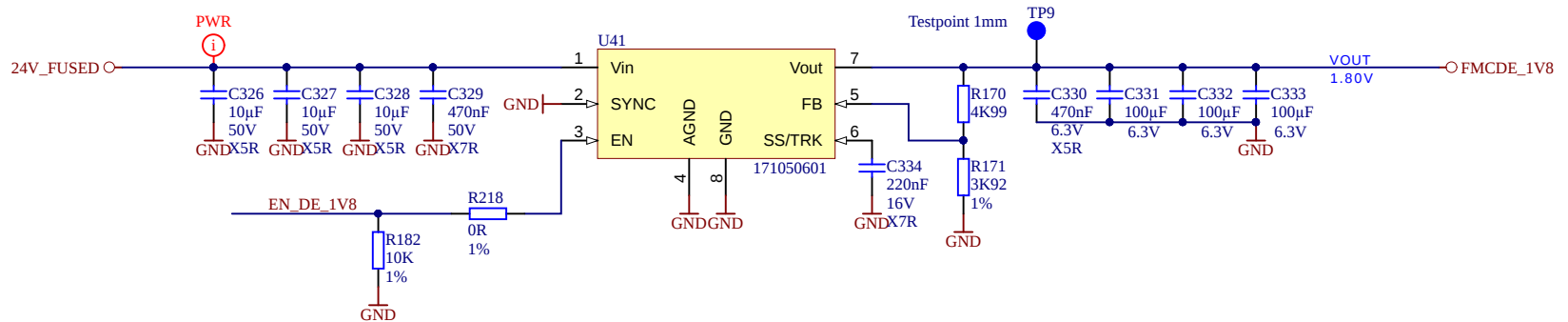
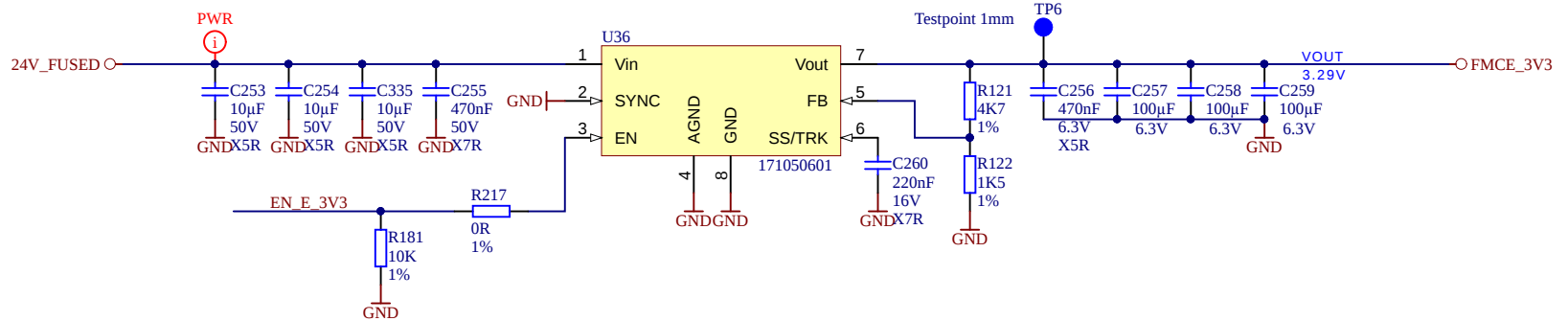
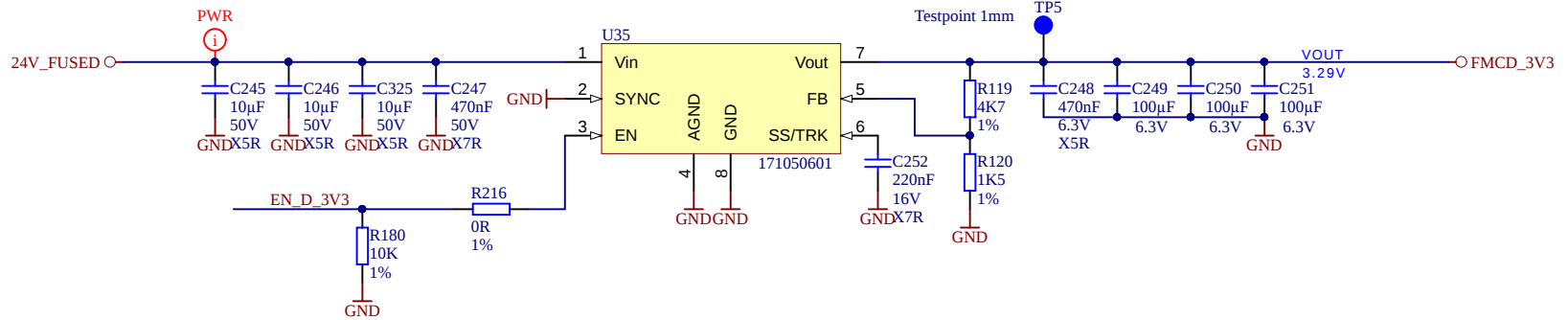
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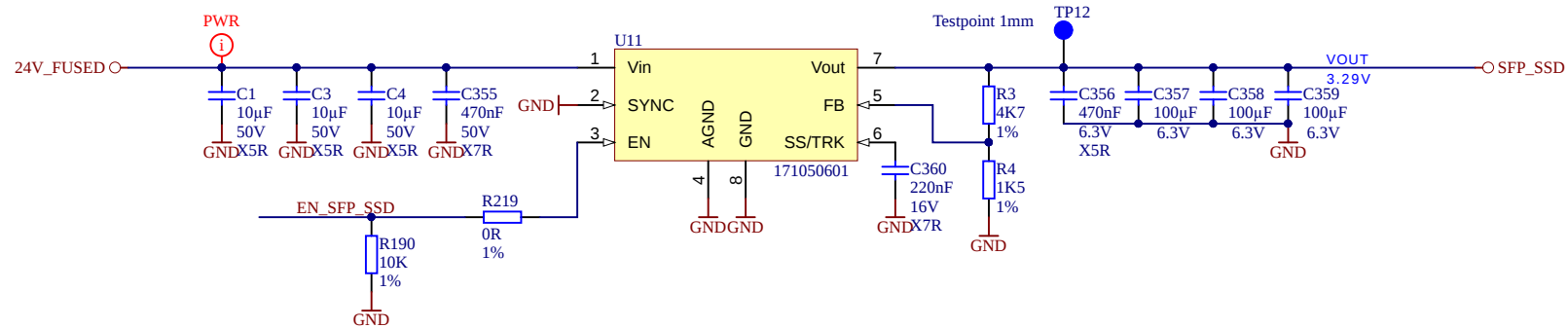
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	A4	Number: TEB0911 ZU9EG1E	Rev. 03
	Date: 2017-10-16	Copyright: Trenz Electronic GmbH	Page41 of 58
	Filename: POWER_1.schdoc		



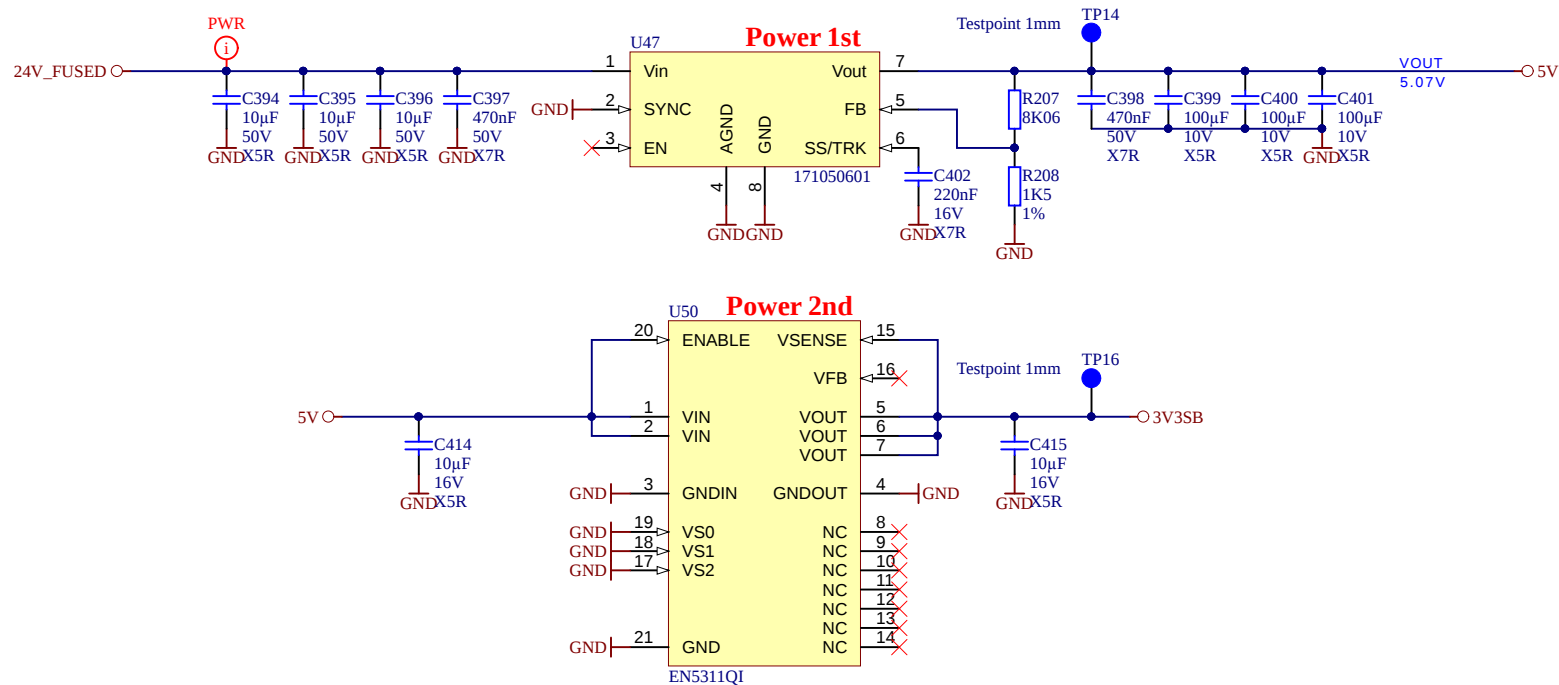
Title: TEB0911 - POWER		
A4	Number: TEB0911 ZU9EG1E	Rev. 03
Date: 2017-10-16	Copyright: Trenz Electronic GmbH	
Filename: POWER_2.schdoc		Page 42 of 58



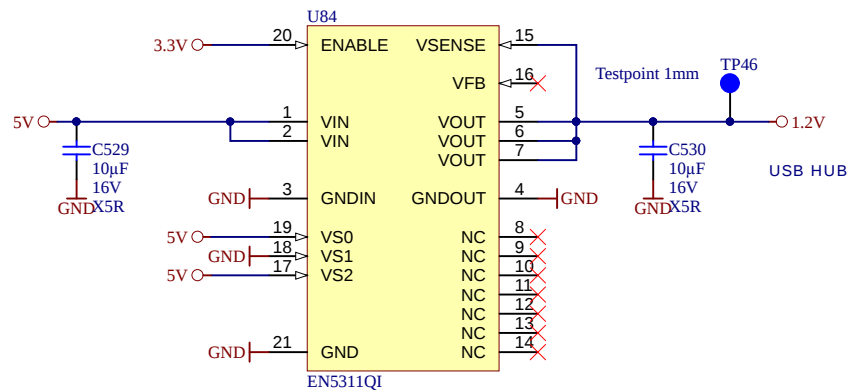
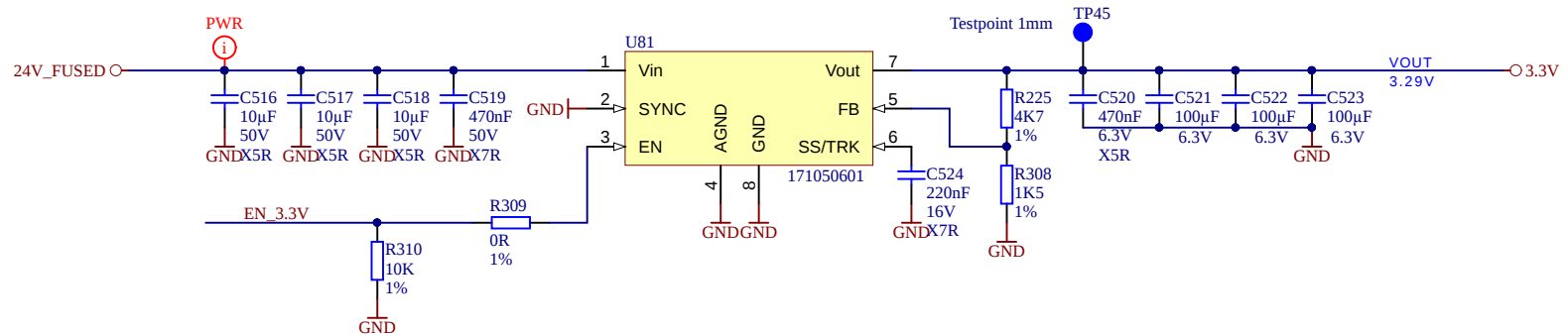
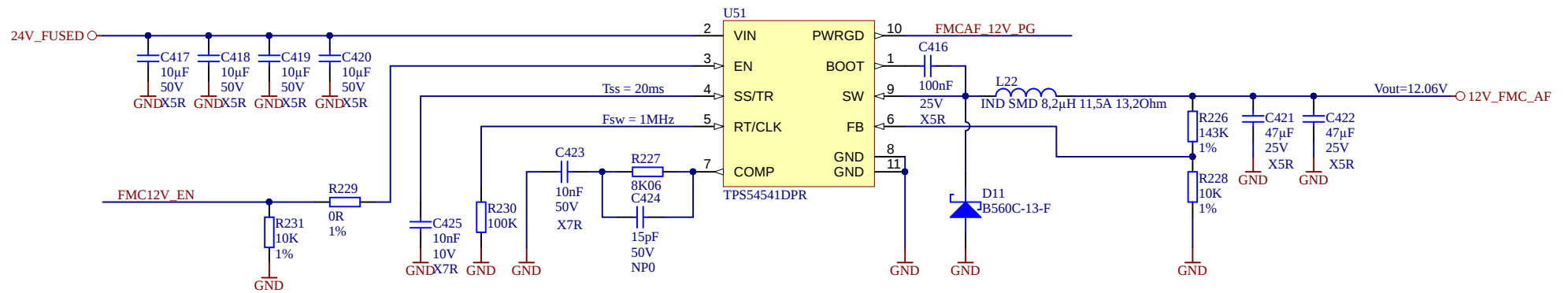
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		A4	Number: TEB0911 ZU9EG1E	Rev. 03
		Date: 2017-10-16	Copyright: Trenz Electronic GmbH	Page43 of 58
		Filename: POWER_3.schdoc		



	Title: TEB0911 - POWER		
	A4	Number: TEB0911 ZU9EG1E	Rev. 03
	Date: 2017-10-16	Copyright: Trenz Electronic GmbH	Page44 of 58
	Filename: POWER_4.schdoc		

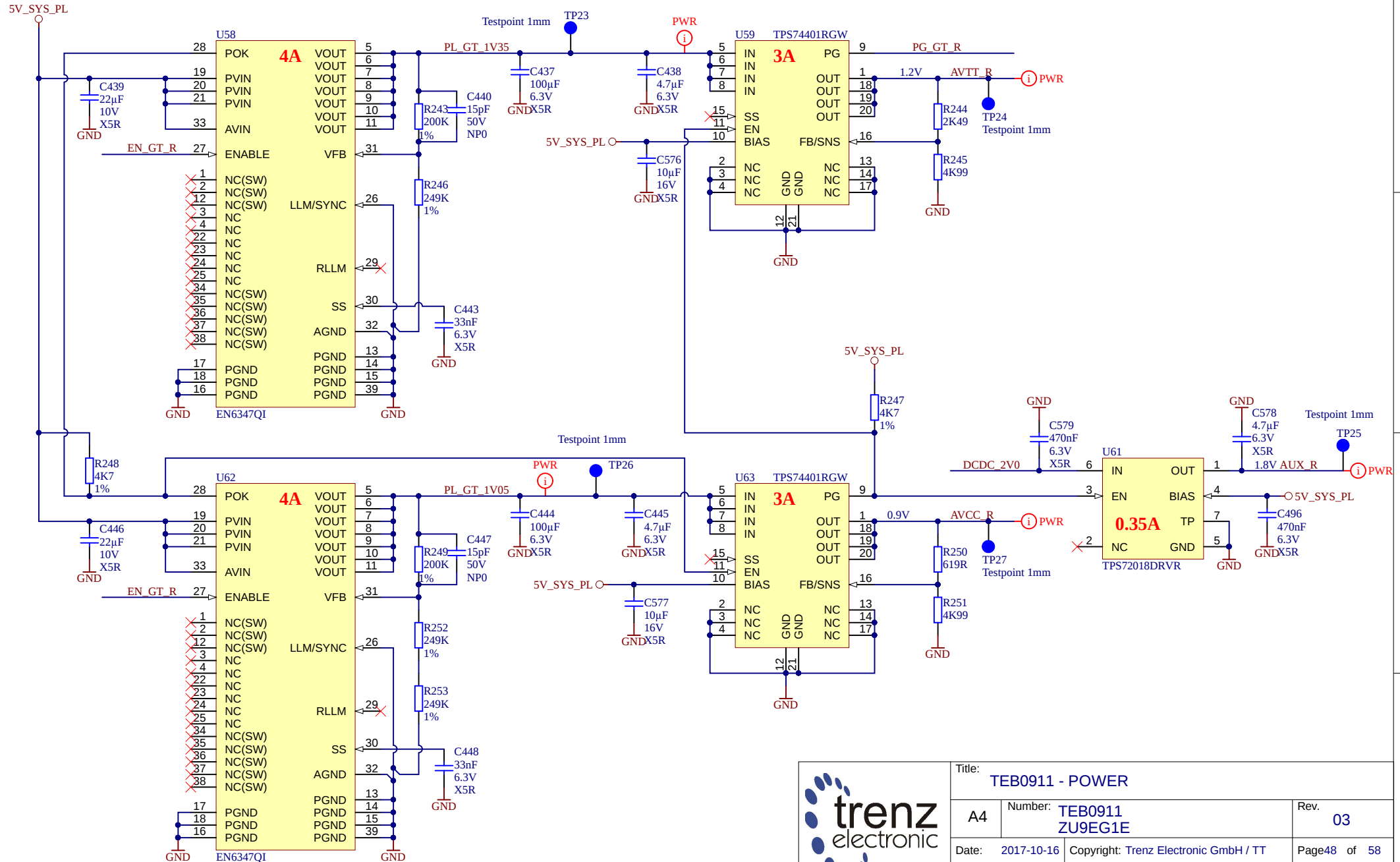


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A4	Number: TEB0911 ZU9EG1E	Rev. 03
Date: 2017-10-16	Copyright: Trenz Electronic GmbH	
Filename: POWER_5.schdoc		Page 45 of 58

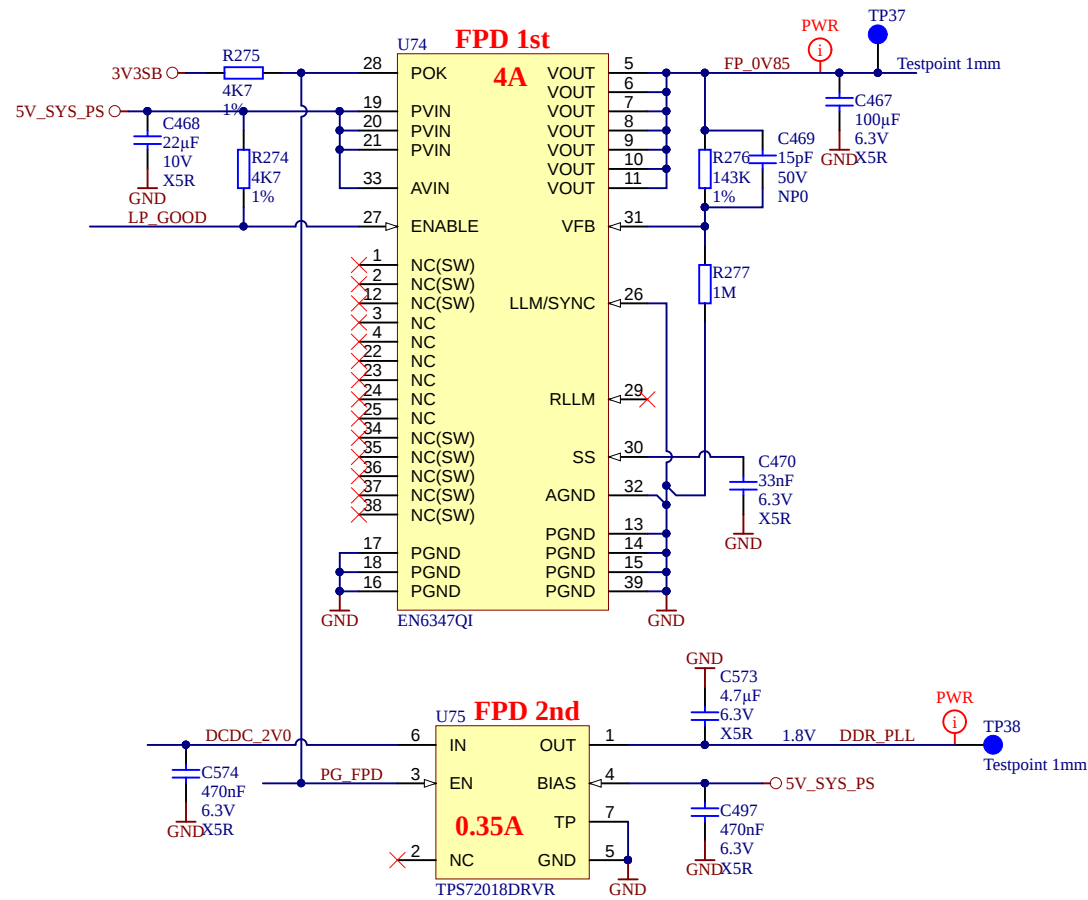


Title: TEB0911 - POWER			
A4	Number: TEB0911 ZU9EG1E	Rev. 03	
Date: 2017-10-16	Copyright: Trenz Electronic GmbH		Page 46 of 58
Filename: POWER_6.SchDoc			

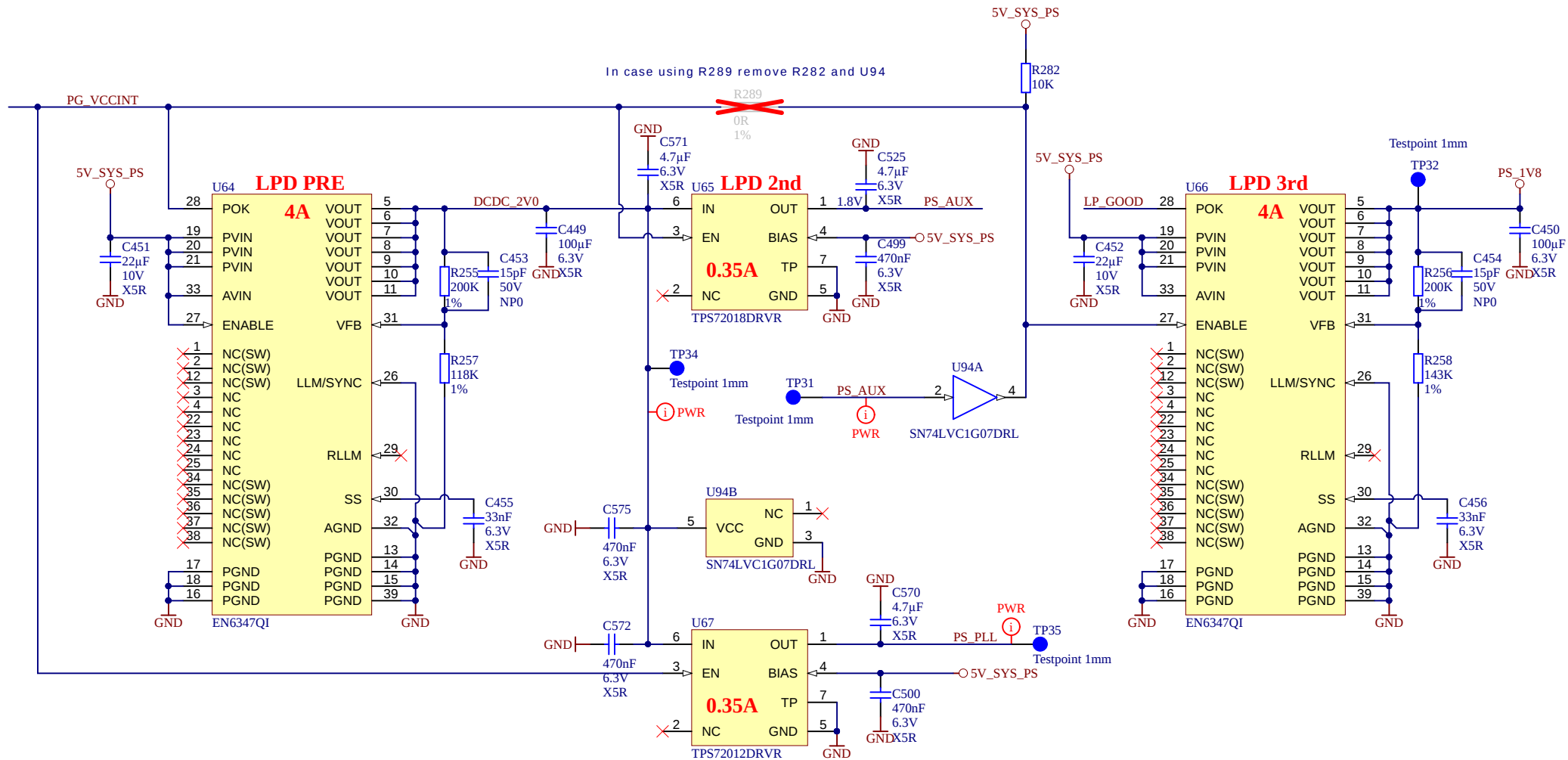




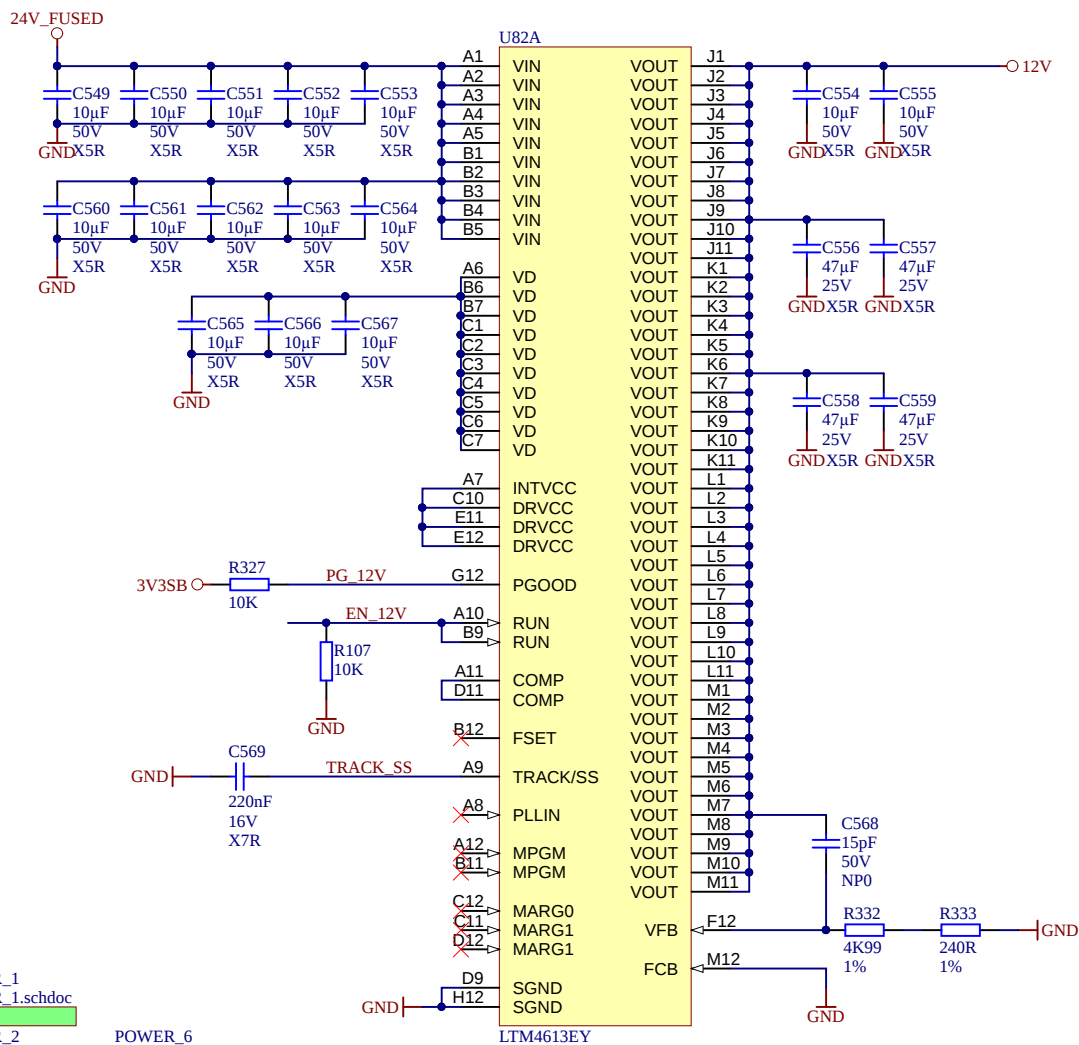
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		A4	Number: TEB0911 ZU9EG1E
Date: 2017-10-16	Copyright: Trenz Electronic GmbH / TT		Rev. 03
Filename: POWER_MGTPLR.schdoc		Page 48 of 58	



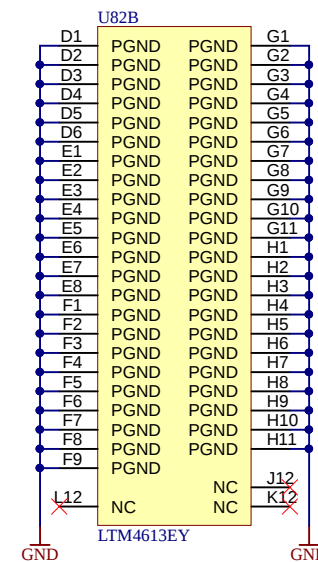
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		A4	Number: TEB0911 ZU9EG1E
Date: 2017-10-16	Copyright: Trenz Electronic GmbH / TT		Rev. 03
Filename: POWER_PSFP.schdoc		Page 50 of 58	



		Title: TEB0911 - POWER	
		A4	Number: TEB0911 ZU9EG1E
Date: 2017-10-16	Copyright: Trenz Electronic GmbH / TT		Rev. 03
Filename: POWER_PSLP.schdoc		Page 51 of 58	



POWER_1
POWER_1.schdoc
POWER_2
POWER_2.schdoc
POWER_3
POWER_3.schdoc
POWER_4
POWER_4.schdoc
POWER_5
POWER_5.schdoc
POWER_6
POWER_6.schdoc
PWR_SYS
PWR_SYS.SchDoc
PWR_CORE
PWR_CORE.SchDoc



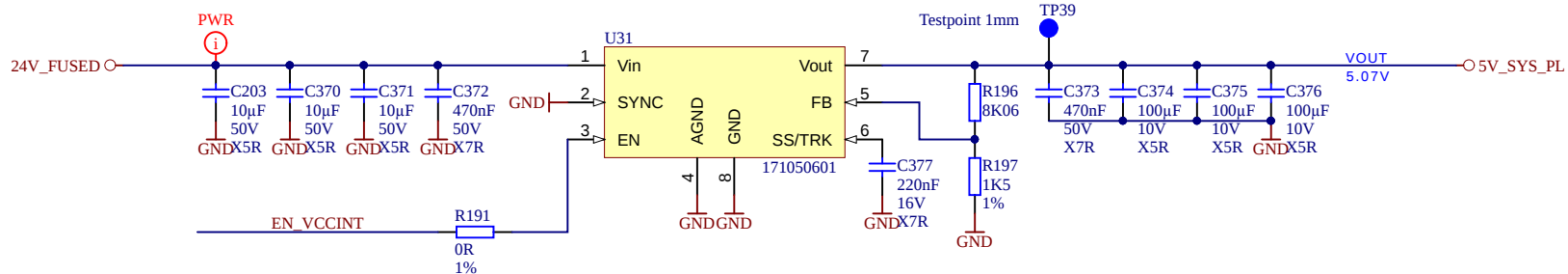
Title: TEB0911 - POWER		Rev. 03
A4	Number: TEB0911 ZU9EG1E	
Date: 2017-10-16	Copyright: Trenz Electronic GmbH	Page 53 of 58
Filename: PWR_IN.SchDoc		

1

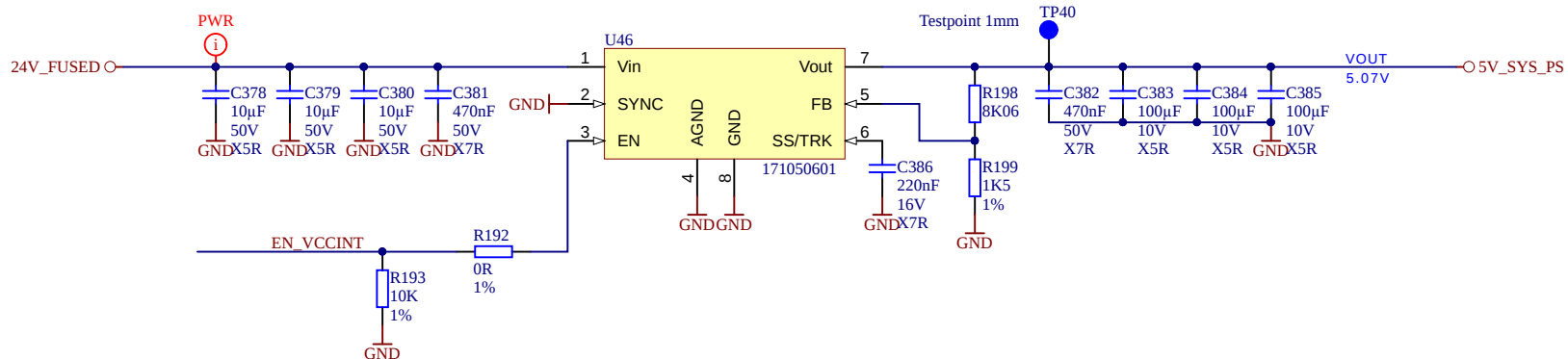
2

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4



POWER_MGTPS
POWER_MGTPS.schdoc
POWER_MGTPLR
POWER_MGTPLR.schdoc
POWER_MGTPLL
POWER_MGTPLL.schdoc
POWER_PSF
POWER_PSF.schdoc
POWER_PSLP
POWER_PSLP.schdoc



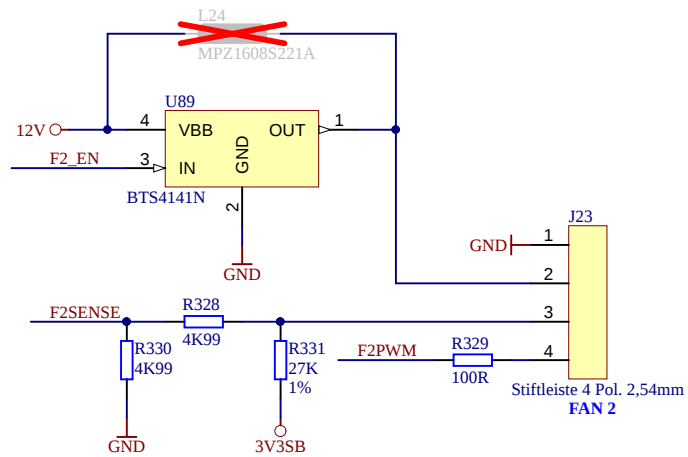
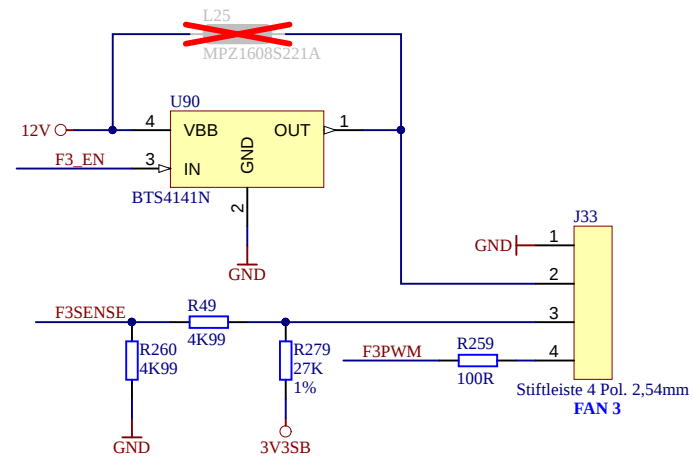
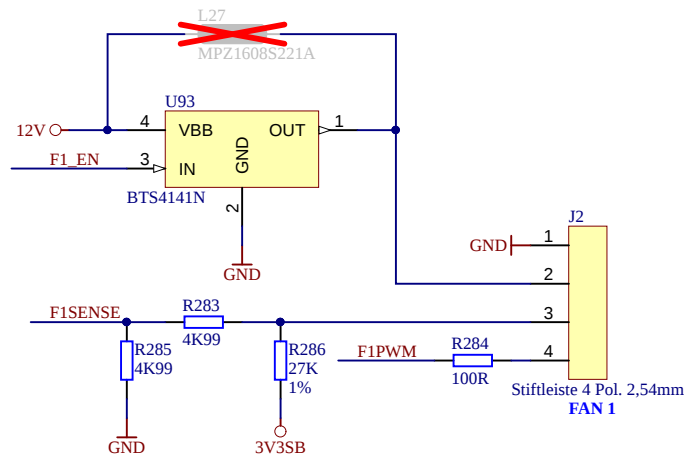
Title: TEB0911 - POWER		
A4	Number: TEB0911 ZU9EG1E	Rev. 03
Date: 2017-10-16	Copyright: Trenz Electronic GmbH	Page 54 of 58
Filename: PWR_SYS.SchDoc		

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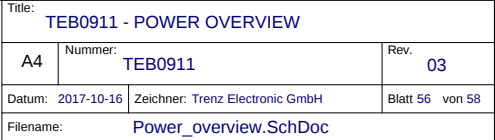
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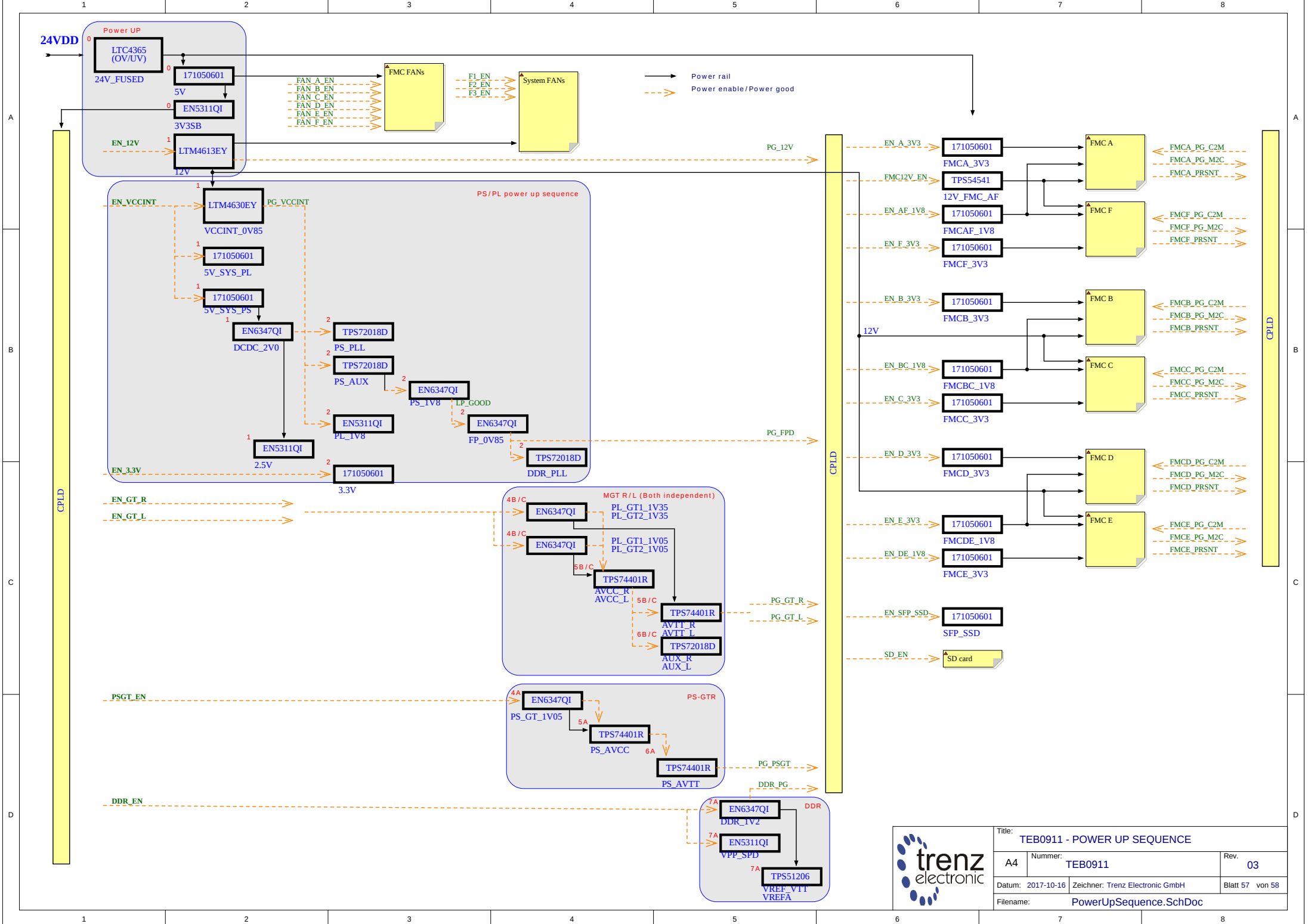
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4



	Title: TEB0911 - FAN connectors		
	A4	Number: TEB0911 ZU9EG1E	Rev. 03
	Date: 2017-10-16	Copyright: Trenz Electronic GmbH	Page55 of 58
	Filename: FAN_CON.schdoc		





Title: TEB0911 - POWER UP SEQUENCE		
A4	Number: TEB0911	Rev. 03
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A	REV02: 1. 2 CPLDs replaced with LCMXO2-7000HC-4FG484C (U27) 2. Single SFP connector removed. MGT lanes B129_TX/RX_1 connected to FMC F 3. DCDC U45 (power supply of 2xSFP and 1xSFP connectors) removed. 2xSFP powered by U11. 4. HDMI: lane B505_TX2 connected to DP1_TX and lane B505_TX3 to DP0_TX. 5. Supervisor U69 TPS3106E33 replaced with TPS3106E09 6. U17 (SI5345): IN_SEL0/1 disconnected from CPLD and connected to GND 7. Added new clock source U87 (SIT8008AI-73-XXS-25.000000E). Connected to CPLD. Not fitted 8. I2C pull-ups R154/R155: changed from 4.7k to 1.5kOhm 9. FAN: R285, R330, R260 changed to 27K (F*SENSE) 10. Added fuse F2 (4A). Fuse F1 (1.85A) "not fitted" 11. C354 (PWR_IN_24V/GND) deleted 12. Removed clock loop-back OUT9P/N - IN2P/N (U17, SI5345) 13. OUT7 (P) of U17 (clock source SI5345) connected to CPLD U27 Bank 5. 14. Signal SD Card detect SD_CD connected to CPLD, MIO45 connected to CPLD 15. Signals FMC*_PG_C2M(M2C) pulled-up to 3V3SB (was FMC*_3V3) 16. CLK_OUT U15 (USB PHY), serial resistor value R13 changed from 33 to 0 Ohm 17. EN5311QI: Cout value changed from 22uF to 10uF 18. BOM optimization 19. Added connections from CPLD to FMC A and F (x_LA06_SC - x_LA33_SC) 20. Net EN_VCCINT connected also to DCDC U31 (5V_SYS_PL) and U46 (5V_SYS_PS) 21. Added 2 2kb EEPROM (U45/U43) 22. Added power-up sequencing 23. MIO6 connected to CPLD 24. Signals EN_SFP was renamed to EN_SFP_SSD, 12V_FMC was renamed to 12V_FMC_AF, FMC12V_PG to FMCAF_12V_PG 25. Nets names F_LA02/03/04 was swapped. REV03: 1. Signals C_LA27_P/N swapped: C_LA27_P connected to pin R10 (IO_L11P_T1U_N8_GC_67), C_LA27_N connected to R9 (IO_L11N_T1U_N9_GC_67) of U1 (XCZU9EG-1FFVB1156)							A												
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