

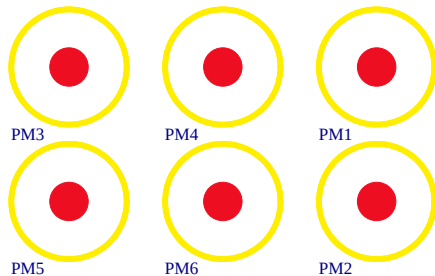
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Ethernet Ethernet.SchDoc	U_USBP USB-PHY.SchDoc	U_SC_P SC_POWER.schdoc
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U_SSD SSD.SchDoc	U_CLK Clock.SchDoc	U_POWER_SEQ PowerUpSequence.SchDoc
U_FrontPanel FrontPanel.SchDoc	U_I2C I2C_system.schdoc	
U_USB USB.SchDoc	U_eMMC eMMC.SchDoc	
U_BackPanel BackPanel.SchDoc	U_DDRP DDR_PWR.SchDoc	
U_FMC PWR FMC PWR.SchDoc	U_DDR DDR_SODIMM.SchDoc	
U_ZU ZU.SchDoc	U_CLK2 Clock2.SchDoc	
U_SFP_CTRL SFP_CTRL.SchDoc	U_SC2 SC2.schdoc	
U_SC SC.schdoc	U_MISC MISC.schdoc	
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U_FAN FAN_CON.schdoc	U_REVCH Revision_changes.SchDoc	

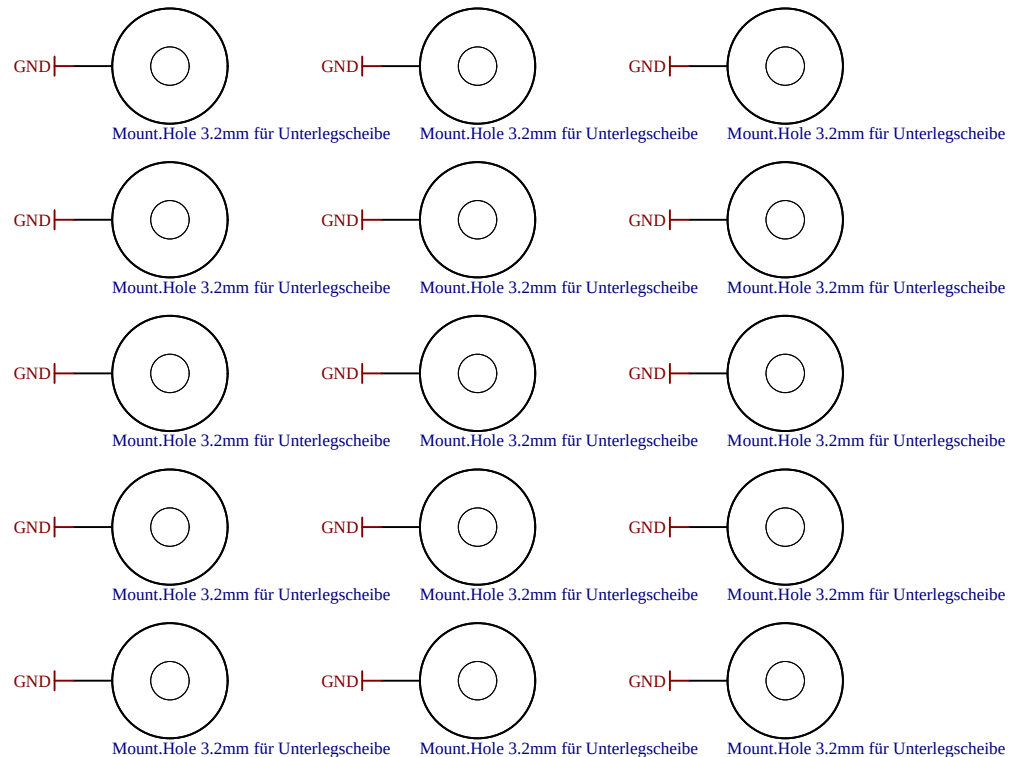


Serial
Serialnumber 6,3 x 6.3mm

LOGO1

TE Logo PRINT Layer

LOGO PRINT



Assembly variant	9BEX1MA
Created by	MR
Modified by	MR
Modified at	2021-10-12
SVN Revision	9077



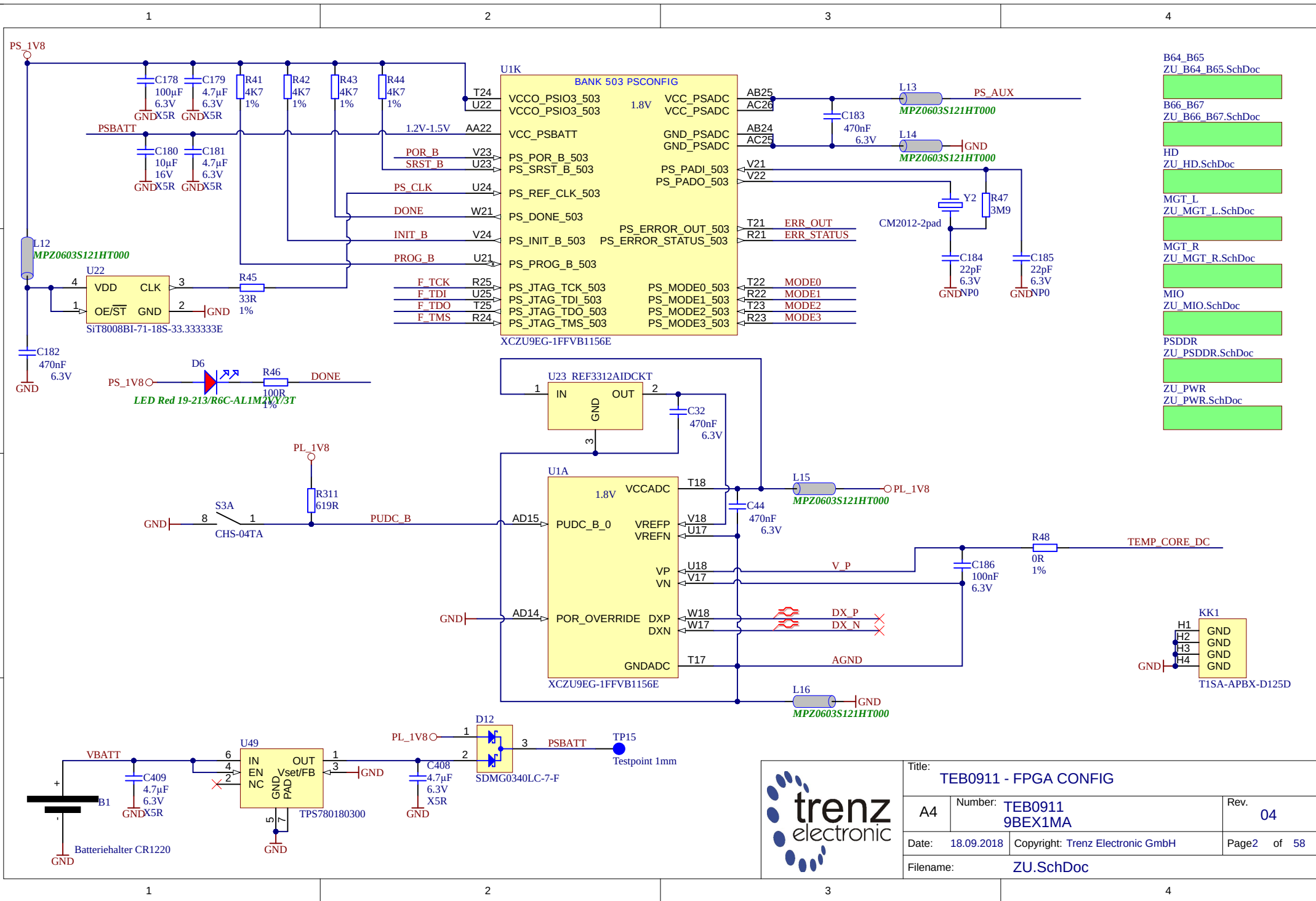
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A4	Number: TEB0911 9BEX1MA	Rev. 04
Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page1 of 58
Filename: TEB0911.SchDoc		

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B64_B65	ZU_B64_B65.SchDoc
B66_B67	ZU_B66_B67.SchDoc
HD	ZU_HD.SchDoc
MGT_L	ZU_MGT_L.SchDoc
MGT_R	ZU_MGT_R.SchDoc
MIO	ZU_MIO.SchDoc
PSDDR	ZU_PSDDR.SchDoc
ZU_PWR	ZU_PWR.SchDoc

KK1	
H1	GND
H2	GND
H3	GND
H4	GND

T1SA-APBX-D125D

Title: TEB0911 - FPGA CONFIG		
A4	Number: TEB0911 9BEX1MA	Rev. 04
Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page2 of 58
Filename: ZU.SchDoc		



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A

A

B

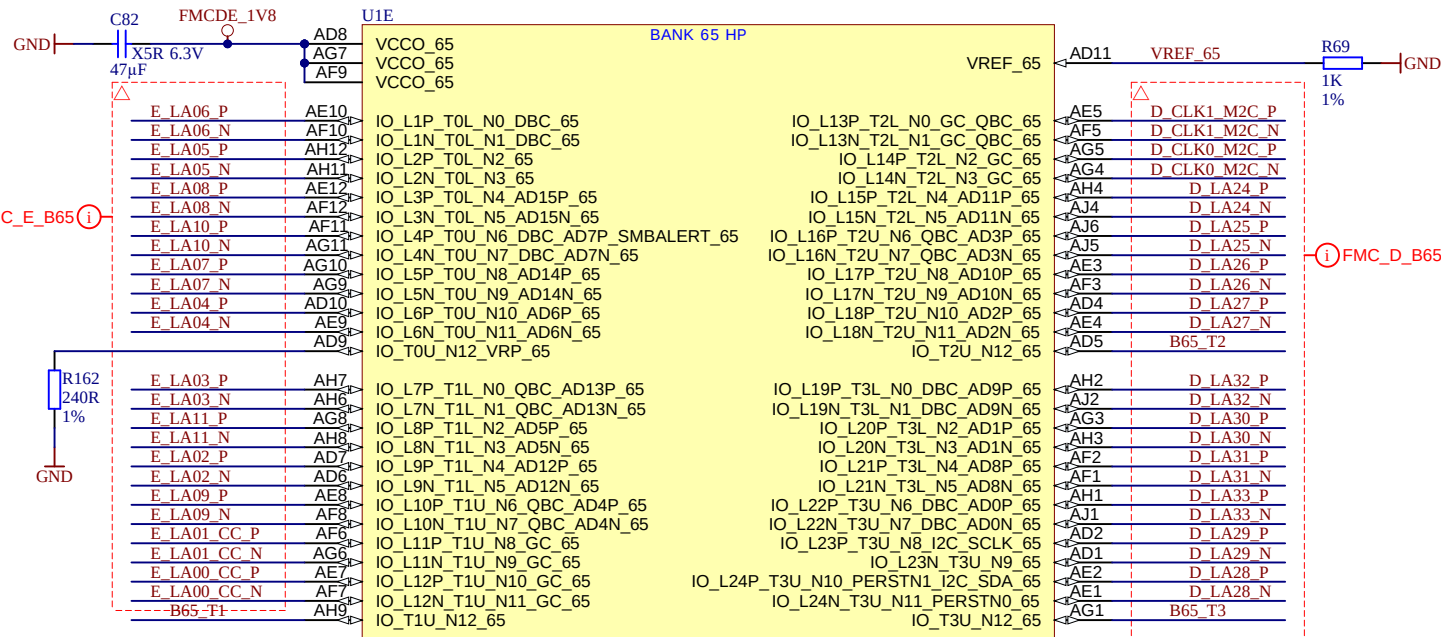
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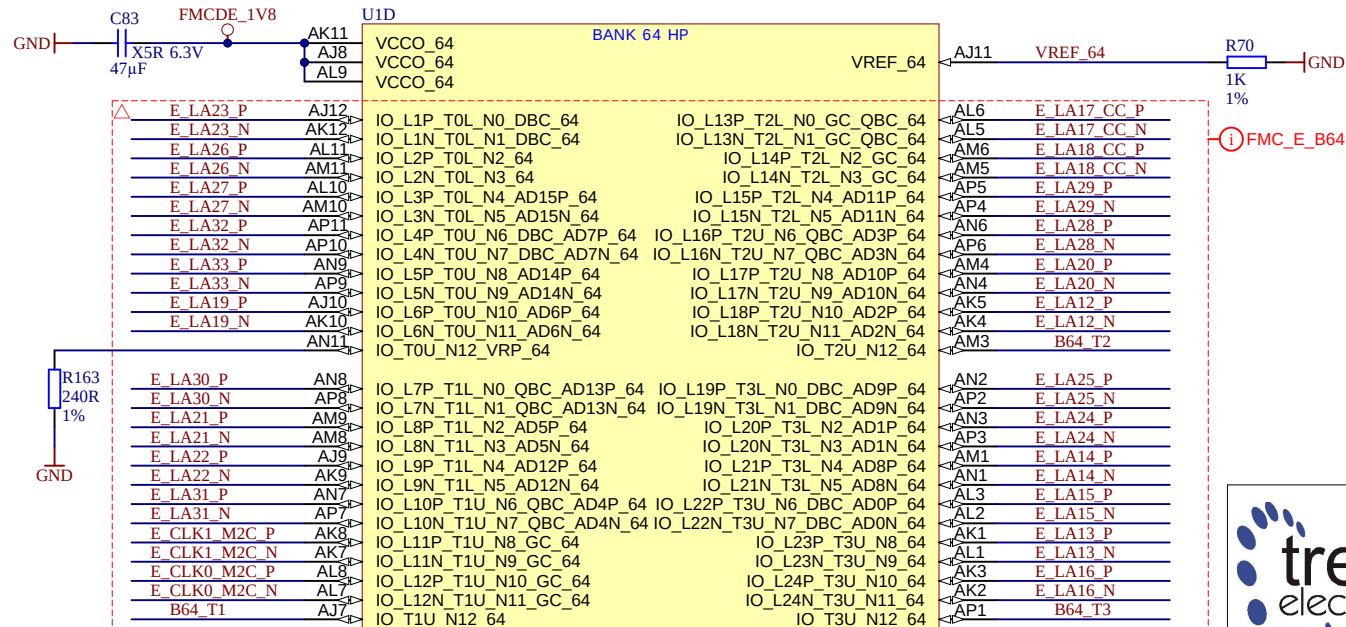
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XCZU9EG-1FFVB156E



XCZU9EG-1FFVB156E



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Date: 18.09.2018	Copyright: Trenz Electronic GmbH		Page3 of 58
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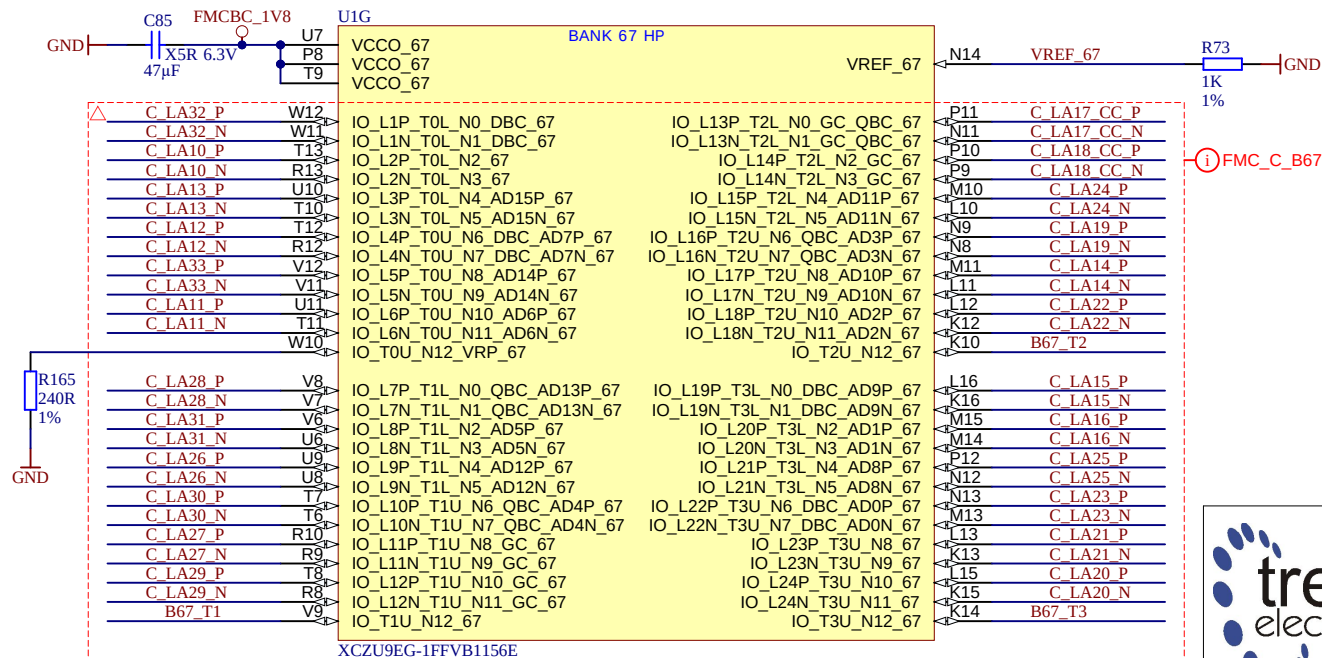
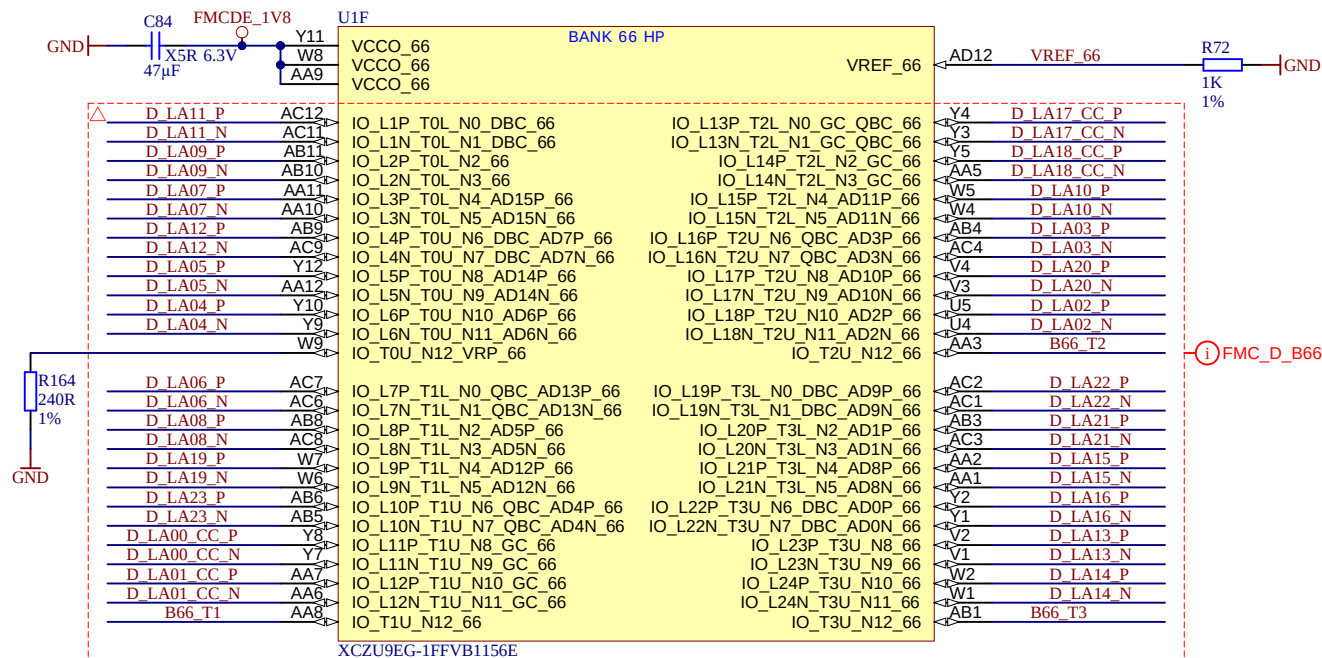
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Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page4 of 58
Filename: ZU_B66_B67.SchDoc		

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A

A

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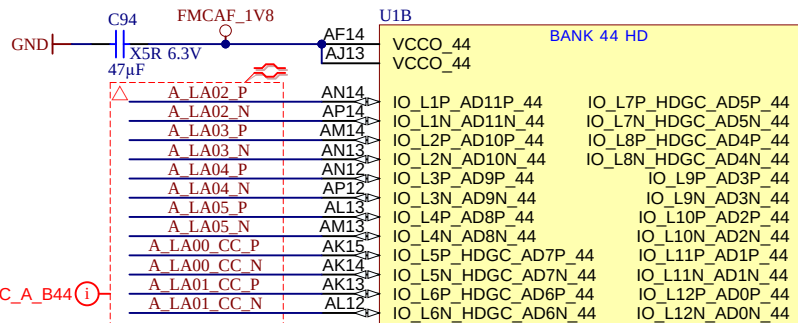
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C

D

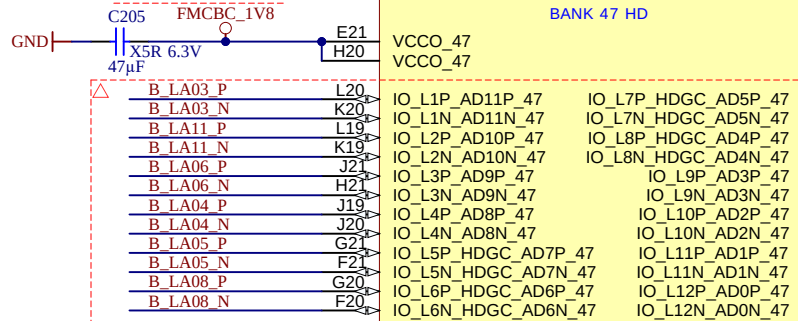
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FMC_A_B44

FMC_F_B44

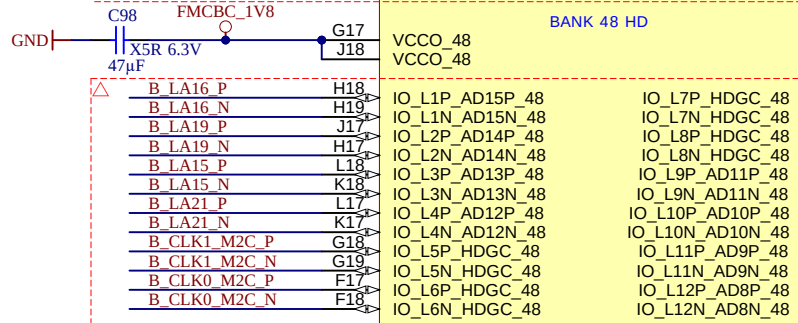
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FMC_B_B47

FMC_B_B48

FMC_C_B50



XCZU9EG-1FFVB1156E

XCZU9EG-1FFVB1156E



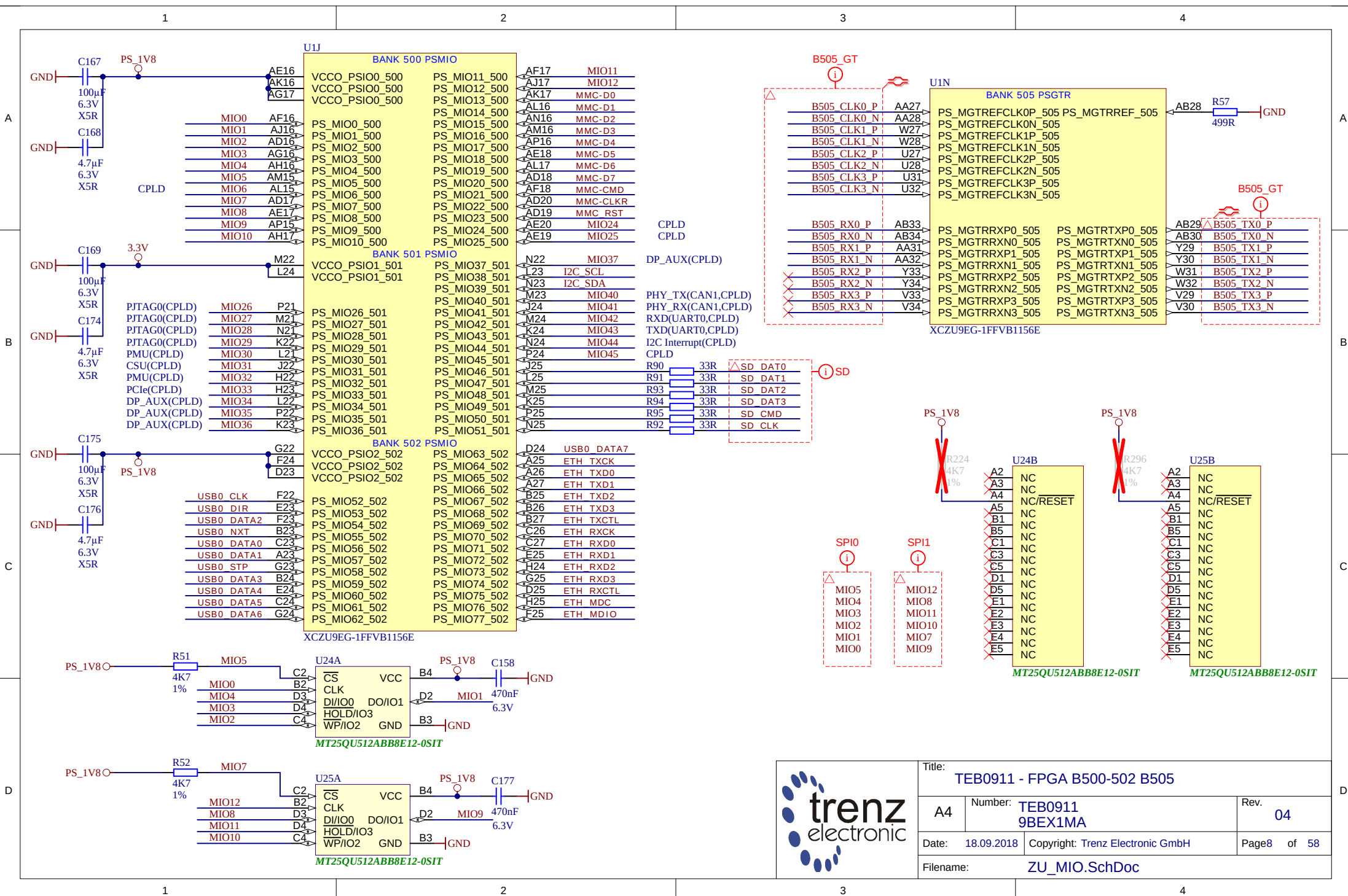
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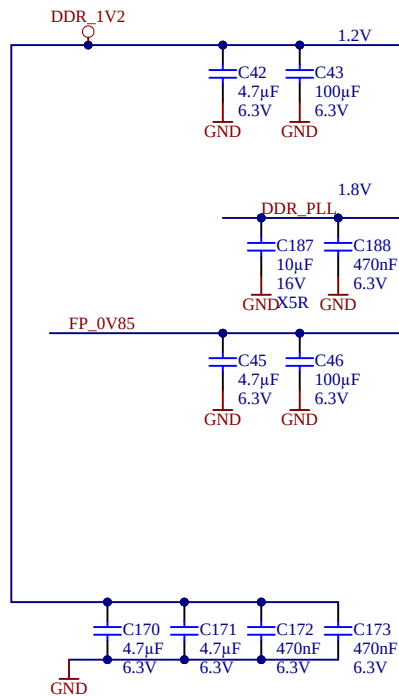


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ADDR_CMD_CTRL

DDR4-A[0..16]
DDR4-CS_N0
DDR4-CS_N1
DDR4-BA0
DDR4-BA1
DDR4-BG0
DDR4-BG1
DDR4-PAR
DDR4-ACT
DDR4-ODT0
DDR4-ODT1
DDR4-CKE0
DDR4-CKE1
DDR4-CLK0_P
DDR4-CLK0_N
DDR4-CLK1_P
DDR4-CLK1_N
DDR4-CLK1_N

LANE0

DO[0..7]
DDR4-DQS0_P
DDR4-DQS0_N
DDR4-DM0

LANE1

DO[8..15]
DDR4-DQS1_P
DDR4-DQS1_N
DDR4-DM1

LANE2

DO[16..23]
DDR4-DQS2_P
DDR4-DQS2_N
DDR4-DM2

LANE3

DO[24..31]
DDR4-DQS3_P
DDR4-DQS3_N
DDR4-DM3

LANE4

DO[32..39]
DDR4-DQS4_P
DDR4-DQS4_N
DDR4-DM4

LANE5

DO[40..47]
DDR4-DQS5_P
DDR4-DQS5_N
DDR4-DM5

LANE6

DO[48..55]
DDR4-DQS6_P
DDR4-DQS6_N
DDR4-DM6

LANE7

DO[56..63]
DDR4-DQS7_P
DDR4-DQS7_N
DDR4-DM7

LANE_ECC

CB[0..7]
DDR4-DQS8_P
DDR4-DQS8_N
DDR4-DM8

U1L

BANK 504 PSDDR

VCCO_PSDDR_504 PS_DDR_CK0_504
VCCO_PSDDR_504 PS_DDR_CK_N0_504
VCCO_PSDDR_504 PS_DDR_CKE0_504
VCCO_PSDDR_504 PS_DDR_CK1_504
VCCO_PSDDR_504 PS_DDR_CK_N1_504
VCCO_PSDDR_504 PS_DDR_CKE1_504

VCC_PSDDR_PLL PS_DDR_A0_504
VCC_PSDDR_PLL PS_DDR_A1_504
VCC_PSDDR_PLL PS_DDR_A2_504
VCC_PSDDR_PLL PS_DDR_A3_504
VCC_PSDDR_PLL PS_DDR_A4_504
VCC_PSDDR_PLL PS_DDR_A5_504
VCC_PSDDR_PLL PS_DDR_A6_504
VCC_PSDDR_PLL PS_DDR_A7_504
VCC_PSDDR_PLL PS_DDR_A8_504
VCC_PSDDR_PLL PS_DDR_A9_504
VCC_PSDDR_PLL PS_DDR_A10_504
VCC_PSDDR_PLL PS_DDR_A11_504
VCC_PSDDR_PLL PS_DDR_A12_504
VCC_PSDDR_PLL PS_DDR_A13_504
VCC_PSDDR_PLL PS_DDR_A14_504
VCC_PSDDR_PLL PS_DDR_A15_504
VCC_PSDDR_PLL PS_DDR_A16_504
VCC_PSDDR_PLL PS_DDR_A17_504

PS_DDR_CS_N0_504
PS_DDR_CS_N1_504

PS_DDR_BA0_504
PS_DDR_BA1_504

PS_DDR_BG0_504
PS_DDR_BG1_504

PS_DDR_PARITY_504
PS_DDR_RAM_RST_N_504
PS_DDR_ACT_N_504
PS_DDR_ALERT_N_504

PS_DDR_ZQ_504

PS_DDR_ODT0_504
PS_DDR_ODT1_504

XCZU9EG-1FFVB1156E

AN26 DDR4-CLK0_P
AN27 DDR4-CLK0_N
AN29 DDR4-CKE0
AL26 DDR4-CLK1_P
AL27 DDR4-CLK1_N
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AP29 DDR4-A0
AP30 DDR4-A1
AP26 DDR4-A2
AP27 DDR4-A3
AP25 DDR4-A4
AN24 DDR4-A5
AM29 DDR4-A6
AM28 DDR4-A7
AM26 DDR4-A8
AM25 DDR4-A9
AL28 DDR4-A10
AK27 DDR4-A11
AJ25 DDR4-A12
AL25 DDR4-A13
AK25 DDR4-A14
AK24 DDR4-A15
AM24 DDR4-A16
AF25 DDR4-A17
AN28 DDR4-CS_N0
AL30 DDR4-CS_N1
AH26 DDR4-BA0
AG26 DDR4-BA1
AK28 DDR4-BG0
AH27 DDR4-BG1
AF20 DDR4-PAR
AF21 DDR4-RESET
AG25 DDR4-ACT
AF22 DDR4-ALERT
AF23
AM30 DDR4-ODT0
AJ26 DDR4-ODT1

X

R2

240R

GND

U1M

BANK 504 PSDDR

PS_DDR_DQ0_504 PS_DDR_DQ32_504
PS_DDR_DQ1_504 PS_DDR_DQ33_504
PS_DDR_DQ2_504 PS_DDR_DQ34_504
PS_DDR_DQ3_504 PS_DDR_DQ35_504
PS_DDR_DQ4_504 PS_DDR_DQ36_504
PS_DDR_DQ5_504 PS_DDR_DQ37_504
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XCZU9EG-1FFVB1156E

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AK30 DQ38
AJ29 DQ39
AE27 DQ40
AF28 DQ41
AF30 DQ42
AF31 DQ43
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AL33 CB7
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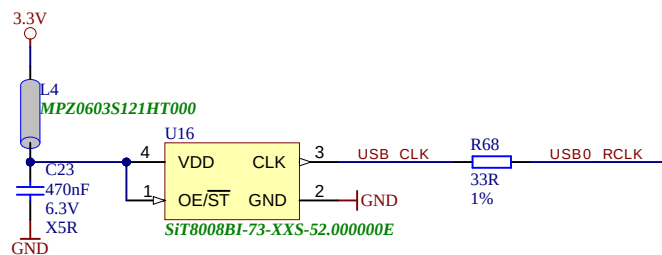
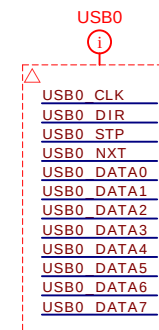
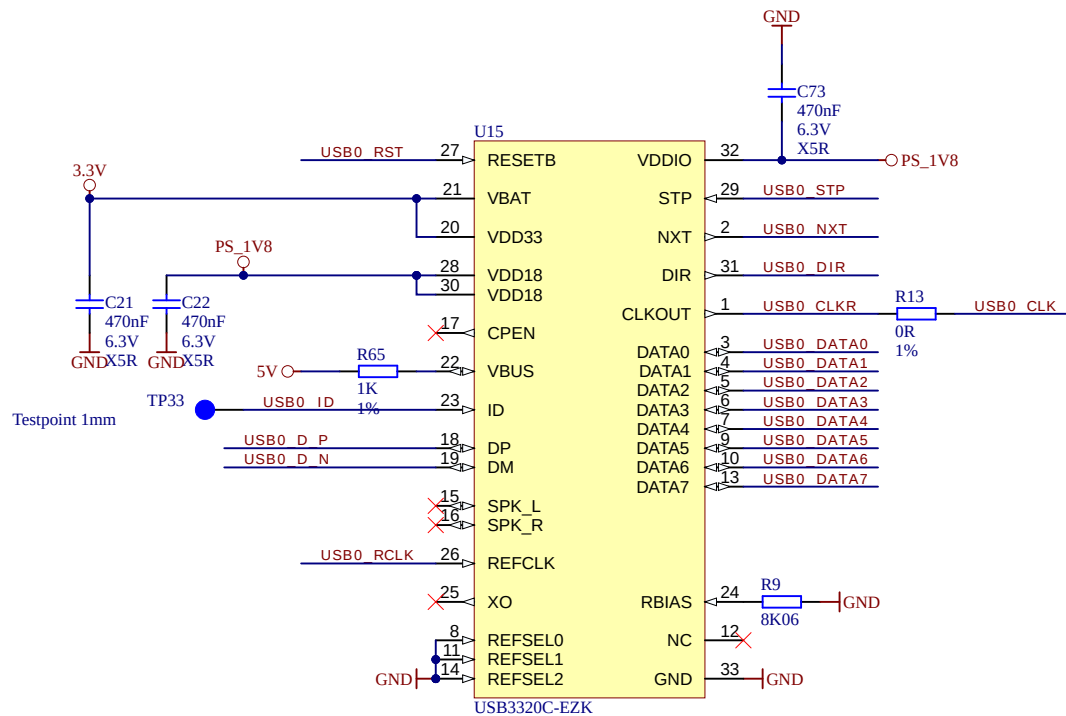
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Date: 18.09.2018	Copyright: Trenz Electronic GmbH	
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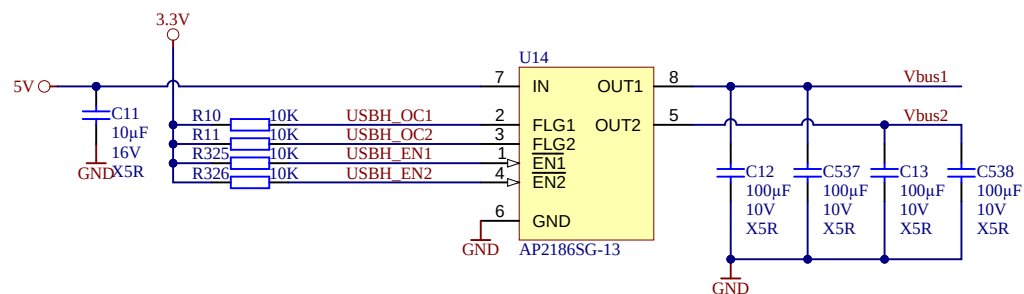
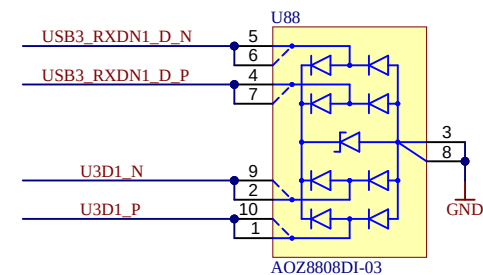
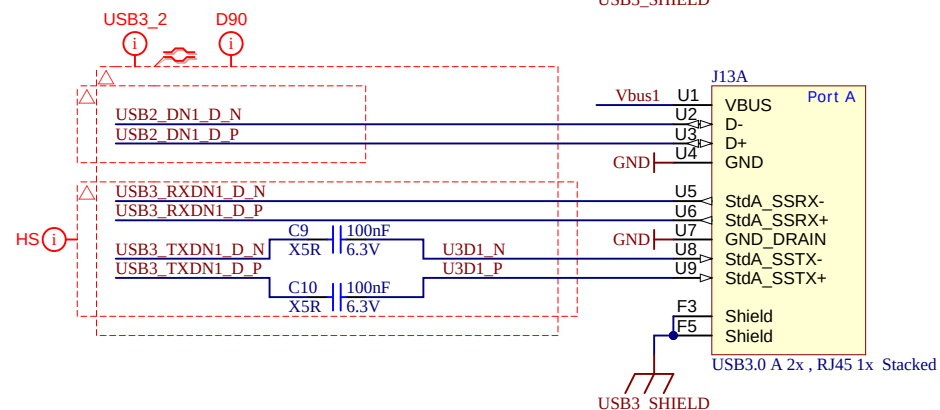
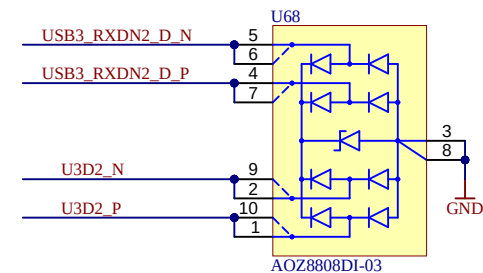
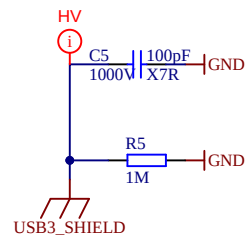
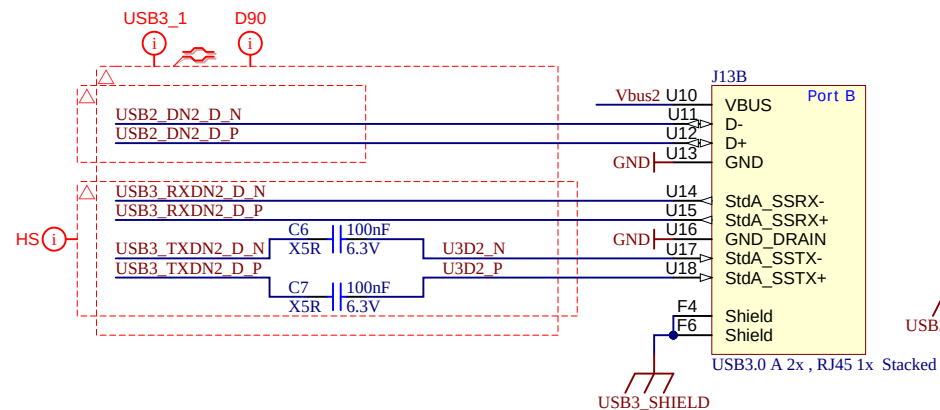
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	A4	Number: TEB0911 9BEX1MA	Rev. 04
	Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page11 of 58
	Filename: USB-PHY.SchDoc		

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Title: TEB0911 - USB CONNECTORS

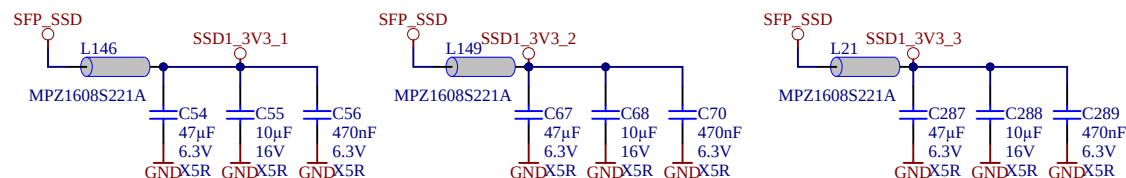
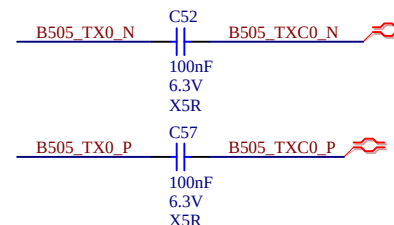
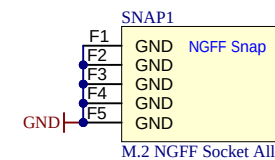
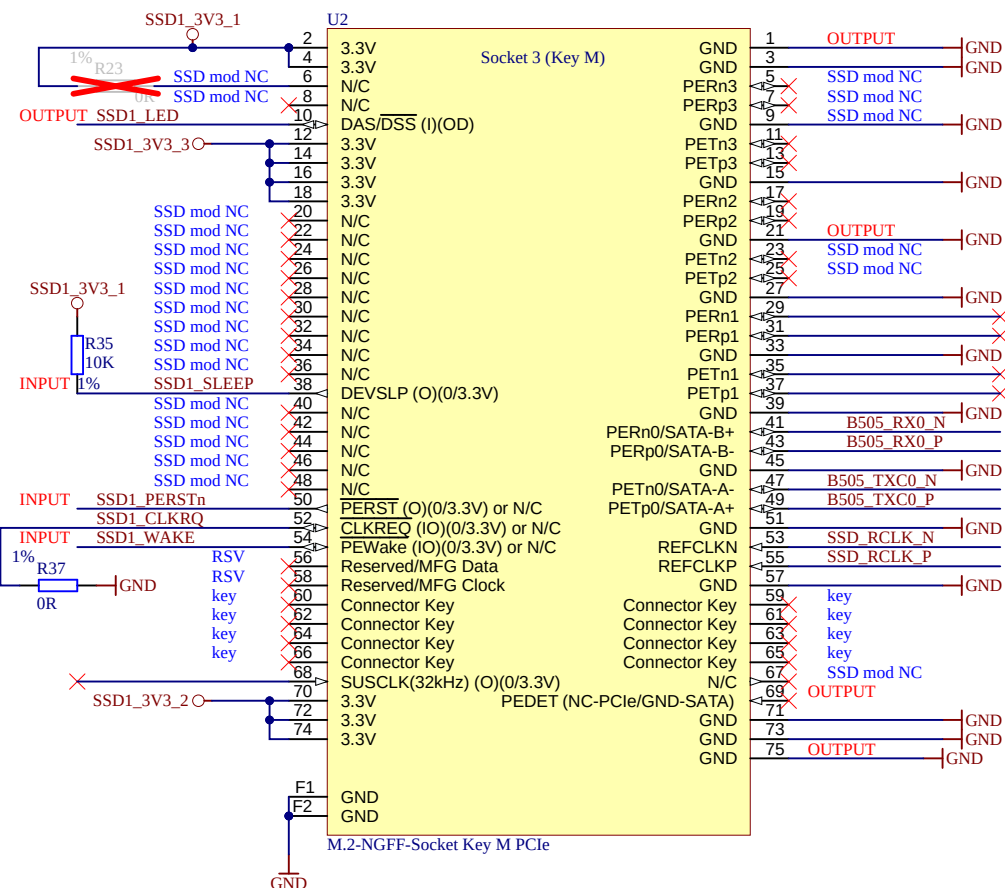
A4	Number: TEB0911 9BEX1MA	Rev. 04
Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page14 of 58
Filename: USB.SchDoc		

1

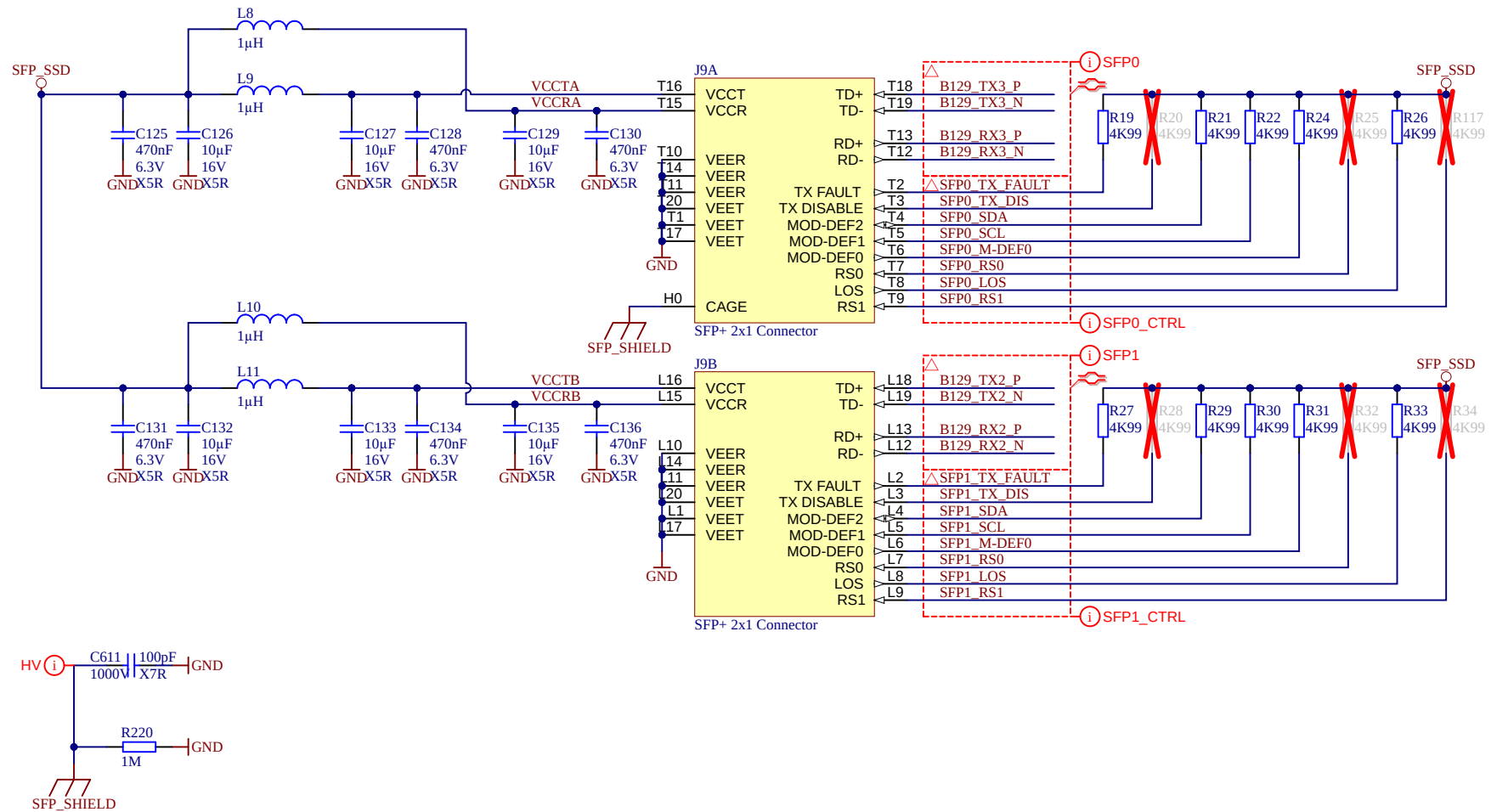
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3

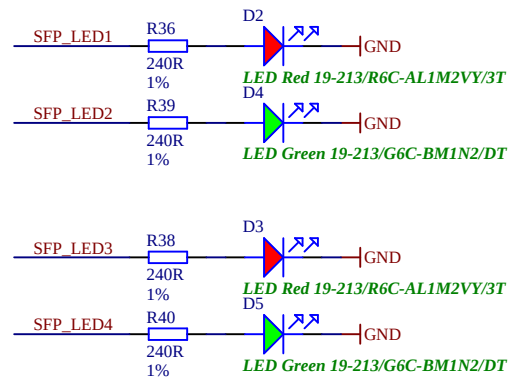
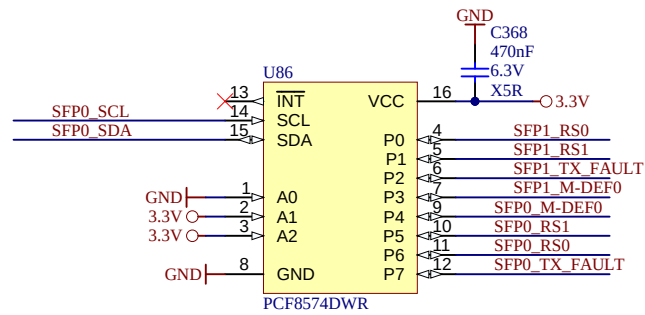
4



Title: TEB0911 - SSD			
A4	Number: TEB0911 9BEX1MA	Rev. 04	
Date: 18.09.2018	Copyright: Trenz Electronic GmbH		Page 16 of 58
Filename: SSD.SchDoc			



		Title: TEB0911 - SFP	
		A4	Rev. 04
Date: 18.09.2018	Copyright: Trenz Electronic GmbH		Page 17 of 58
Filename: SFP.SchDoc			



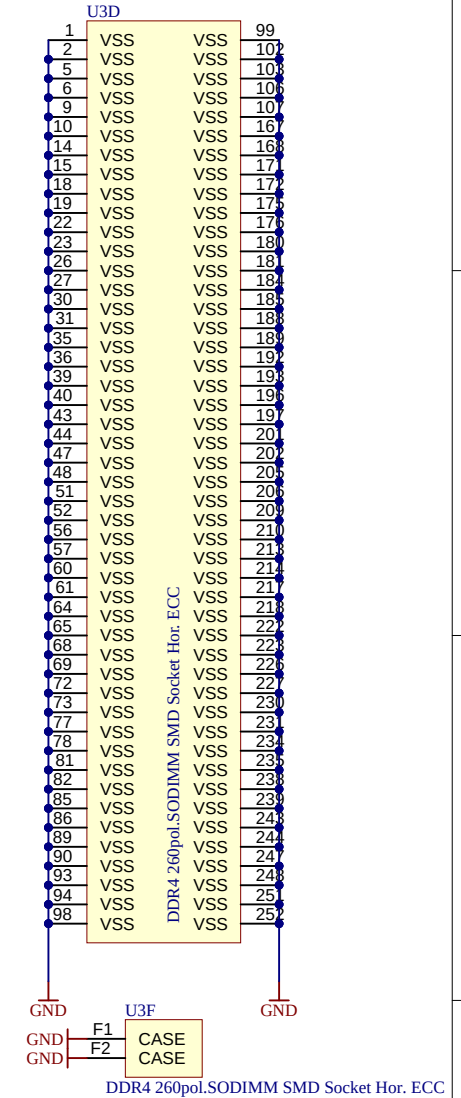
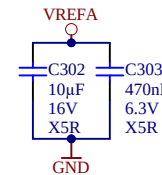
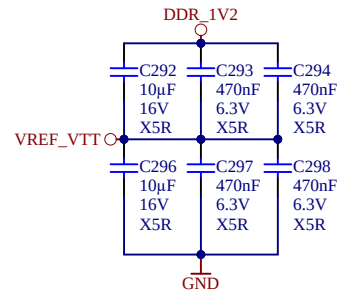
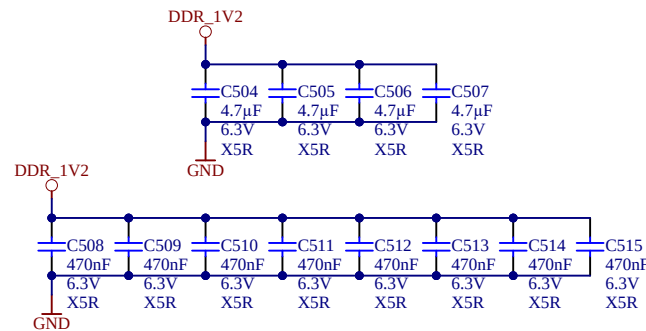
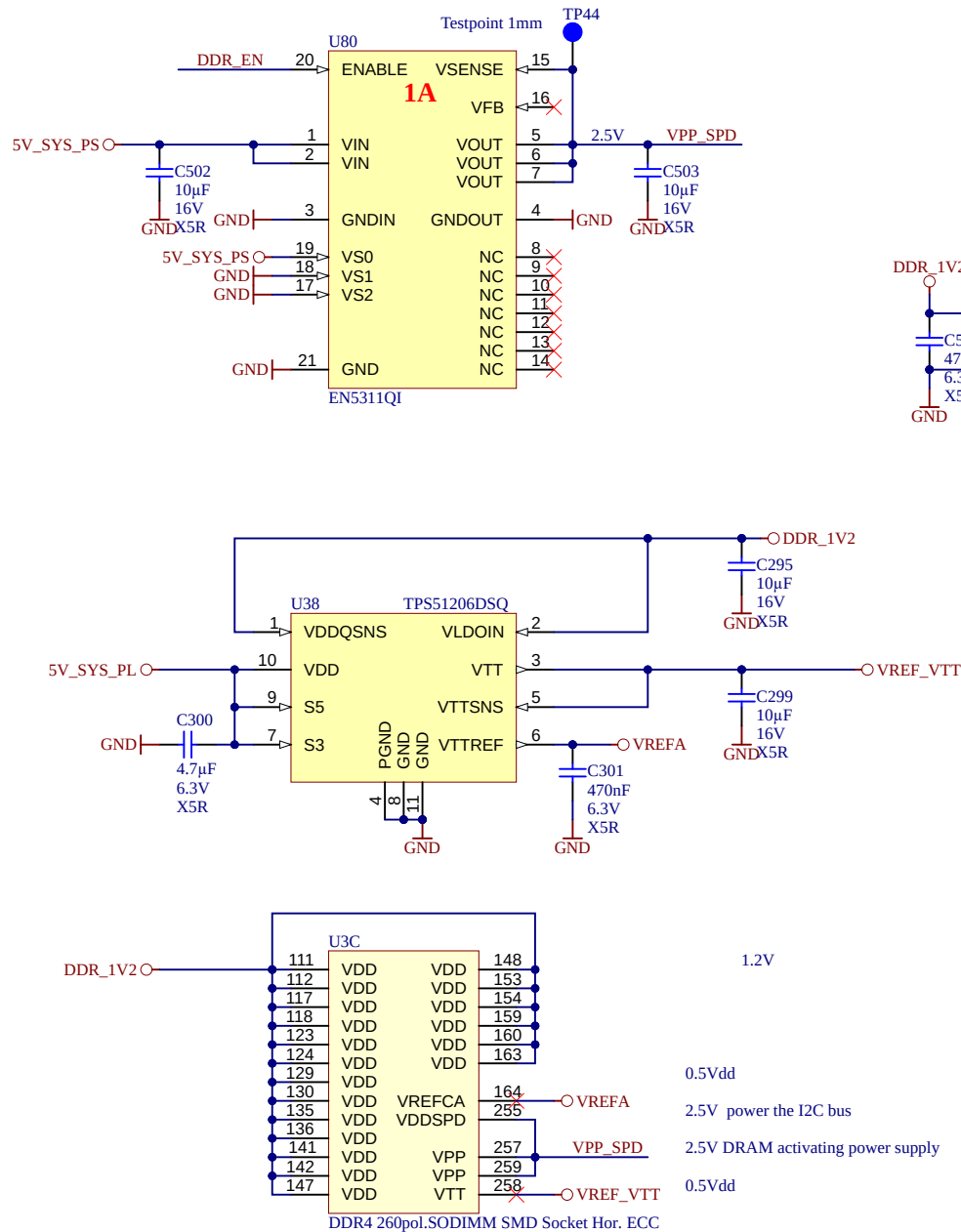
	Title: TEB0911 - SFP		
	A4	Number: TEB0911 9BEX1MA	Rev. 04
	Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page 18 of 58
	Filename: SFP_CTRL.SchDoc		

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4



Title: TEB0911 - DDR POWER		
A4	Number: TEB0911 9BEX1MA	Rev. 04
Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page19 of 58
Filename: DDR_PWR.SchDoc		

1

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4

A

A

B

B

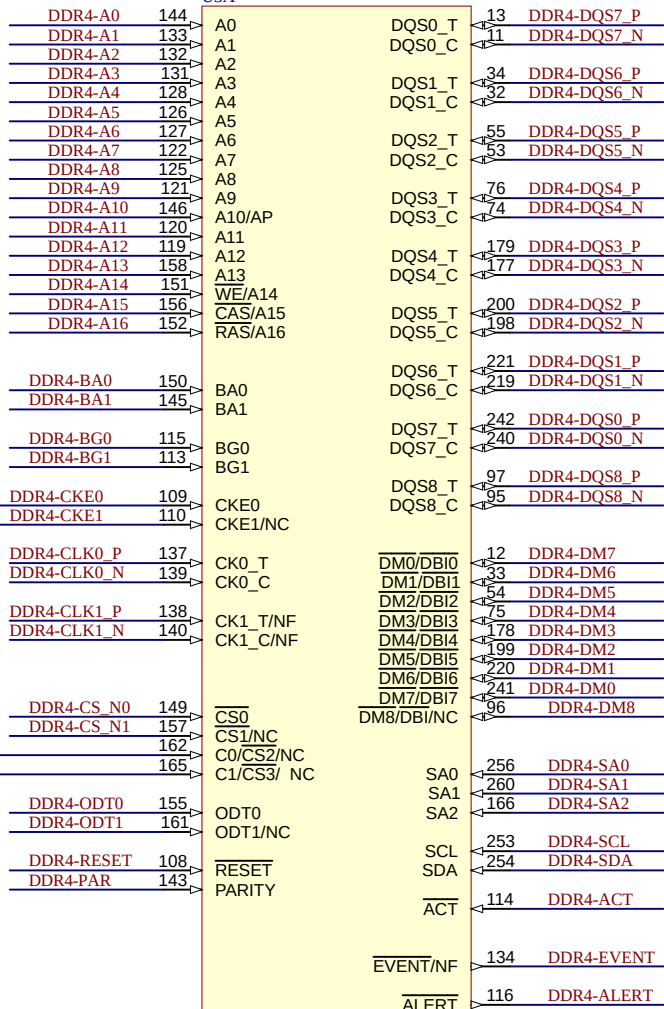
C

C

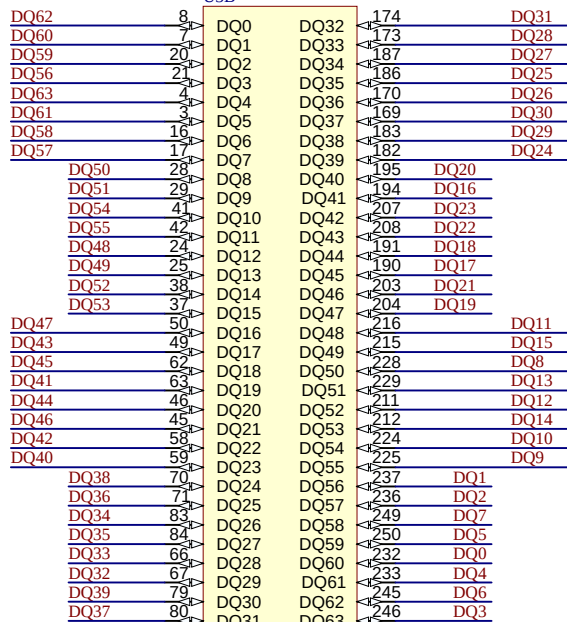
D

D

U3A

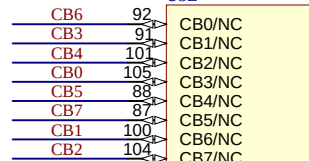


U3B



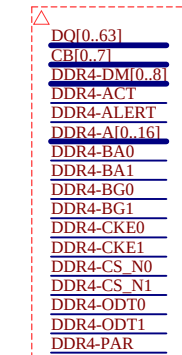
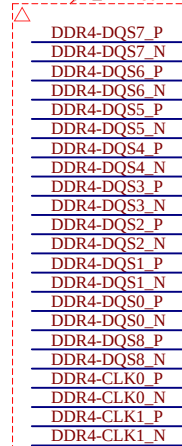
DDR4 260pol.SODIMM SMD Socket Hor. ECC

U3E



DDR4 260pol.SODIMM SMD Socket Hor. ECC

D80



Title: TEB0911 - DDR4 SODIMM		
A4	Number: TEB0911 9BEX1MA	Rev. 04
Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page20 of 58
Filename: DDR_SODIMM.SchDoc		

1

2

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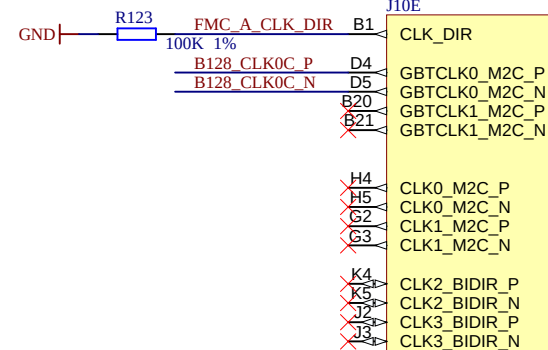
FMC_A_DP

FMC_A_DP

J10A

J10C

J10E



ASP-134486-01, CC-HPC-10

FMC_A_LA_F

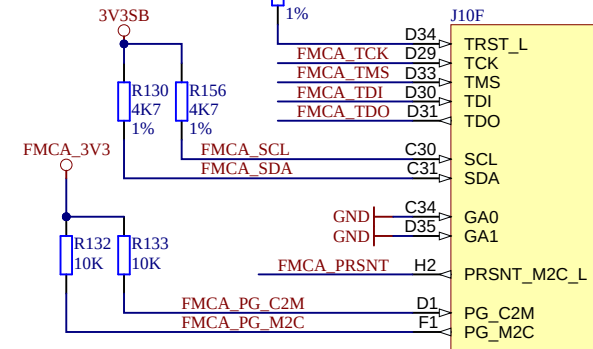
FMC_A_LA_C

ASP-134486-01, CC-HPC-10

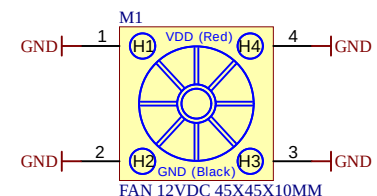
J10B

ASP-134486-01, CC-HPC-10

J10D



ASP-134486-01, CC-HPC-10



U10

SNAP

Single FMC Commercial Baseboard Snap



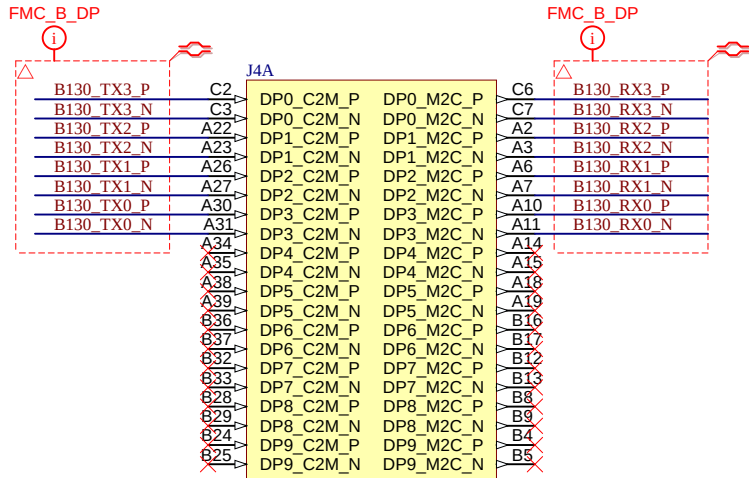
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A4	Number: TEB0911 9BEX1MA	Rev. 04
Date: 18.09.2018	Copyright: Trenz Electronic GmbH	
Filename: FMC_A.SchDoc		Page21 of 58

1

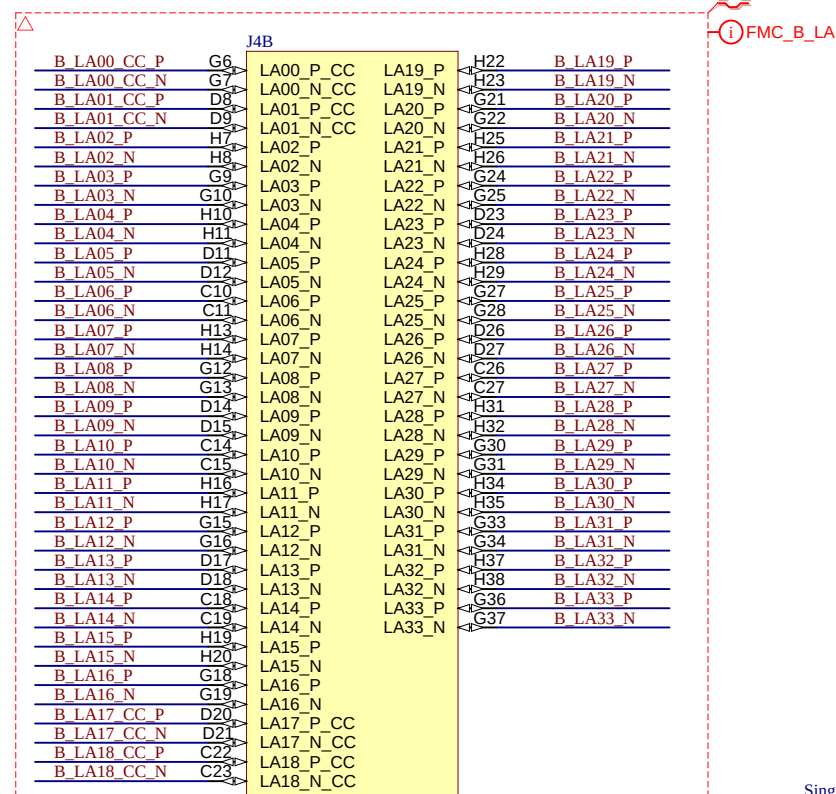
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ASP-134486-01, CC-HPC-10

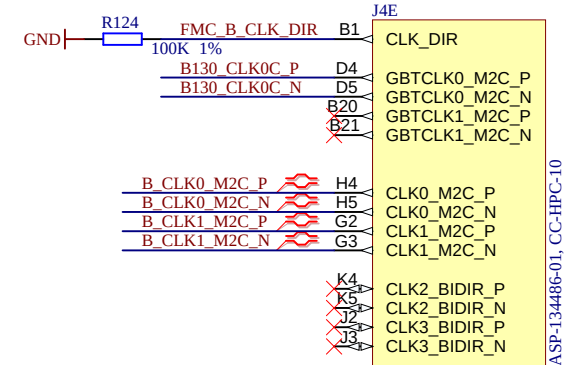


ASP-134486-01, CC-HPC-10

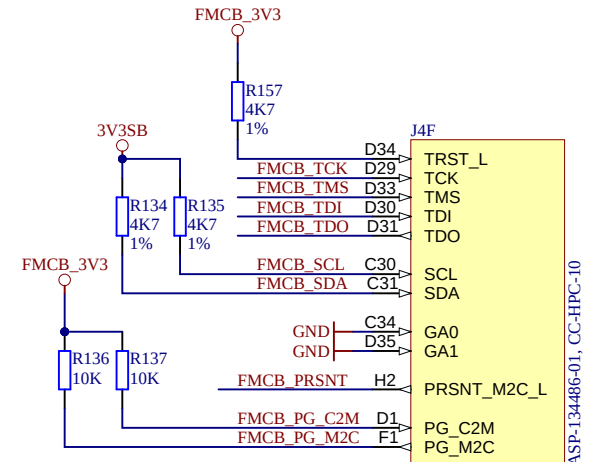
U9

SNAP

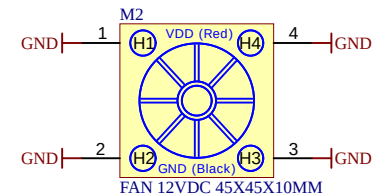
Single FMC Commercial Baseboard Snap



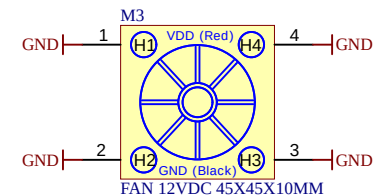
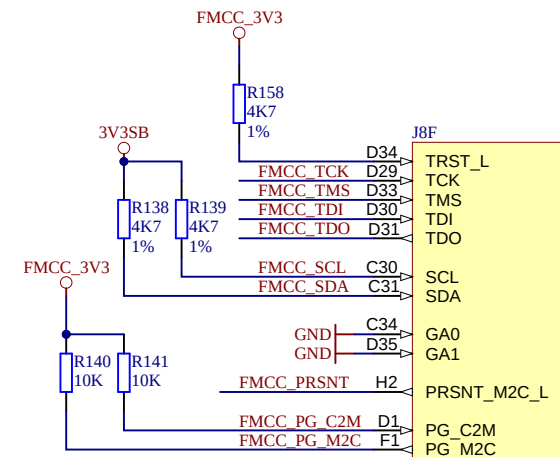
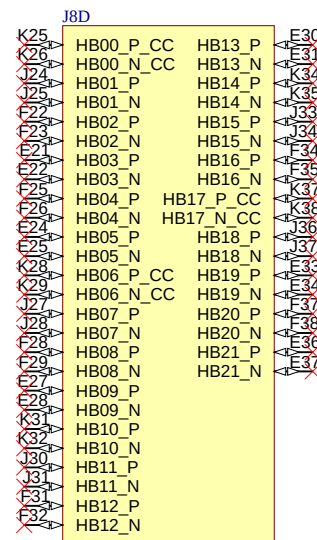
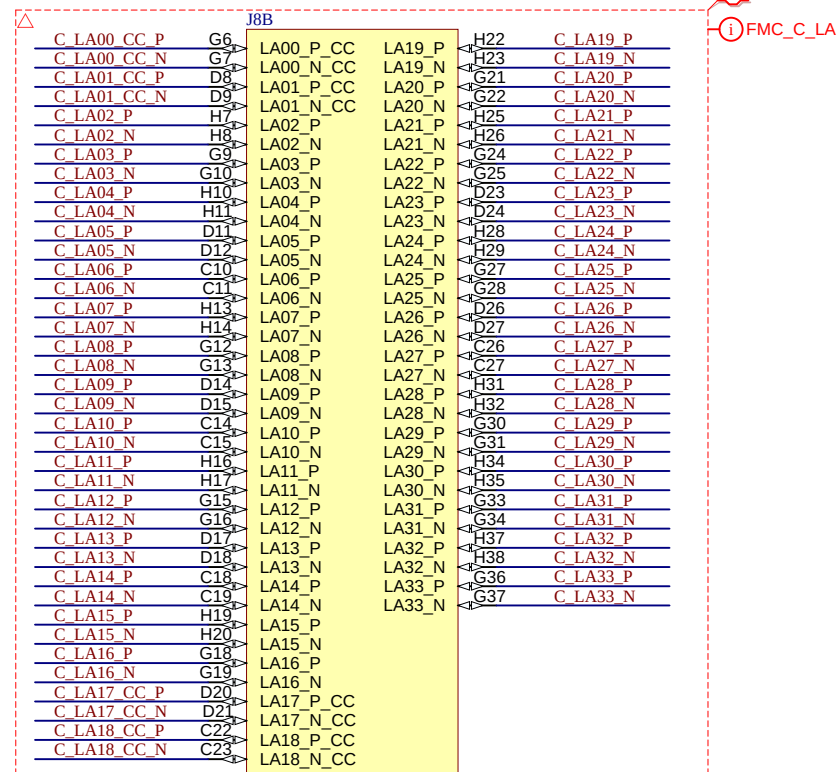
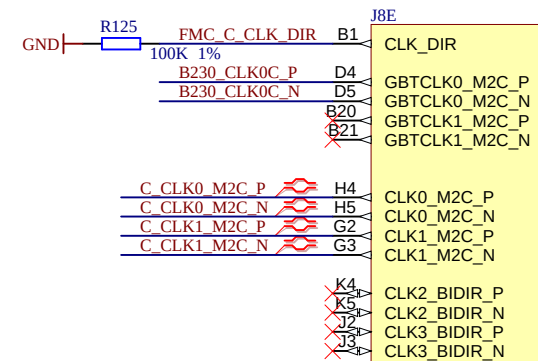
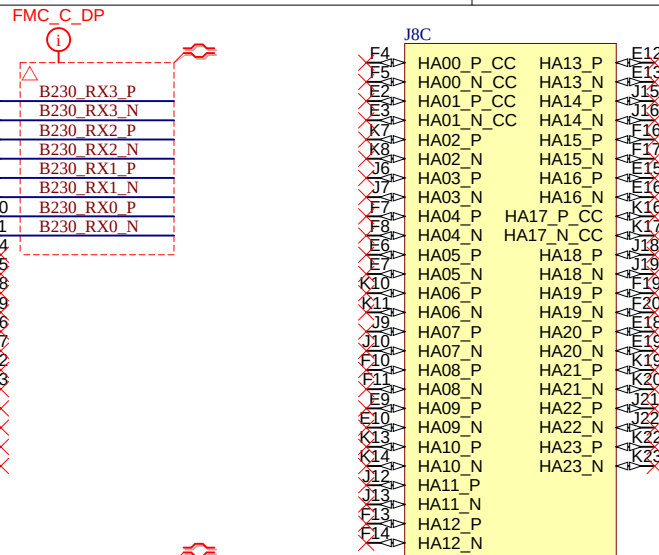
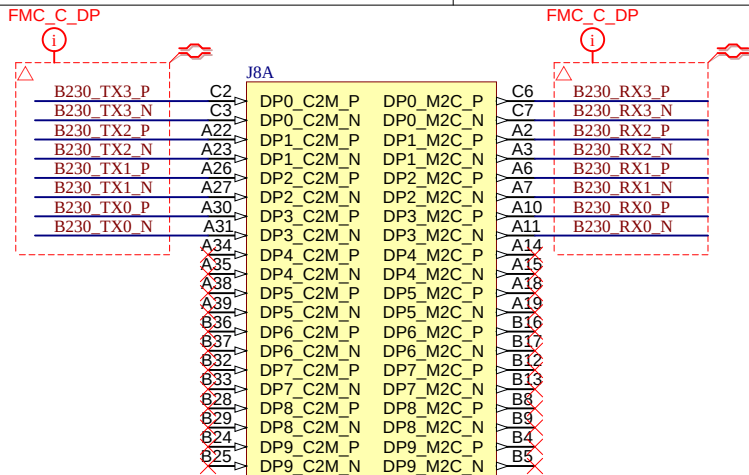
ASP-134486-01, CC-HPC-10



ASP-134486-01, CC-HPC-10



Title: TEB0911 - FMC B		
A4	Number: TEB0911 9BEX1MA	Rev. 04
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Filename: FMC_B.SchDoc		Page22 of 58



U8
SNAP

Single FMC Commercial Baseboard Snap



Title: TEB0911 - FMC C		
A4	Number: TEB0911 9BEX1MA	Rev. 04
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Filename: FMC_C.SchDoc		Page23 of 58

FMC_D_DP

FMC_D_DP

J7A

B229_TX3_P	C2	DP0_C2M_P	DP0_M2C_P	C6	B229_RX3_P
B229_TX3_N	C3	DP0_C2M_N	DP0_M2C_N	C7	B229_RX3_N
B229_TX2_P	A22	DP1_C2M_P	DP1_M2C_P	A2	B229_RX2_P
B229_TX2_N	A23	DP1_C2M_N	DP1_M2C_N	A3	B229_RX2_N
B229_TX1_P	A26	DP2_C2M_P	DP2_M2C_P	A6	B229_RX1_P
B229_TX1_N	A27	DP2_C2M_N	DP2_M2C_N	A7	B229_RX1_N
B229_TX0_P	A30	DP3_C2M_P	DP3_M2C_P	A10	B229_RX0_P
B229_TX0_N	A31	DP3_C2M_N	DP3_M2C_N	A11	B229_RX0_N
	A34	DP4_C2M_P	DP4_M2C_P	A14	
	A35	DP4_C2M_N	DP4_M2C_N	A15	
	A38	DP5_C2M_P	DP5_M2C_P	A18	
	A39	DP5_C2M_N	DP5_M2C_N	A19	
	B36	DP6_C2M_P	DP6_M2C_P	B16	
	B37	DP6_C2M_N	DP6_M2C_N	B17	
	B32	DP7_C2M_P	DP7_M2C_P	B12	
	B33	DP7_C2M_N	DP7_M2C_N	B13	
	B28	DP8_C2M_P	DP8_M2C_P	B8	
	B29	DP8_C2M_N	DP8_M2C_N	B9	
	B24	DP9_C2M_P	DP9_M2C_P	B4	
	B25	DP9_C2M_N	DP9_M2C_N	B5	

ASP-134486-01, CC-HPC-10

J7C

HA00_P_CC	HA13_P	E12
HA00_N_CC	HA13_N	E13
HA01_P_CC	HA14_P	E15
HA01_N_CC	HA14_N	E16
HA02_P	HA15_P	E17
HA02_N	HA15_N	E18
HA03_P	HA16_P	E19
HA03_N	HA16_N	E20
HA04_P	HA17_P_CC	E17
HA04_N	HA17_N_CC	E18
HA05_P	HA18_P	E19
HA05_N	HA18_N	E20
HA06_P	HA19_P	E21
HA06_N	HA19_N	E22
HA07_P	HA20_P	E23
HA07_N	HA20_N	E24
HA08_P	HA21_P	E25
HA08_N	HA21_N	E26
HA09_P	HA22_P	E27
HA09_N	HA22_N	E28
HA10_P	HA23_P	E29
HA10_N	HA23_N	E30
HA11_P		
HA11_N		
HA12_P		
HA12_N		

ASP-134486-01, CC-HPC-10

J7D

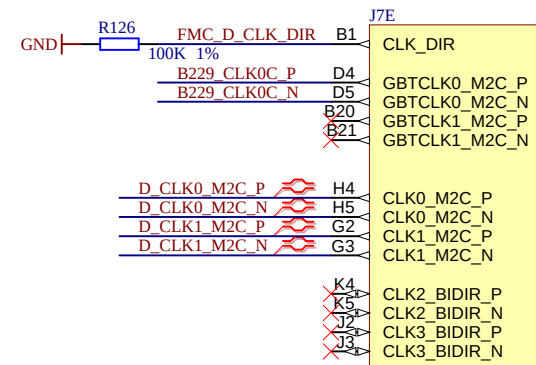
HB00_P_CC	HB13_P	E30
HB00_N_CC	HB13_N	E31
HB01_P	HB14_P	E32
HB01_N	HB14_N	E33
HB02_P	HB15_P	E34
HB02_N	HB15_N	E35
HB03_P	HB16_P	E36
HB03_N	HB16_N	E37
HB04_P	HB17_P_CC	E38
HB04_N	HB17_N_CC	E39
HB05_P	HB18_P	E40
HB05_N	HB18_N	E41
HB06_P_CC	HB19_P	E42
HB06_N_CC	HB19_N	E43
HB07_P	HB20_P	E44
HB07_N	HB20_N	E45
HB08_P	HB21_P	E46
HB08_N	HB21_N	E47
HB09_P		
HB09_N		
HB10_P		
HB10_N		
HB11_P		
HB11_N		
HB12_P		
HB12_N		

ASP-134486-01, CC-HPC-10

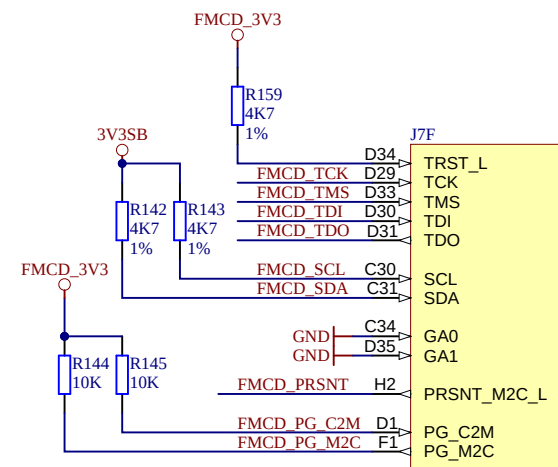
U6

SNAP

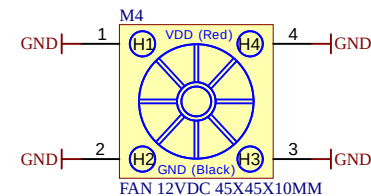
Single FMC Commercial Baseboard Snap



ASP-134486-01, CC-HPC-10



ASP-134486-01, CC-HPC-10



FAN 12VDC 45X45X10MM

Title:

TEB0911 - FMC D

A4

Number: TEB0911
9BEX1MA

Rev.

04

Date: 18.09.2018

Copyright: Trenz Electronic GmbH

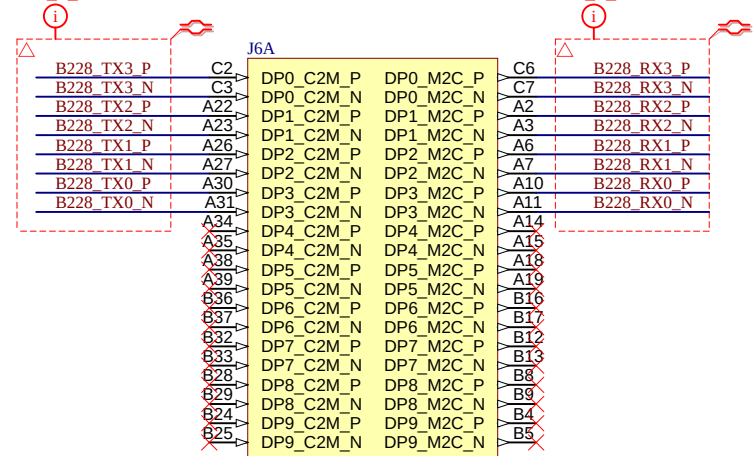
Page24 of 58

Filename:

FMC_D.SchDoc

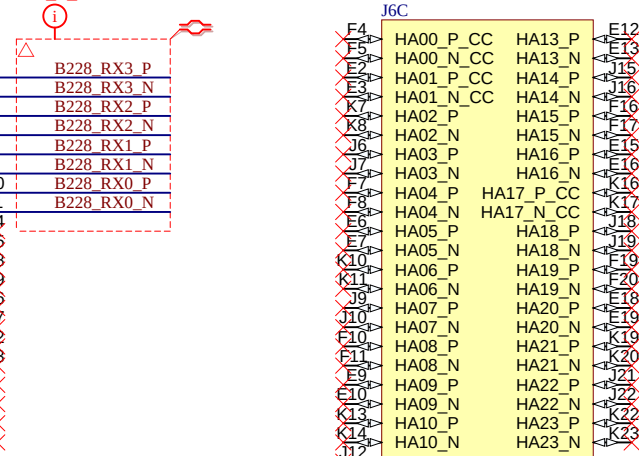


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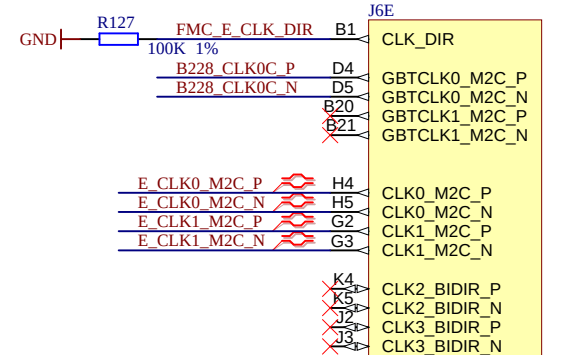


ASP-134486-01, CC-HPC-10

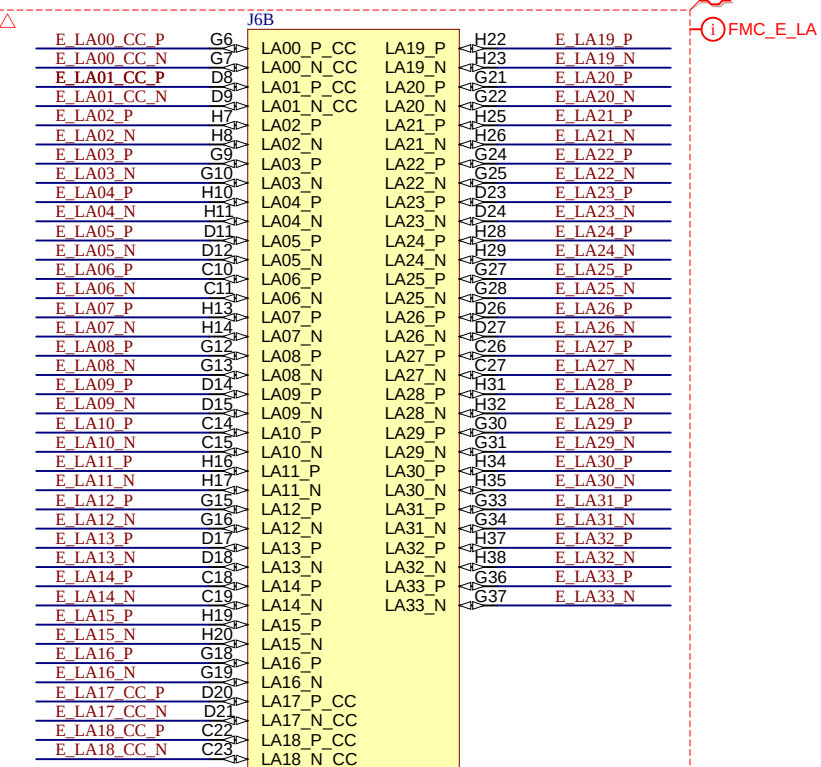
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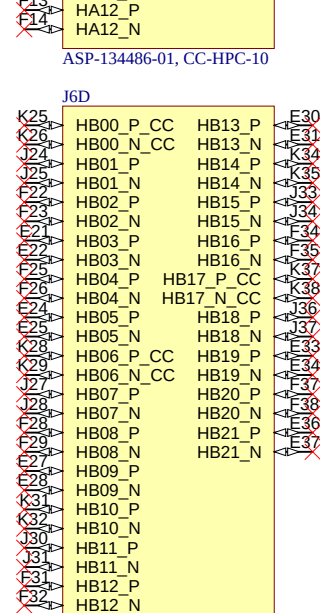
ASP-134486-01, CC-HPC-10



ASP-134486-01, CC-HPC-10



ASP-134486-01, CC-HPC-10



ASP-134486-01, CC-HPC-10

U7

SNAP

Single FMC Commercial Baseboard Snap



Title: TEB0911 - FMC E		Rev. 04
A4	Number: TEB0911 9BEX1MA	
Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page25 of 58
Filename: FMC_E.SchDoc		

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FMC_F_DP

FMC_F_DP

J21A

C2	DP0_C2M_P	DP0_M2C_P	C6
C3	DP0_C2M_N	DP0_M2C_N	C7
A22	DP1_C2M_P	DP1_M2C_P	A2
A23	DP1_C2M_N	DP1_M2C_N	A3
A26	DP2_C2M_P	DP2_M2C_P	A6
A27	DP2_C2M_N	DP2_M2C_N	A7
A30	DP3_C2M_P	DP3_M2C_P	A10
A31	DP3_C2M_N	DP3_M2C_N	A11
A34	DP4_C2M_P	DP4_M2C_P	A14
A35	DP4_C2M_N	DP4_M2C_N	A15
A38	DP5_C2M_P	DP5_M2C_P	A18
A39	DP5_C2M_N	DP5_M2C_N	A19
B36	DP6_C2M_P	DP6_M2C_P	B16
B37	DP6_C2M_N	DP6_M2C_N	B17
B32	DP7_C2M_P	DP7_M2C_P	B12
B33	DP7_C2M_N	DP7_M2C_N	B13
B28	DP8_C2M_P	DP8_M2C_P	B8
B29	DP8_C2M_N	DP8_M2C_N	B9
B24	DP9_C2M_P	DP9_M2C_P	B4
B25	DP9_C2M_N	DP9_M2C_N	B5

ASP-134486-01, CC-HPC-10

FMC_F_LA_C

J21B

G6	LA00_P_CC	LA19_P	H22	F LA19_SC_P
G7	LA00_N_CC	LA19_N	H23	F LA19_SC_N
D8	LA01_P_CC	LA20_P	G21	F LA20_SC_P
D9	LA01_N_CC	LA20_N	G22	F LA20_SC_N
H7	LA02_P	LA21_P	H25	F LA21_SC_P
H8	LA02_N	LA21_N	H26	F LA21_SC_N
G9	LA03_P	LA22_P	G24	F LA22_SC_P
G10	LA03_N	LA22_N	G25	F LA22_SC_N
H10	LA04_P	LA23_P	D23	F LA23_SC_P
H11	LA04_N	LA23_N	D24	F LA23_SC_N
D11	LA05_P	LA24_P	H28	F LA24_SC_P
D12	LA05_N	LA24_N	H29	F LA24_SC_N
C10	LA06_P	LA25_P	G27	F LA25_SC_P
C11	LA06_N	LA25_N	G28	F LA25_SC_N
H13	LA07_P	LA26_P	D26	F LA26_SC_P
H14	LA07_N	LA26_N	D27	F LA26_SC_N
G12	LA08_P	LA27_P	C26	F LA27_SC_P
G13	LA08_N	LA27_N	C27	F LA27_SC_N
D14	LA09_P	LA28_P	H31	F LA28_SC_P
D15	LA09_N	LA28_N	H32	F LA28_SC_N
C14	LA10_P	LA29_P	G30	F LA29_SC_P
C15	LA10_N	LA29_N	G31	F LA29_SC_N
H16	LA11_P	LA30_P	H34	F LA30_SC_P
H17	LA11_N	LA30_N	H35	F LA30_SC_N
G15	LA12_P	LA31_P	G33	F LA31_SC_P
G16	LA12_N	LA31_N	G34	F LA31_SC_N
D17	LA13_P	LA32_P	H37	F LA32_SC_P
D18	LA13_N	LA32_N	H38	F LA32_SC_N
C18	LA14_P	LA33_P	G36	F LA33_SC_P
C19	LA14_N	LA33_N	G37	F LA33_SC_N
H19	LA15_P			
H20	LA15_N			
G18	LA16_P			
G19	LA16_N			
D20	LA17_P_CC			
D21	LA17_N_CC			
C22	LA18_P_CC			
C23	LA18_N_CC			

ASP-134486-01, CC-HPC-10

J21C

F4	HA00_P_CC	HA13_P	E12
F5	HA00_N_CC	HA13_N	E13
E2	HA01_P_CC	HA14_P	F15
E3	HA01_N_CC	HA14_N	F16
K8	HA02_P	HA15_P	F17
J6	HA02_N	HA15_N	F18
J7	HA03_P	HA16_P	F19
F7	HA03_N	HA16_N	F20
F8	HA04_P	HA17_P_CC	K16
E6	HA04_N	HA17_N_CC	K17
E7	HA05_P	HA18_P	J18
E8	HA05_N	HA18_N	J19
K10	HA06_P	HA19_P	F21
K11	HA06_N	HA19_N	F22
J9	HA07_P	HA20_P	F23
J10	HA07_N	HA20_N	F24
F10	HA08_P	HA21_P	K19
F11	HA08_N	HA21_N	K20
E9	HA09_P	HA22_P	J21
E10	HA09_N	HA22_N	J22
K12	HA10_P	HA23_P	K22
K13	HA10_N	HA23_N	K23
J12	HA11_P		
J13	HA11_N		
F13	HA12_P		
F14	HA12_N		

ASP-134486-01, CC-HPC-10

J21D

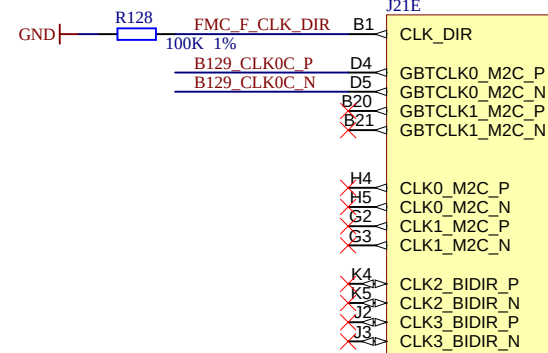
K25	HB00_P_CC	HB13_P	F30
K26	HB00_N_CC	HB13_N	F31
J24	HB01_P	HB14_P	K34
J25	HB01_N	HB14_N	K35
F23	HB02_P	HB15_P	F33
F24	HB02_N	HB15_N	F34
E21	HB03_P	HB16_P	F35
E22	HB03_N	HB16_N	F36
F25	HB04_P	HB17_P_CC	K37
F26	HB04_N	HB17_N_CC	K38
E23	HB05_P	HB18_P	F37
E24	HB05_N	HB18_N	F38
K28	HB06_P_CC	HB19_P	F39
K29	HB06_N_CC	HB19_N	F40
J27	HB07_P	HB20_P	F37
J28	HB07_N	HB20_N	F38
F28	HB08_P	HB21_P	F36
F29	HB08_N	HB21_N	F37
E27	HB09_P		
E28	HB09_N		
K31	HB10_P		
K32	HB10_N		
J30	HB11_P		
J31	HB11_N		
F32	HB12_P		
F33	HB12_N		

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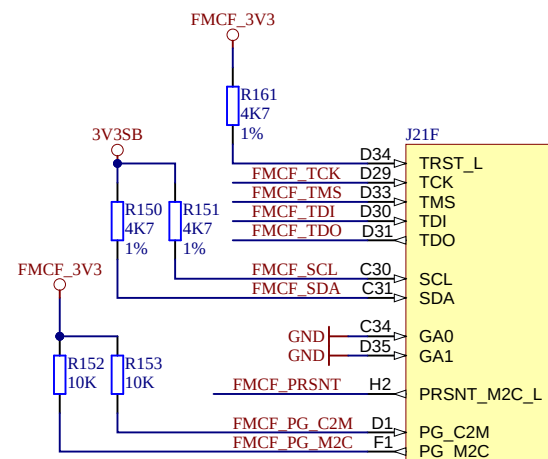
U19

SNAP

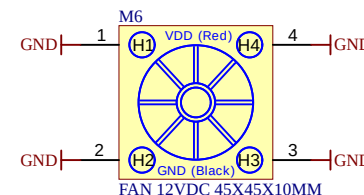
Single FMC Commercial Baseboard Snap



ASP-134486-01, CC-HPC-10



ASP-134486-01, CC-HPC-10



FAN 12VDC 45X45X10MM

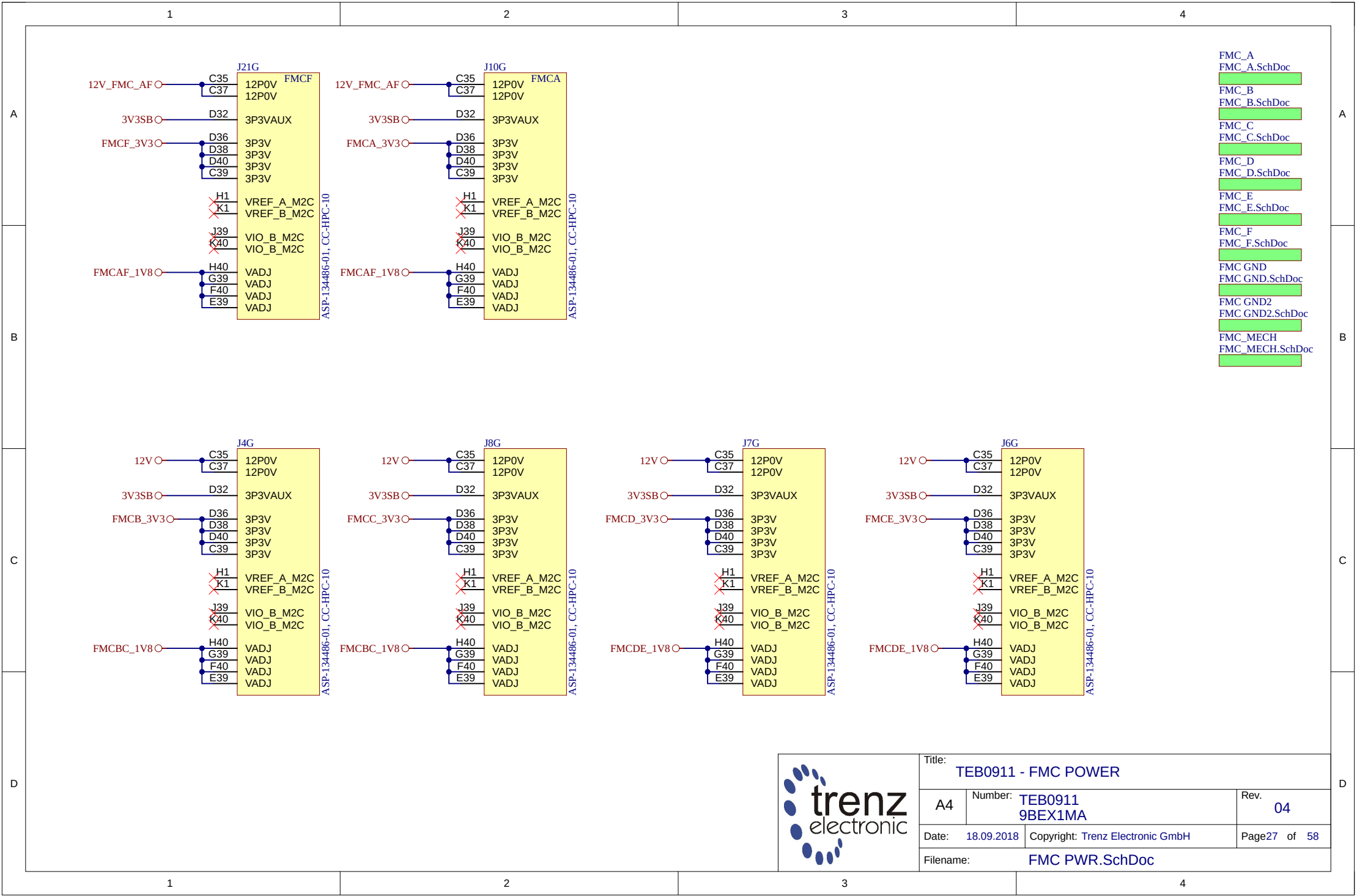
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A4	Number: TEB0911 9BEX1MA	Rev. 04	
Date: 18.09.2018	Copyright: Trenz Electronic GmbH		Page 26 of 58
Filename: FMC_F.SchDoc			

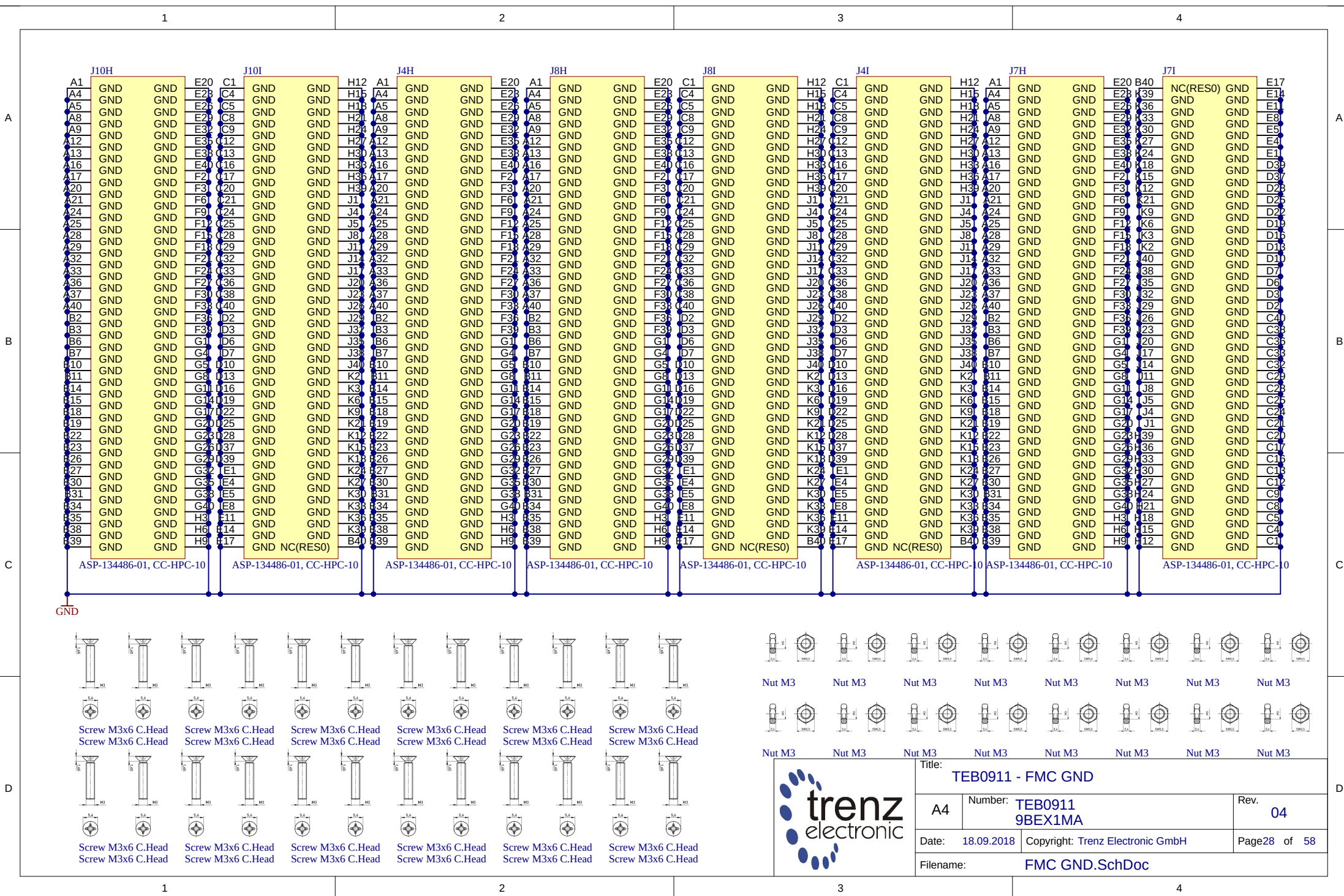
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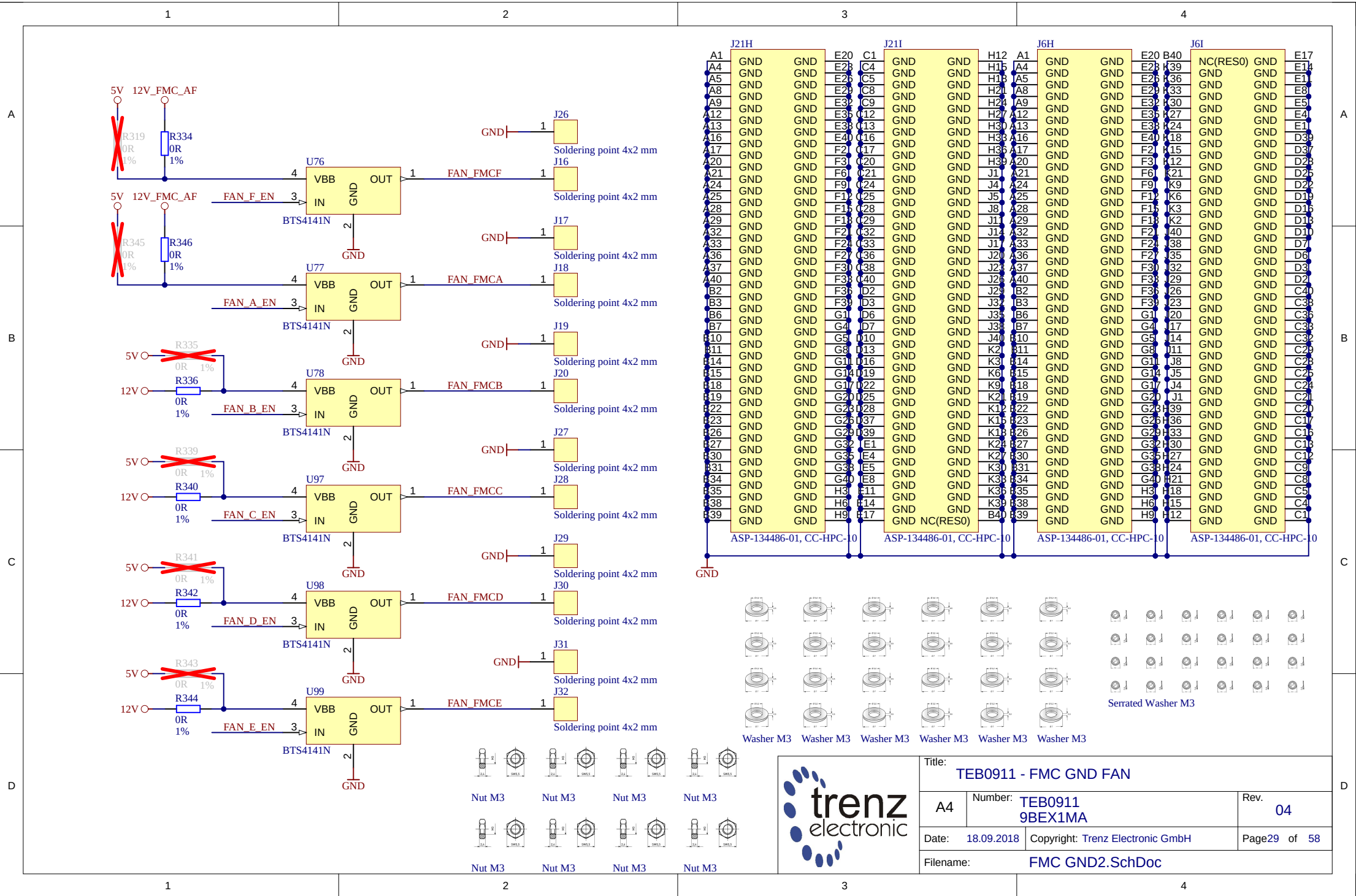
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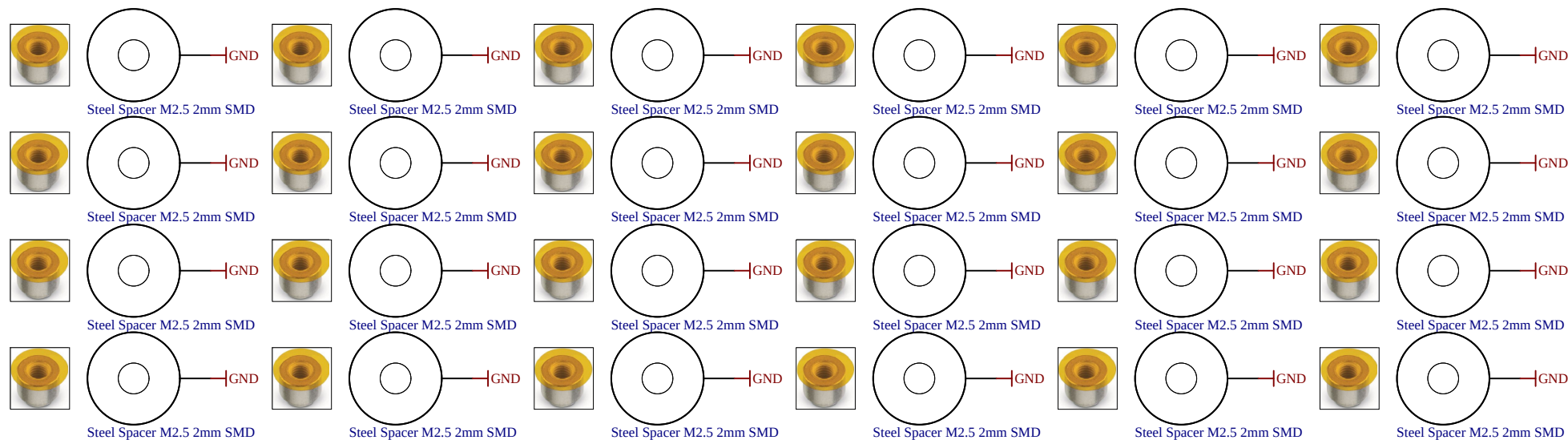
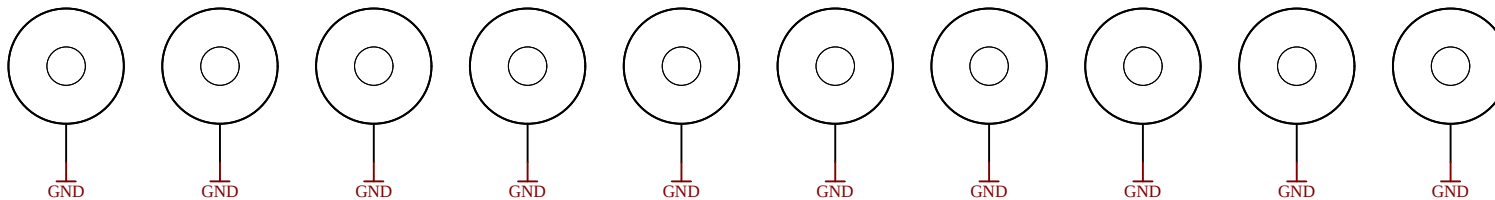
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Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm Mount.Hole 3.2mm



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	A4	Number: TEB0911 9BEX1MA
Date: 18.09.2018	Copyright: Trenz Electronic GmbH	
Filename: FMC_MECH.SchDoc		Rev. 04 Page 30 of 58

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A

A

B

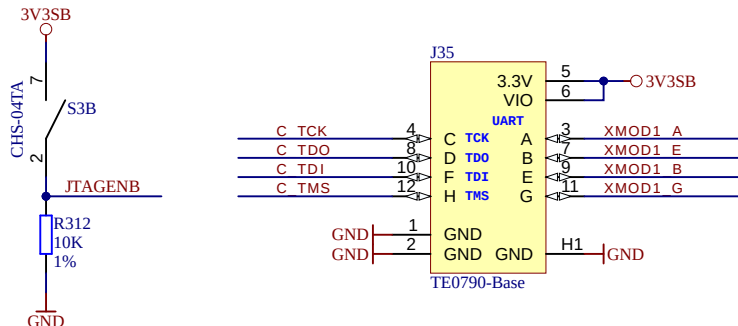
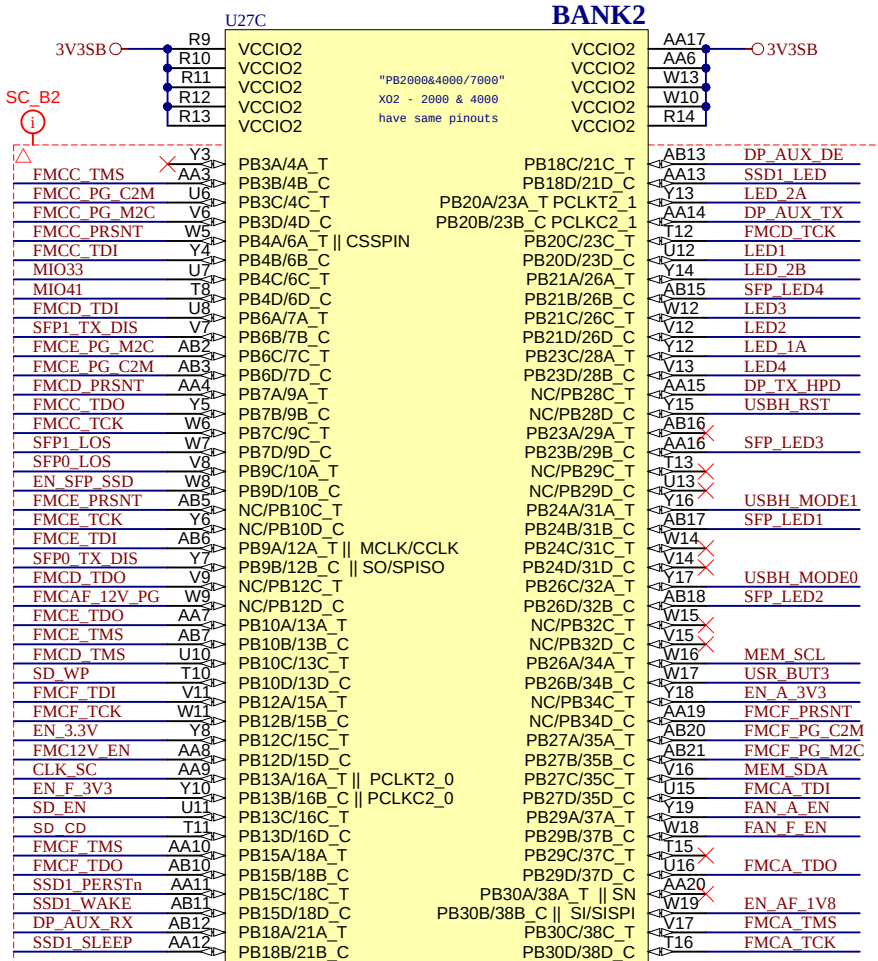
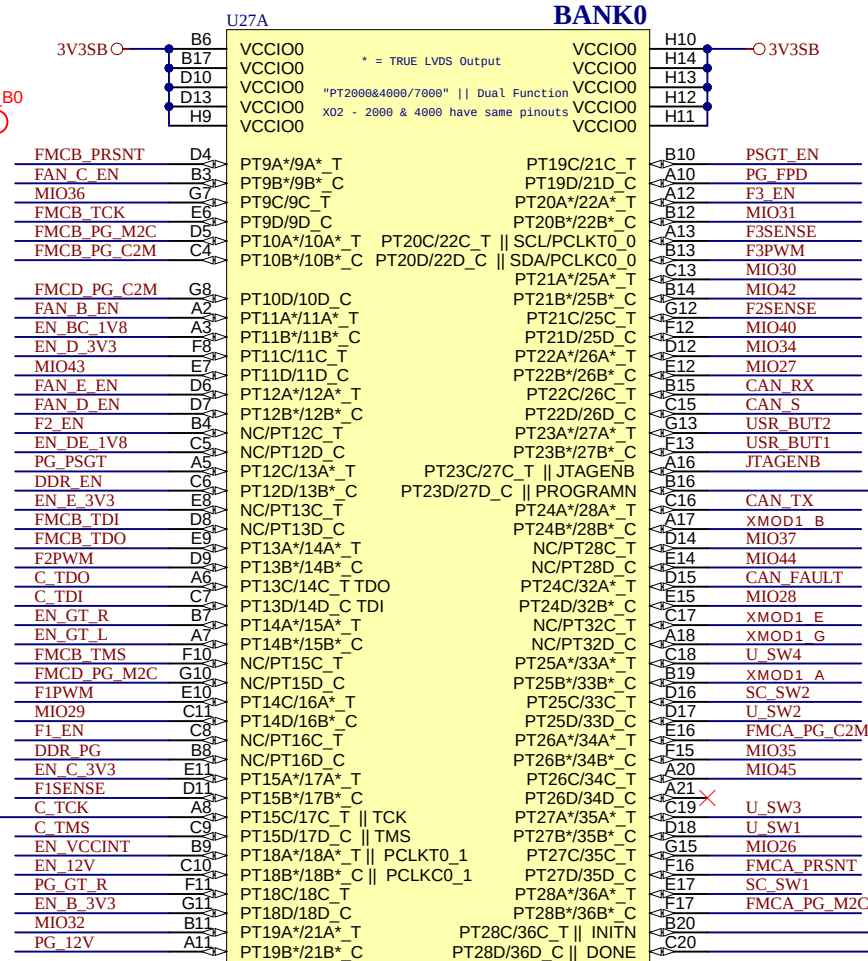
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D

D



Title: TEB0911 - CPLD		
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Date: 18.09.2018	Copyright: Trenz Electronic GmbH	
Filename: SC.schdoc		Page31 of 58

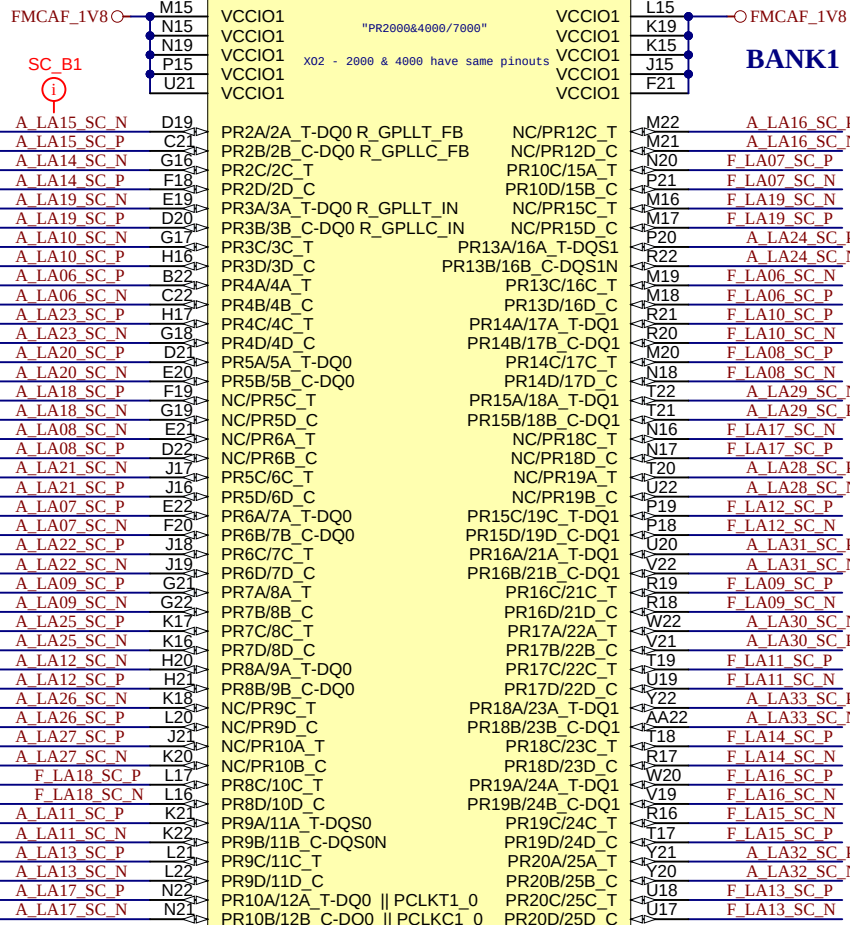
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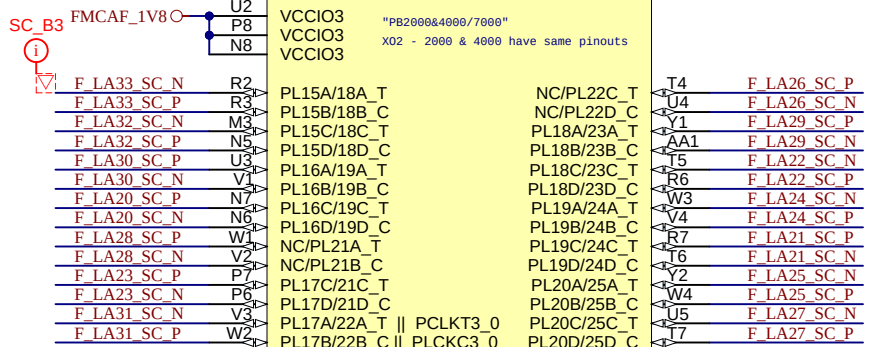
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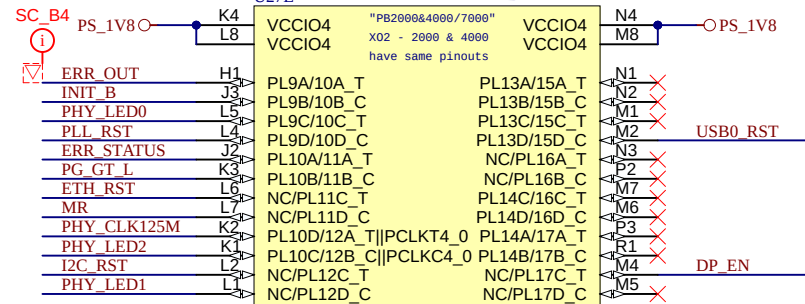
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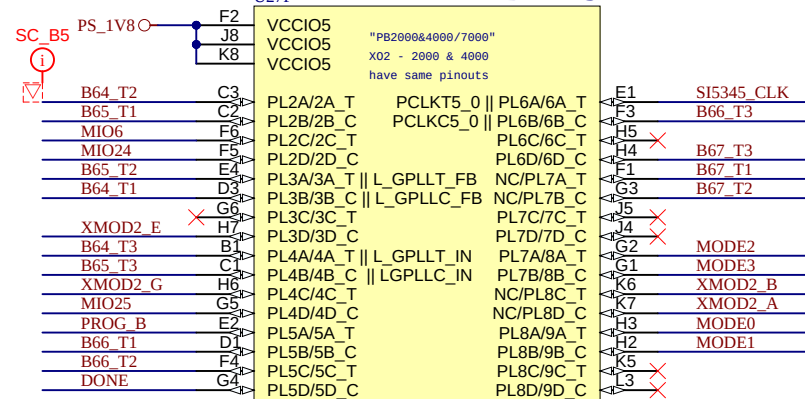
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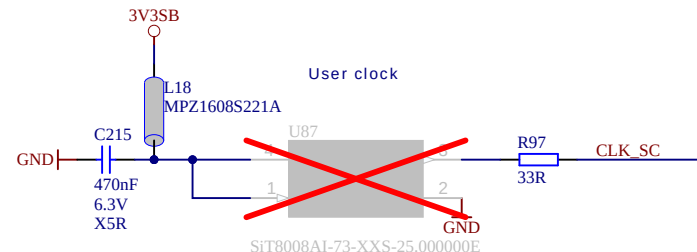
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U27F

BANK5



LCMX02-7000HC-4FG484C



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Date: 18.09.2018	Copyright: Trenz Electronic GmbH		Page 32 of 58
Filename: SC2.schdoc			

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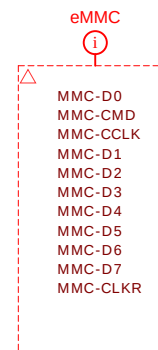
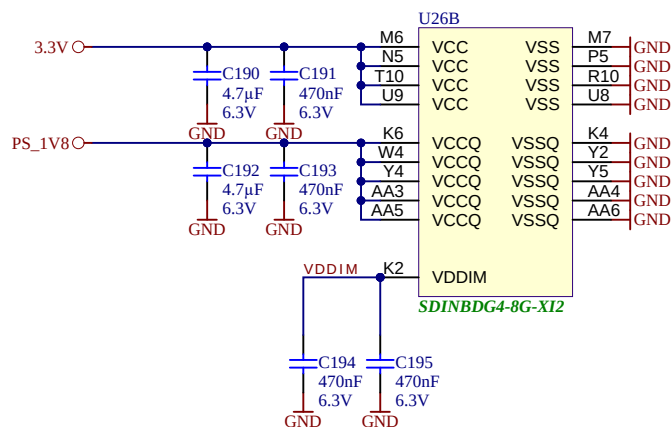
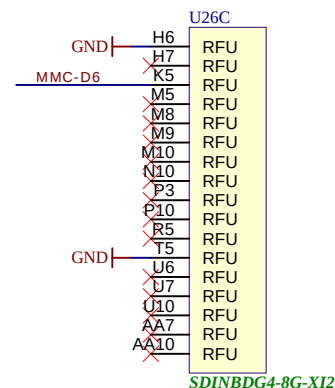
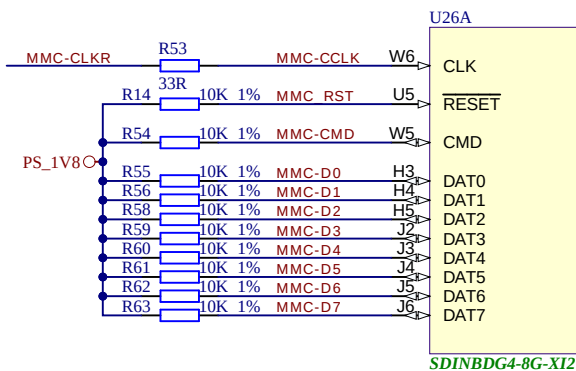
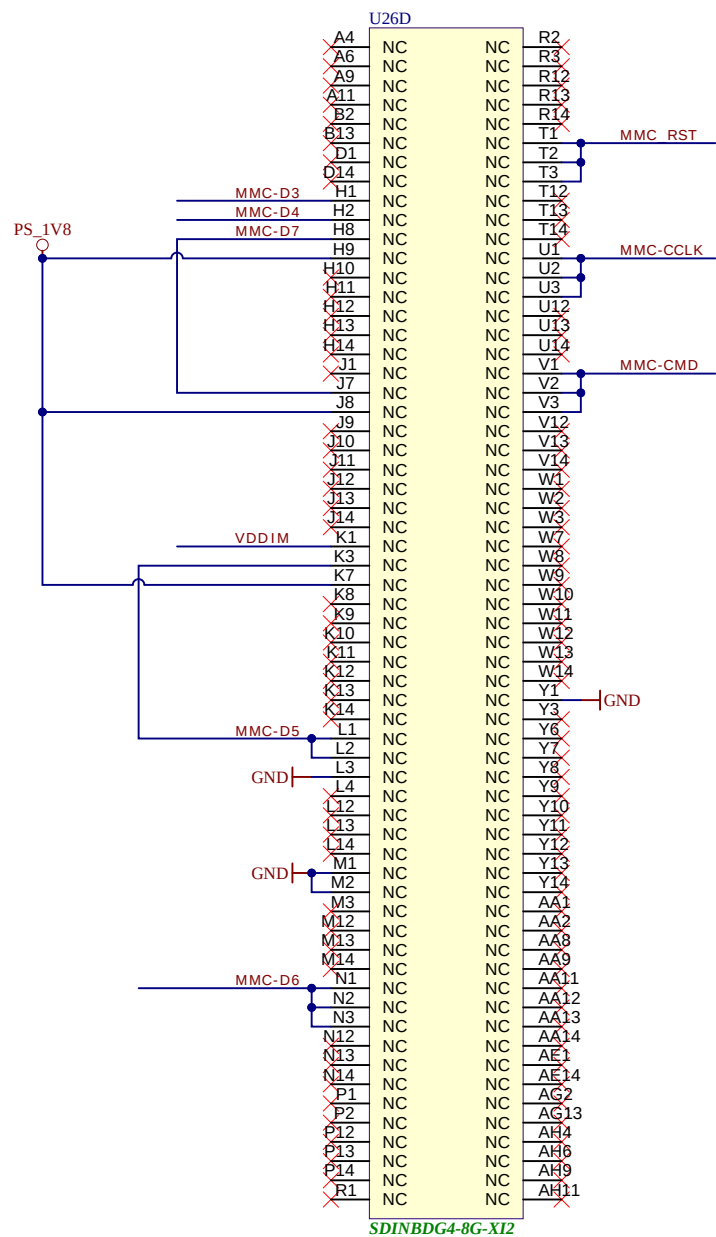
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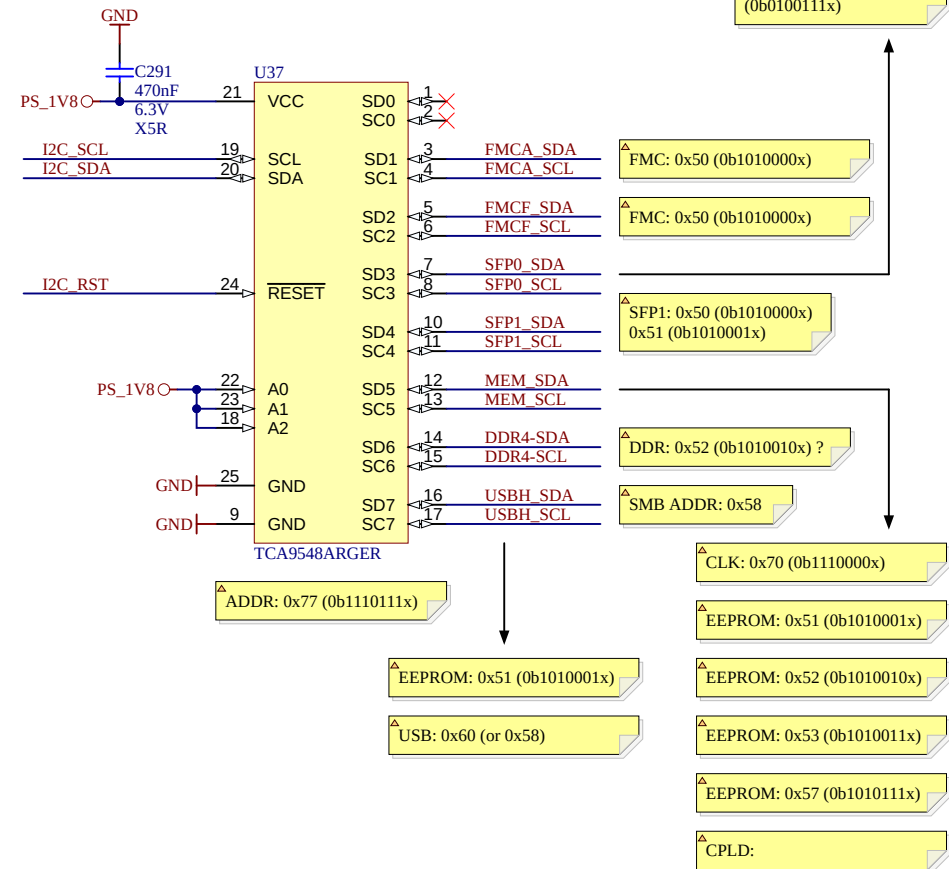
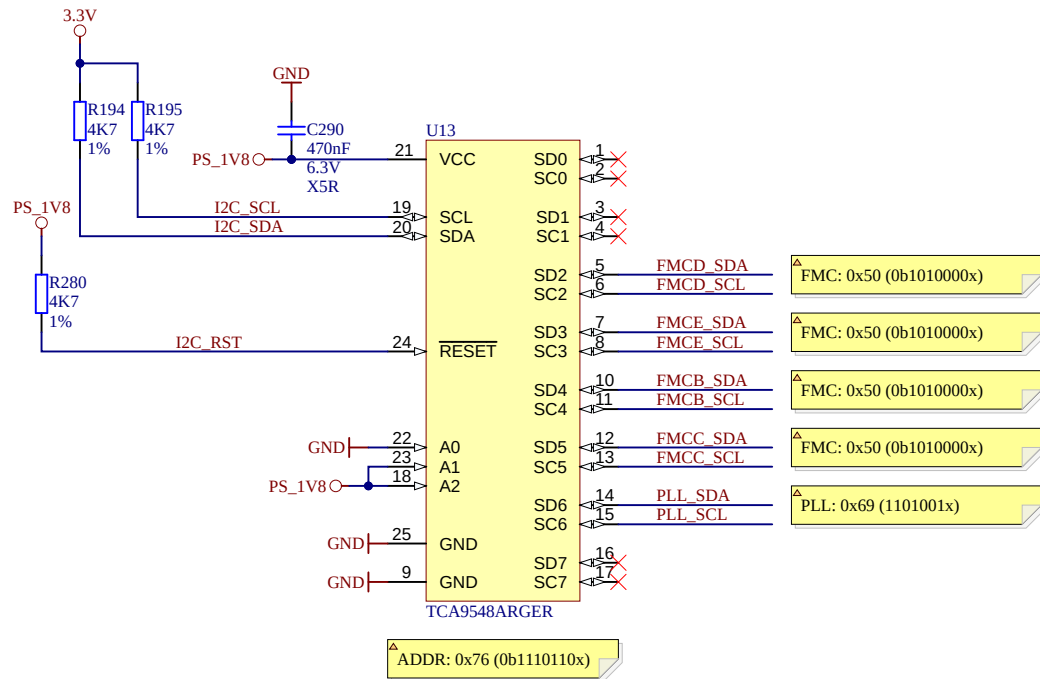
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Filename: eMMC.SchDoc		

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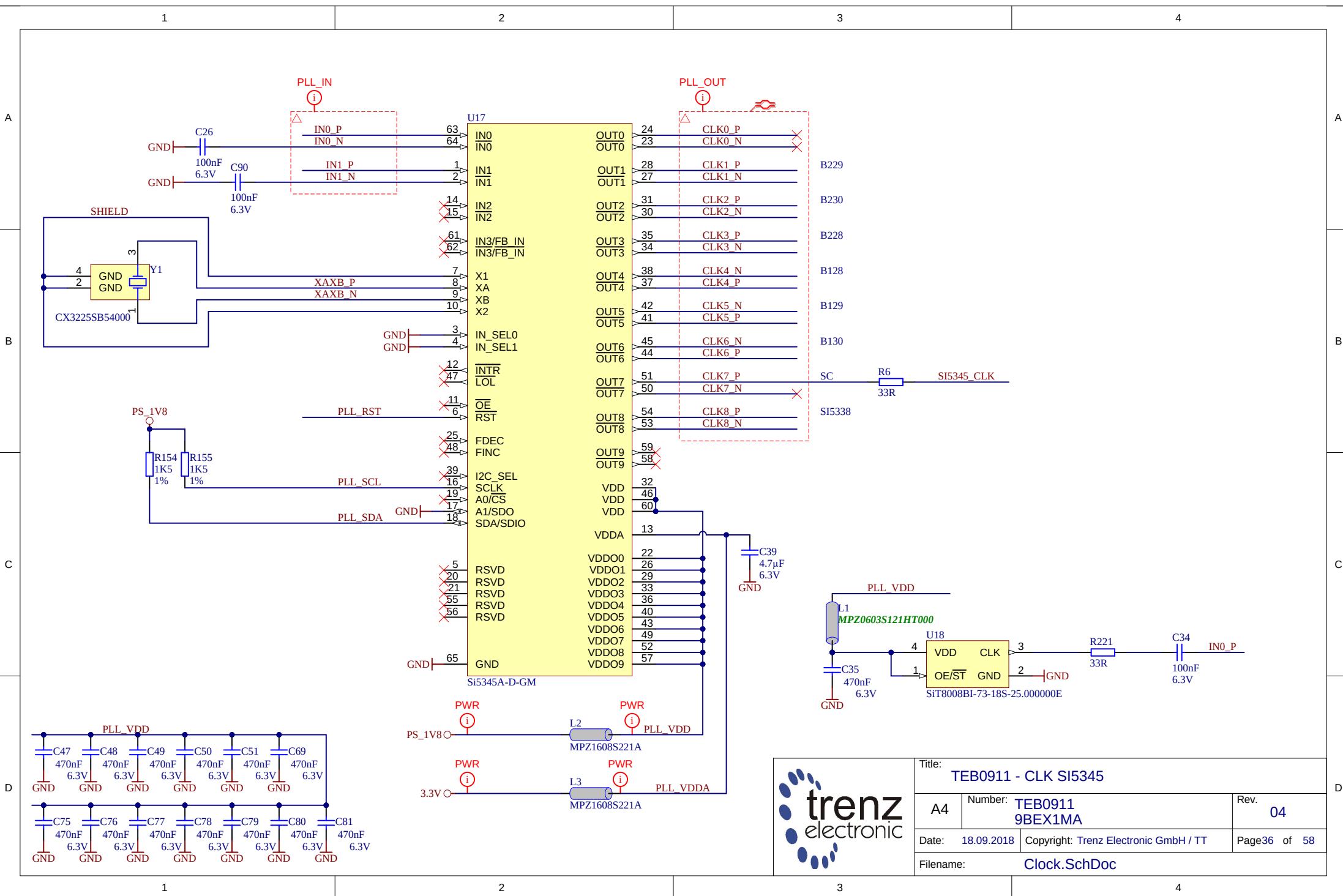
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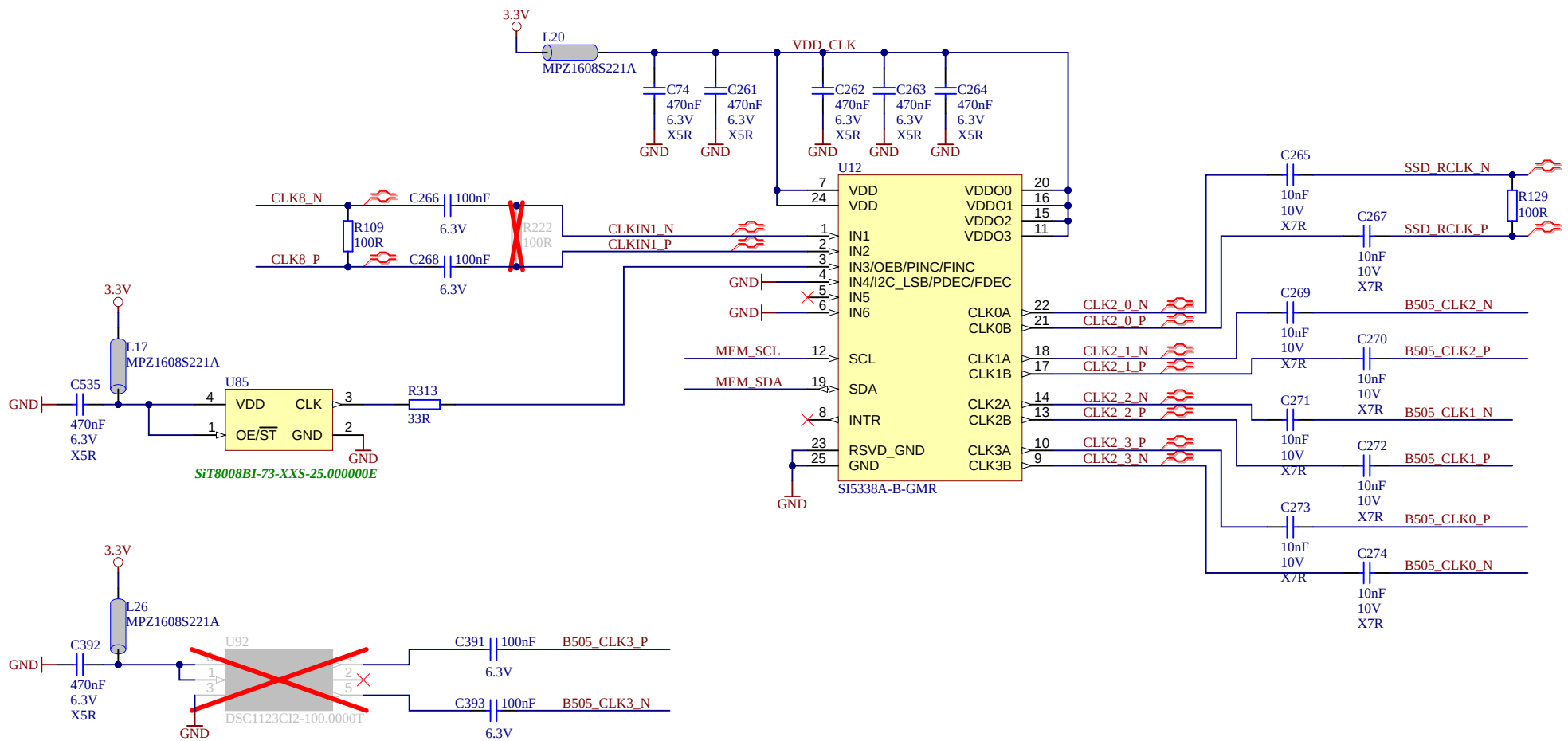
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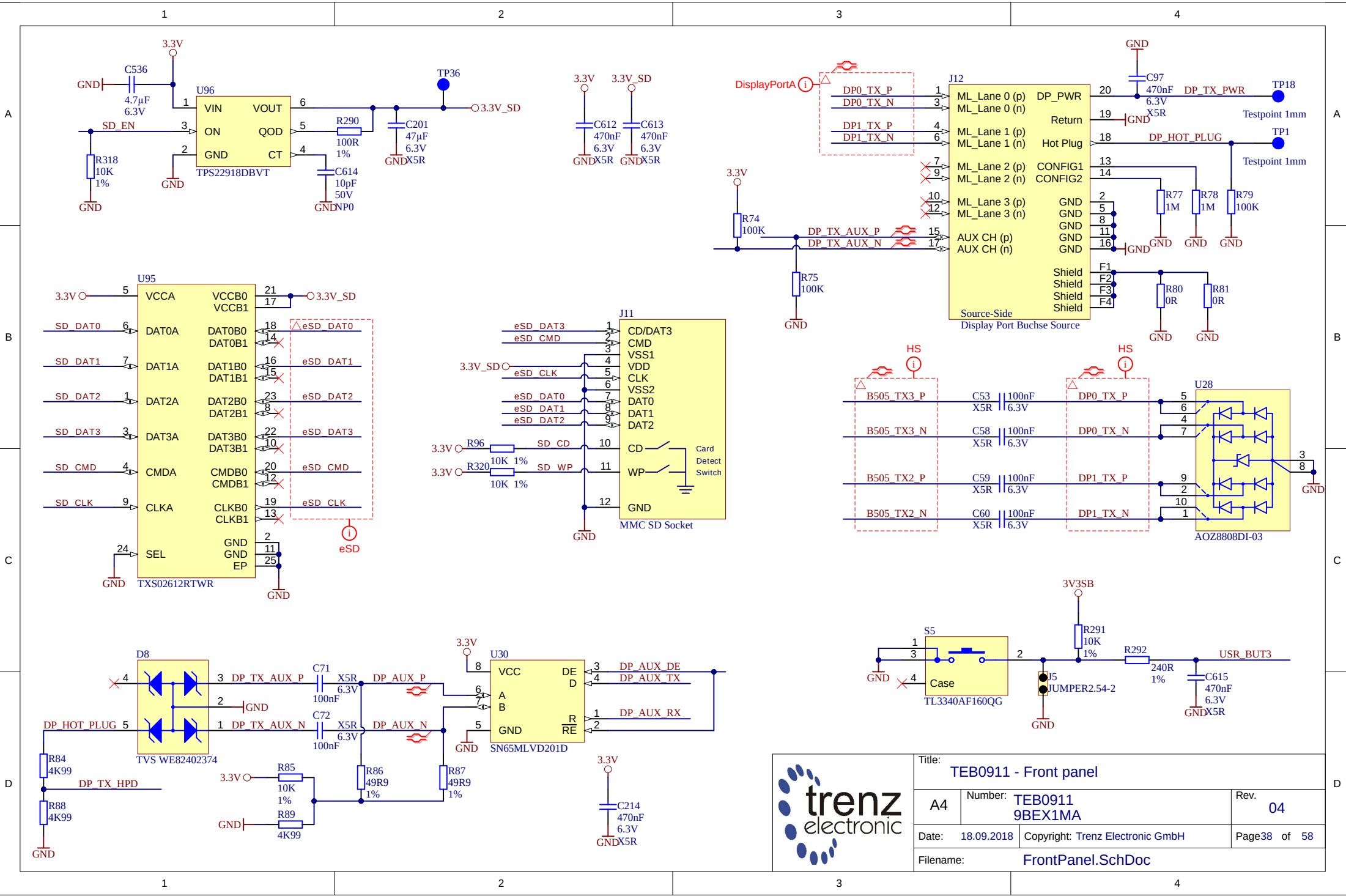


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Date: 18.09.2018	Copyright: Trenz Electronic GmbH		Rev. 04
Filename: I2C_system.schdoc		Page 35 of 58	

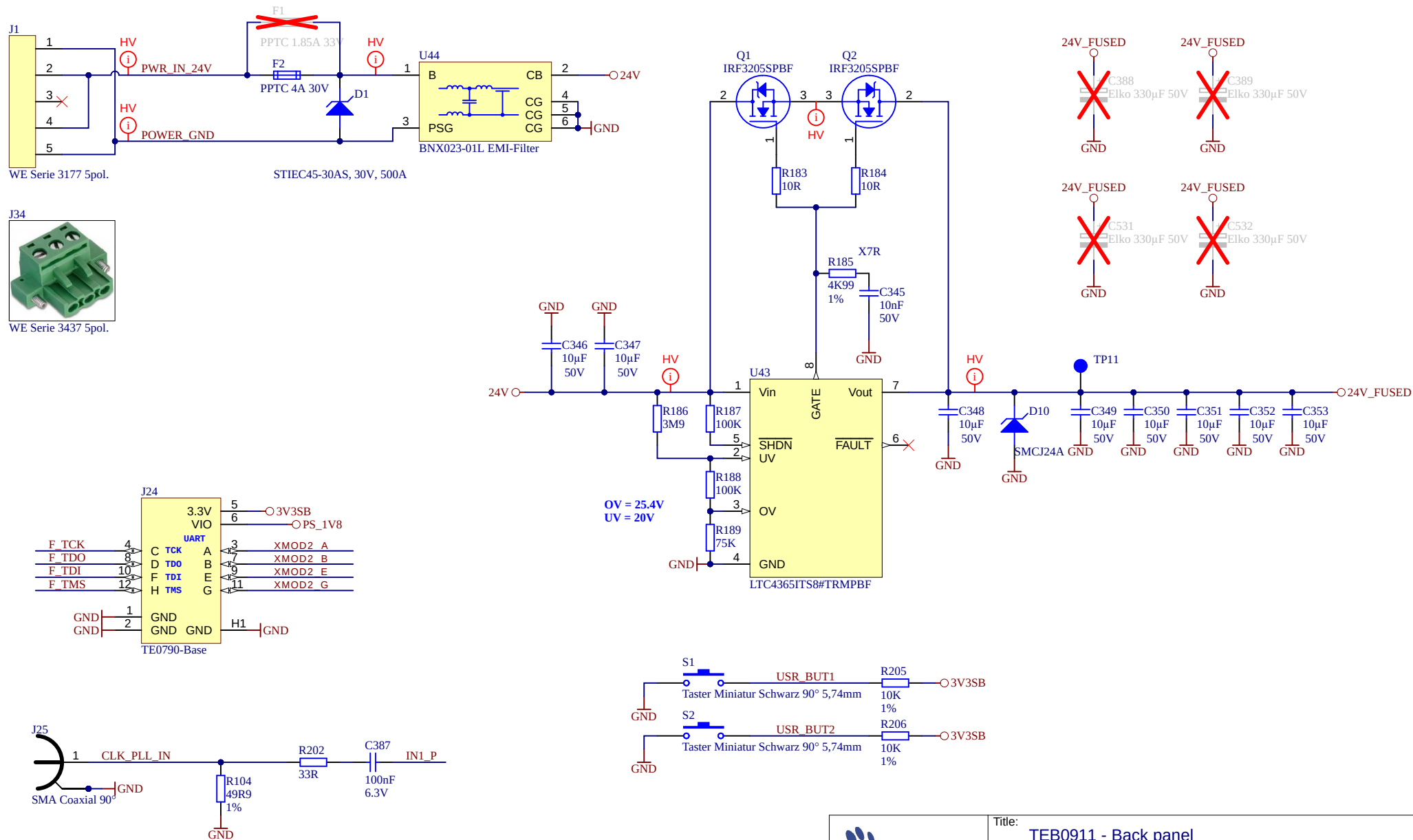




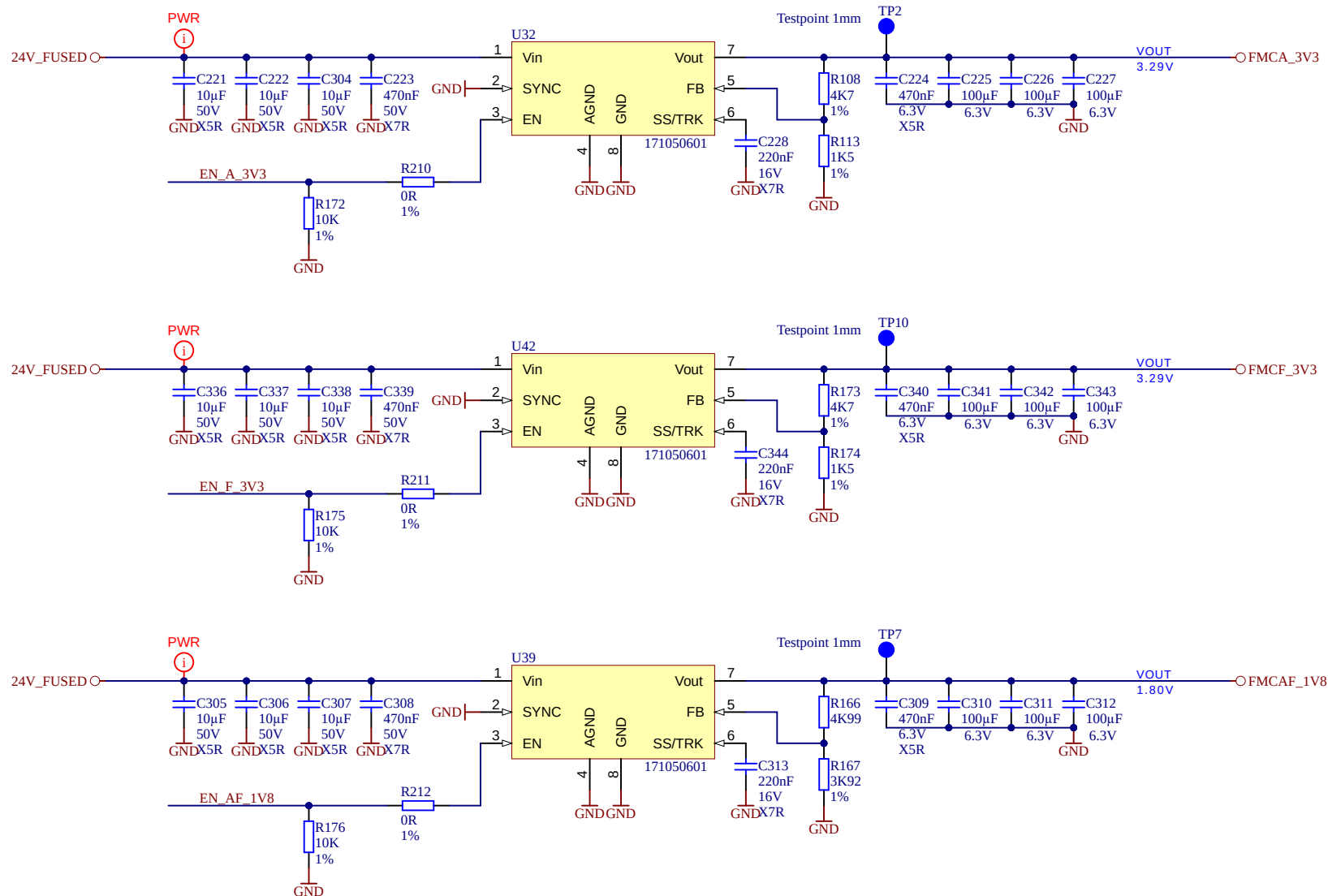
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	Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page37 of 58
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Title: TEB0911 - Front panel		
A4	Number: TEB0911 9BEX1MA	Rev. 04
Date: 18.09.2018	Copyright: Trenz Electronic GmbH	
Filename: FrontPanel.SchDoc		Page38 of 58



	Title: TEB0911 - Back panel		
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	Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page39 of 58
	Filename: BackPanel.SchDoc		



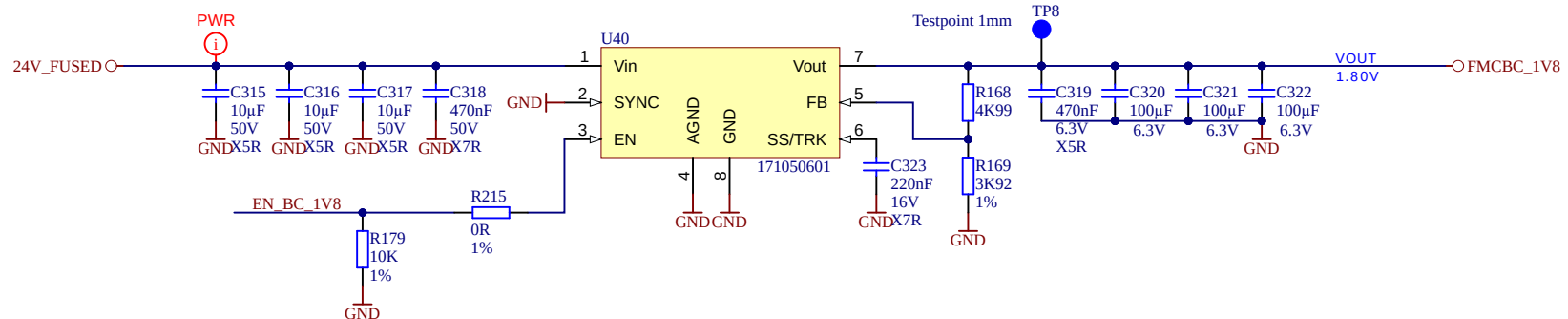
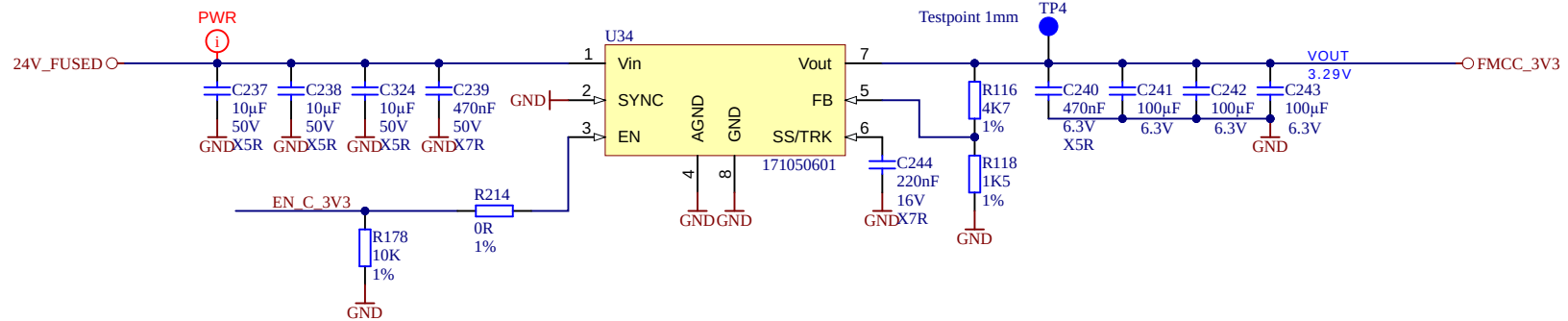
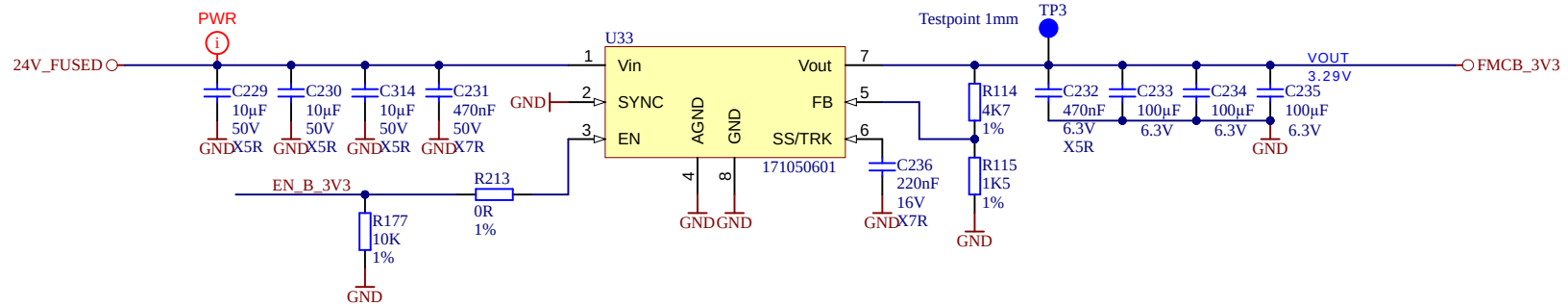
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	A4	Number: TEB0911 9BEX1MA	Rev. 04
	Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page41 of 58
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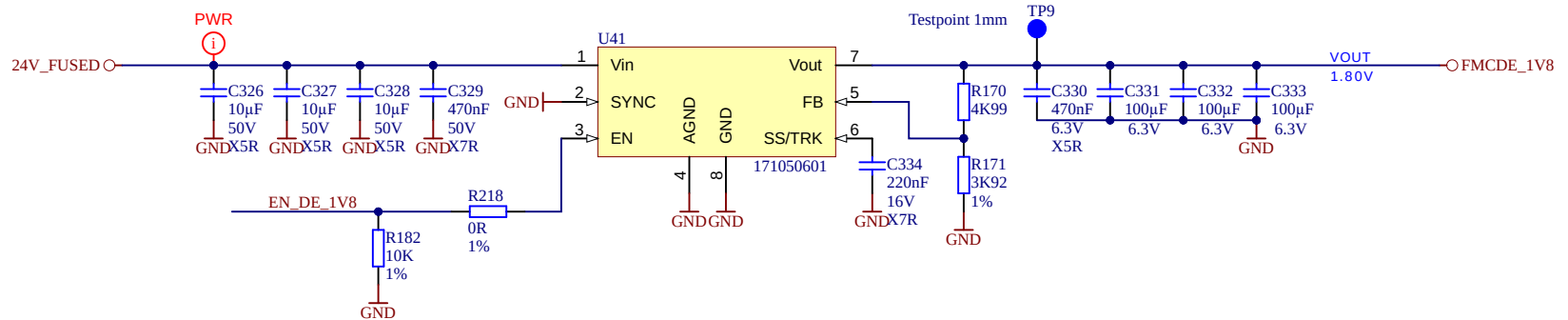
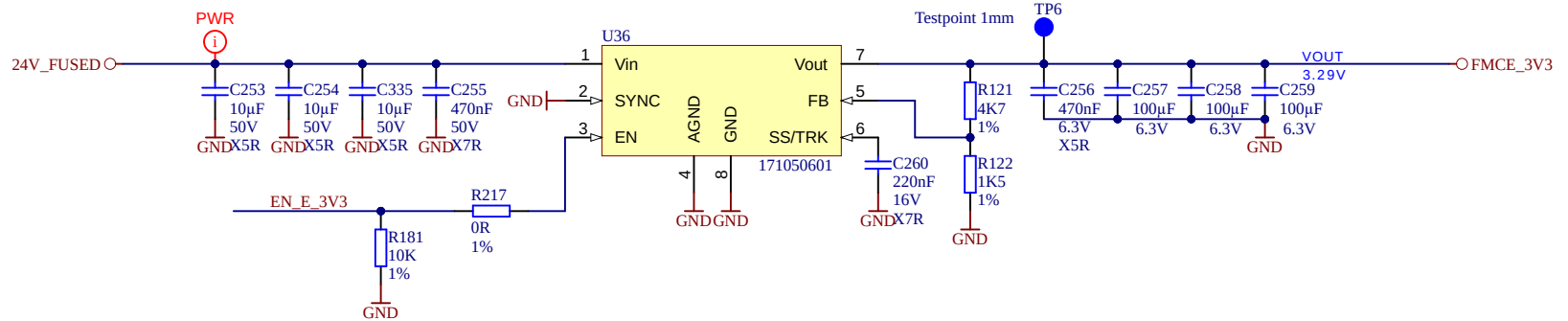
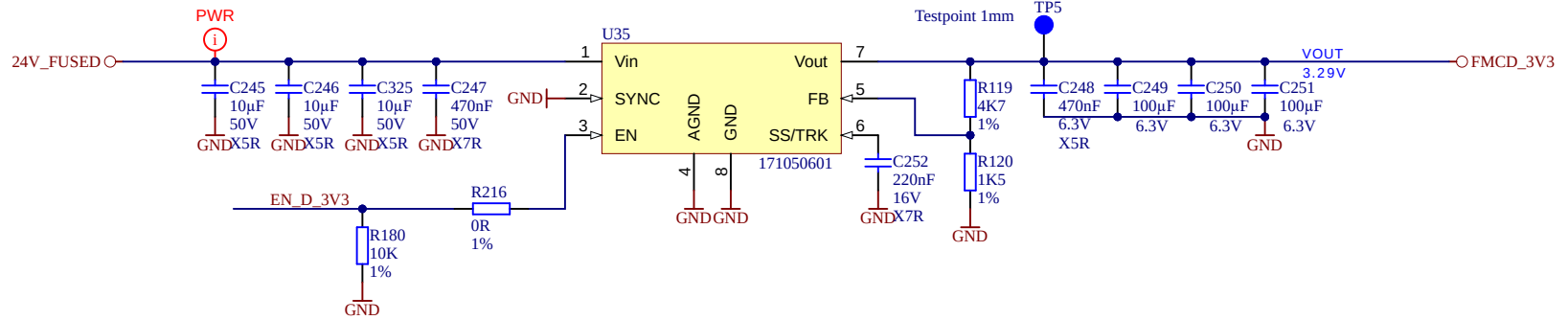
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Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page 42 of 58
Filename: POWER_2.schdoc		

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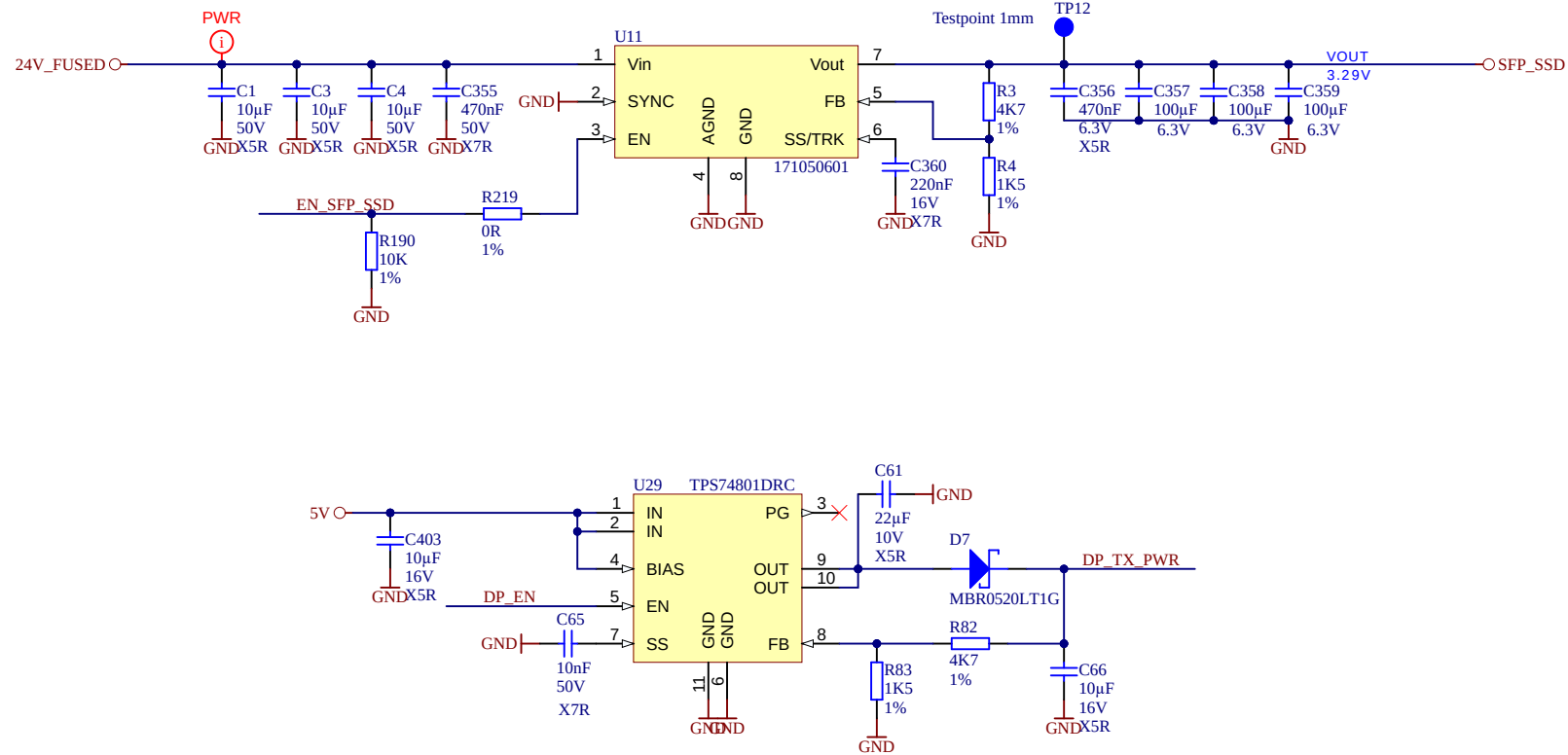
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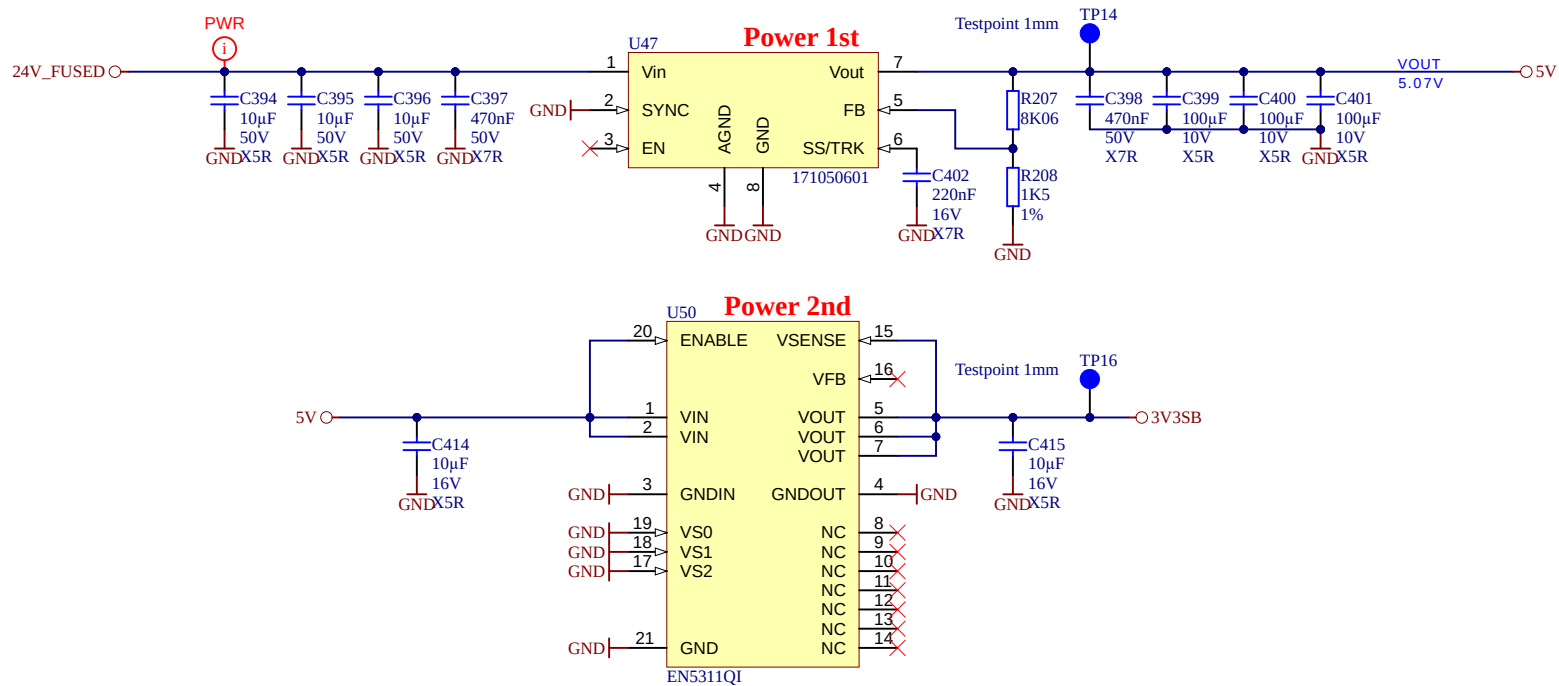
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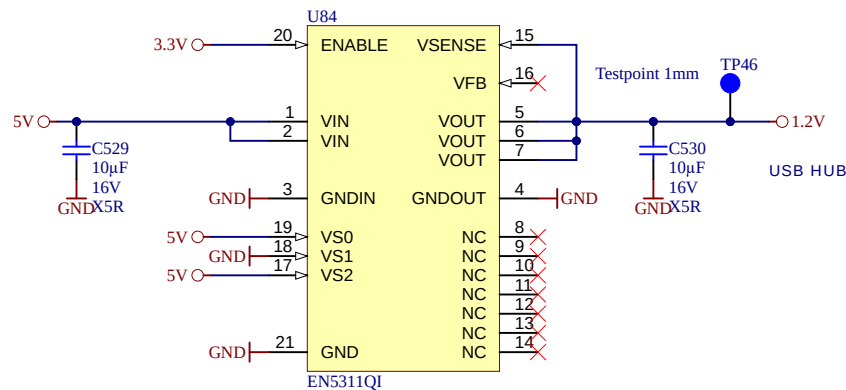
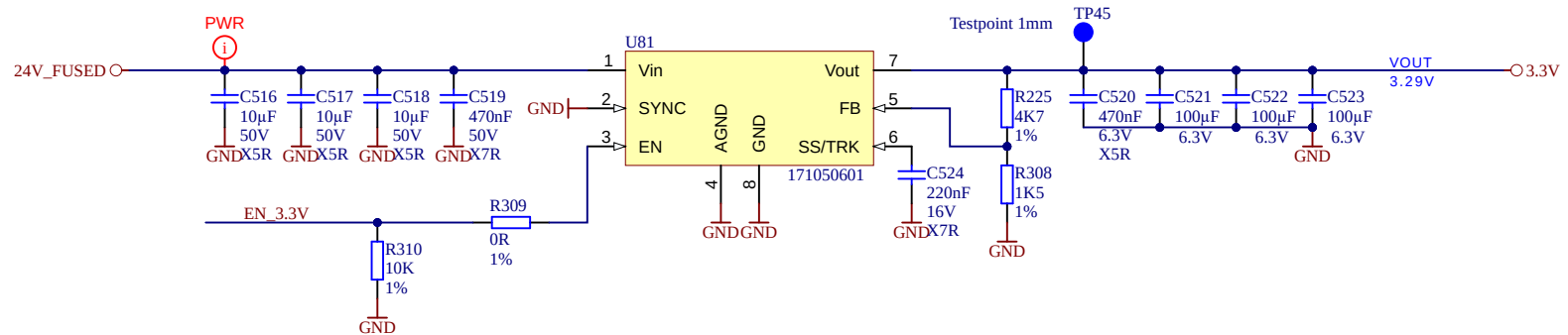
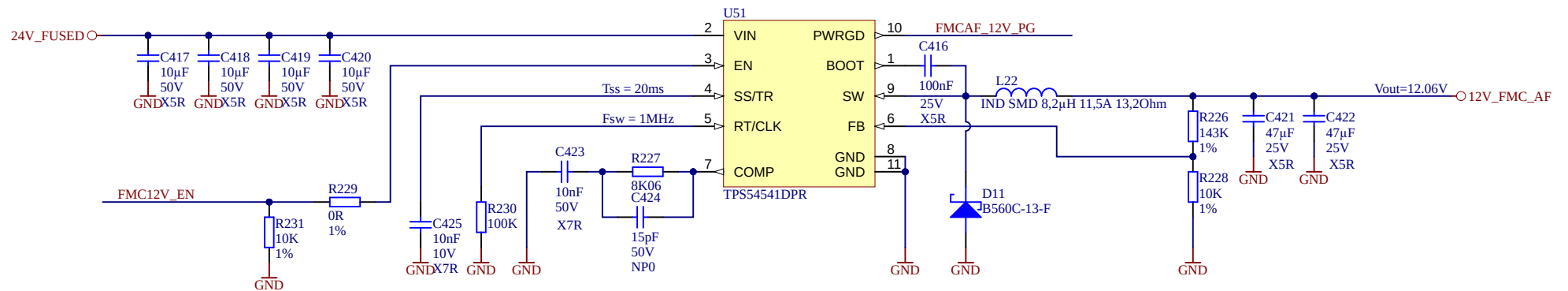
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		Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page43 of 58
		Filename: POWER_3.schdoc		



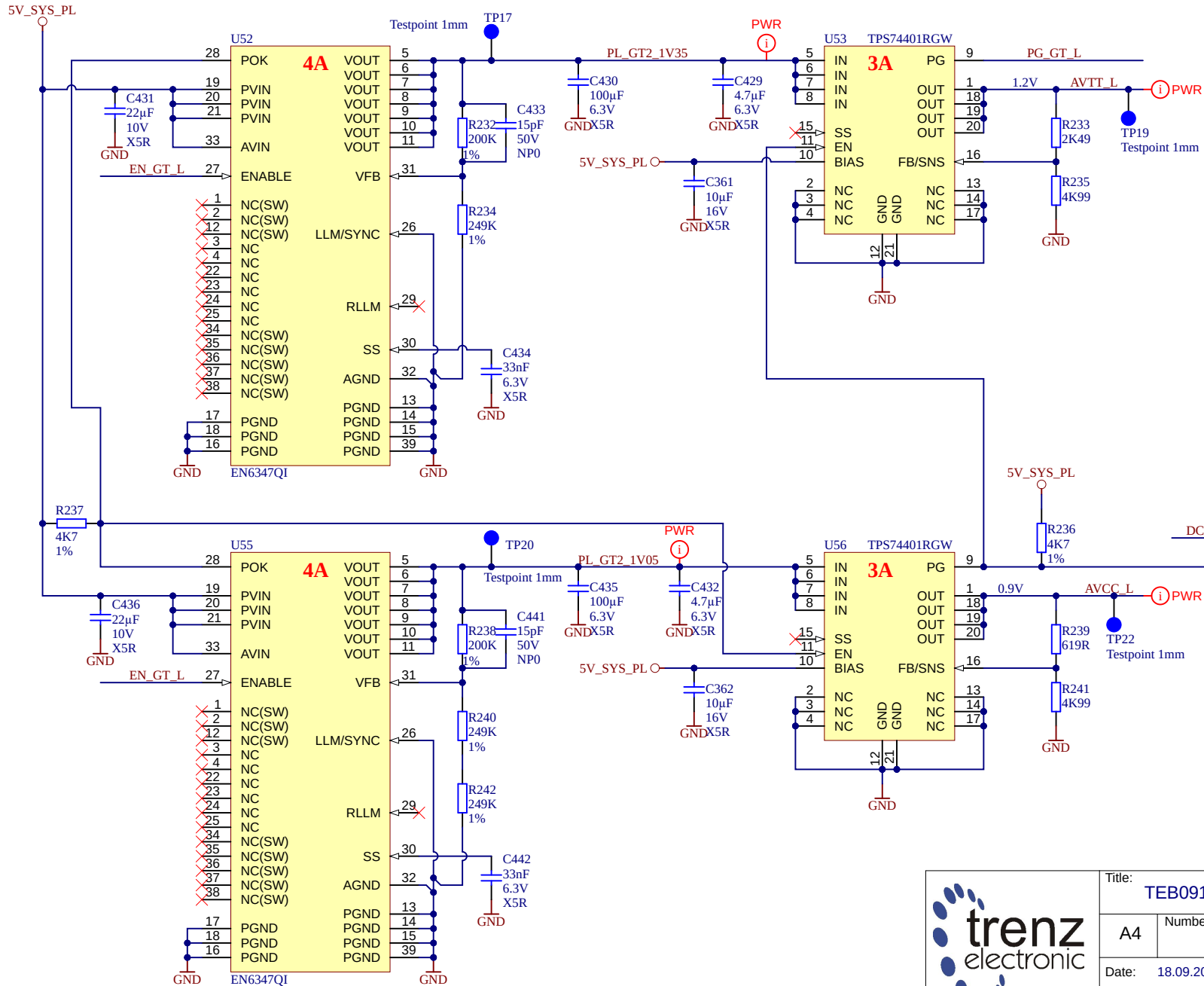
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A4	Number: TEB0911 9BEX1MA			Rev. 04	
Date:	18.09.2018	Copyright: Trenz Electronic GmbH			Page 44 of 58
Filename:		POWER_4.schdoc			



Title: TEB0911 - POWER		
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Date: 18.09.2018	Copyright: Trenz Electronic GmbH	
Filename: POWER_5.schdoc		Page 45 of 58



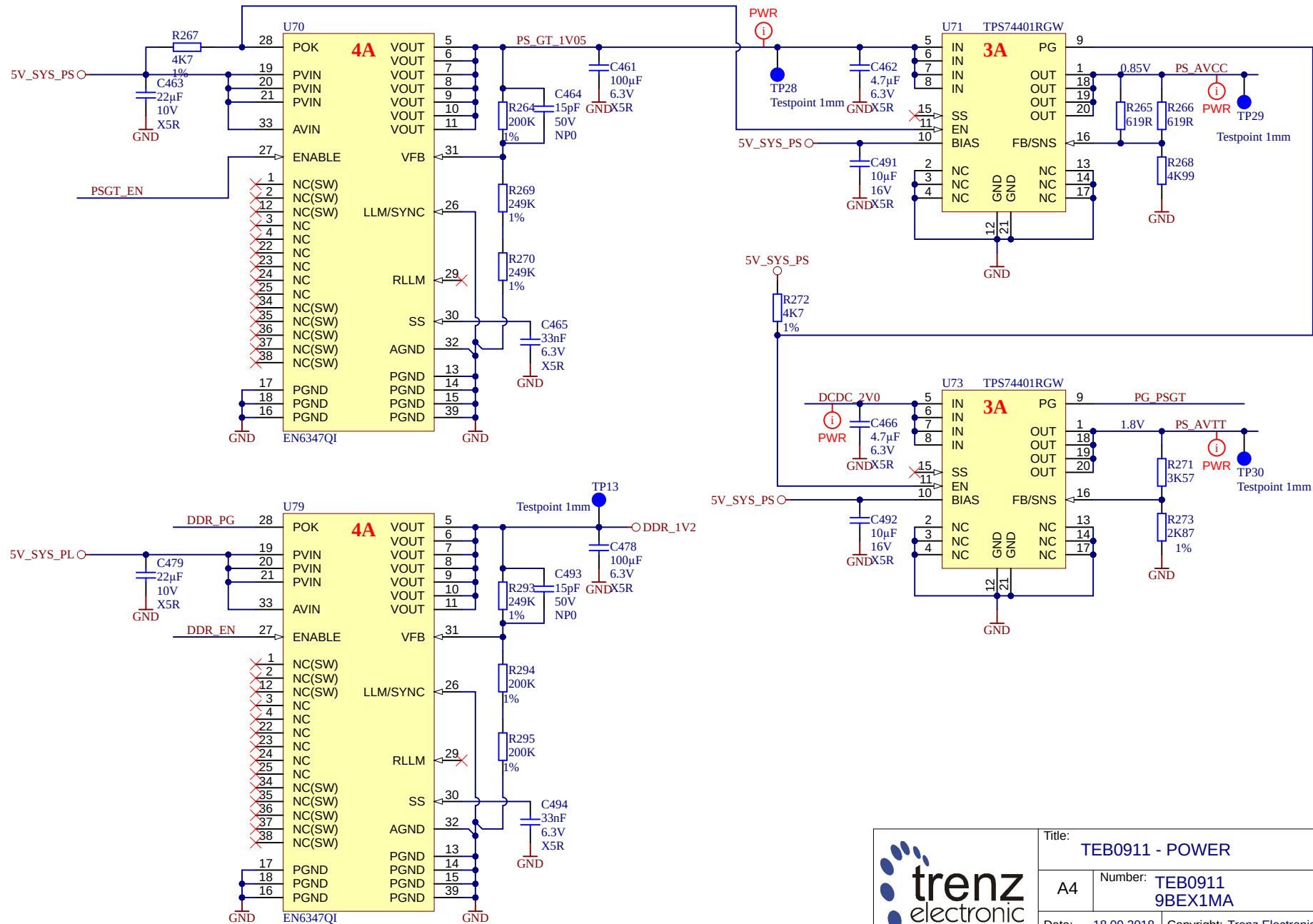
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Date: 18.09.2018		Copyright: Trenz Electronic GmbH	
Filename: POWER_6.SchDoc		Page 46 of 58	



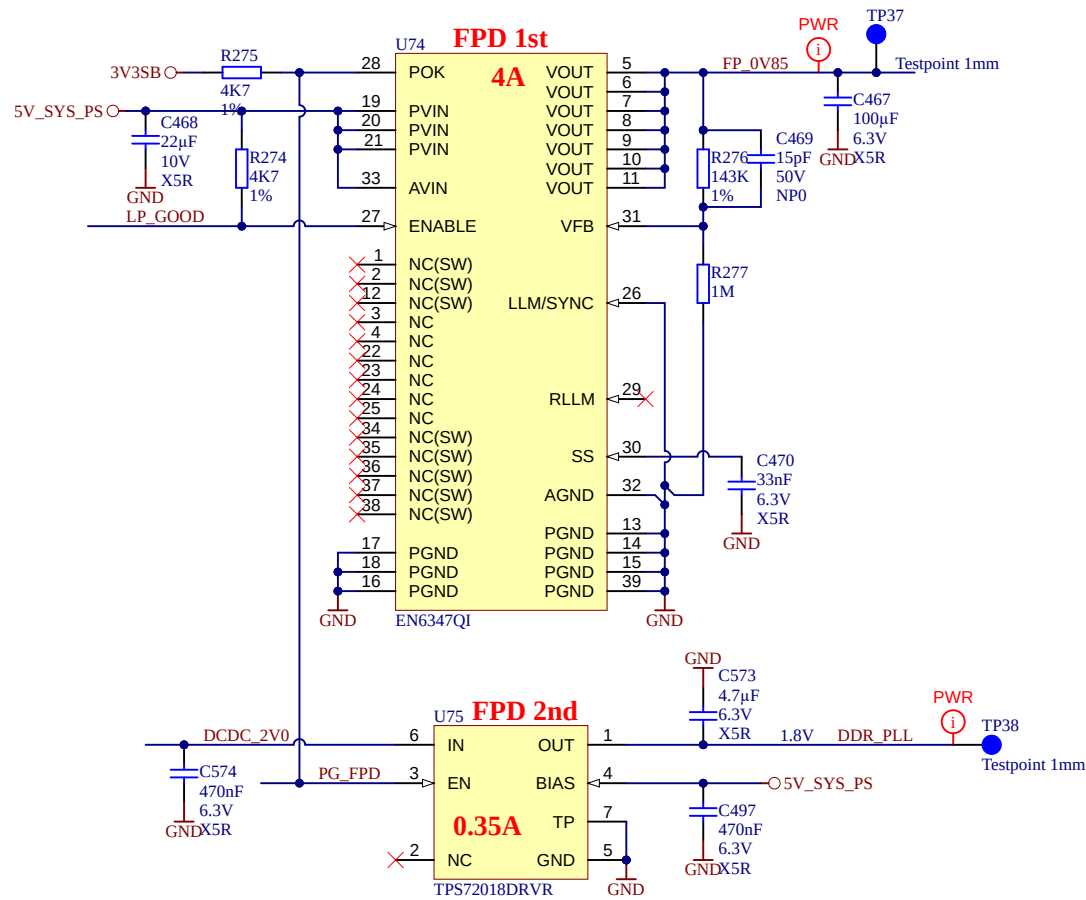
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Date: 18.09.2018	Copyright: Trenz Electronic GmbH / TT	
Filename: POWER_MGTPLL.schdoc		Page 47 of 58



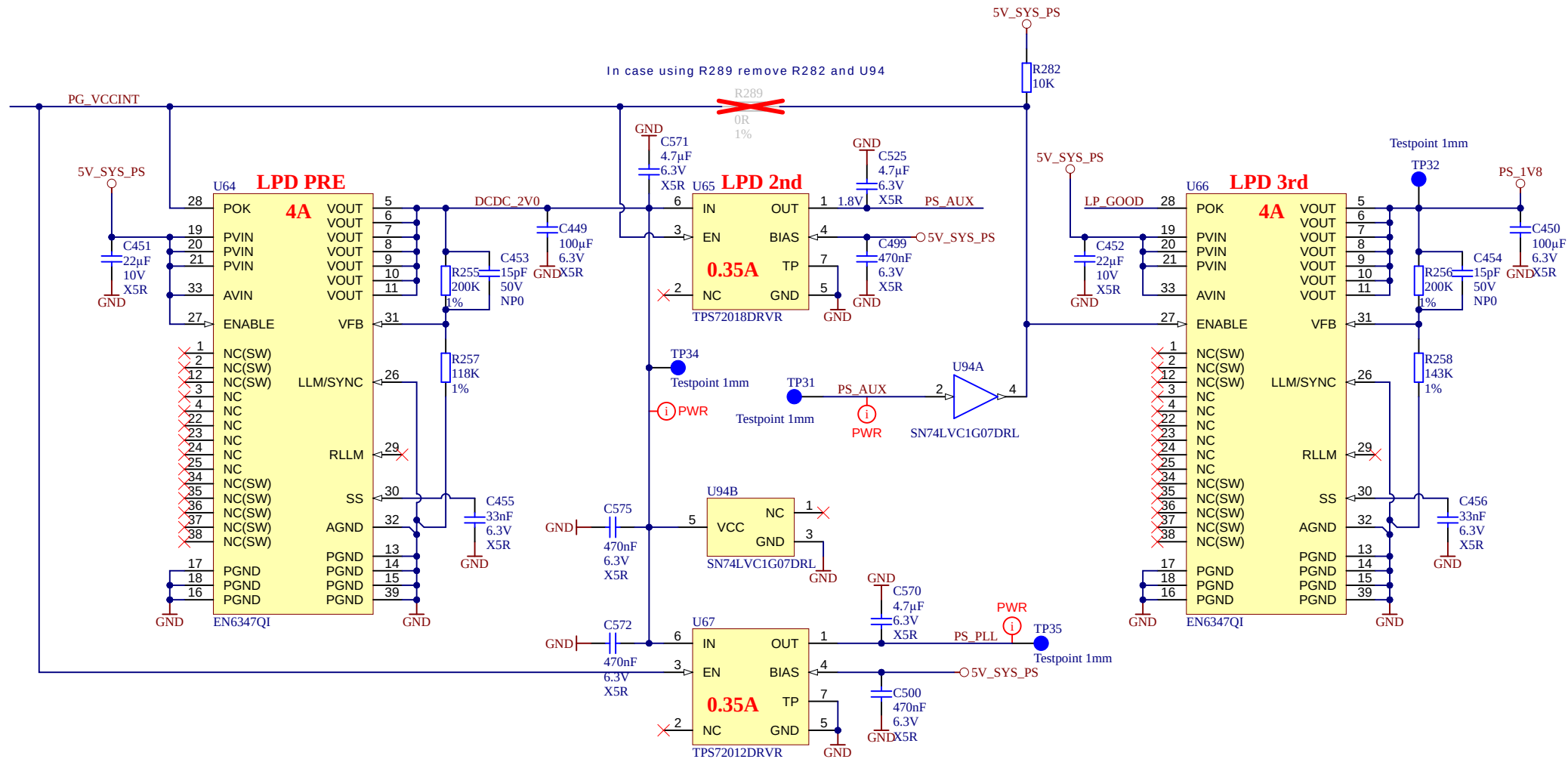
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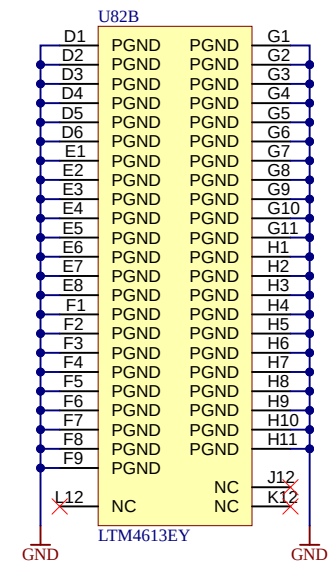
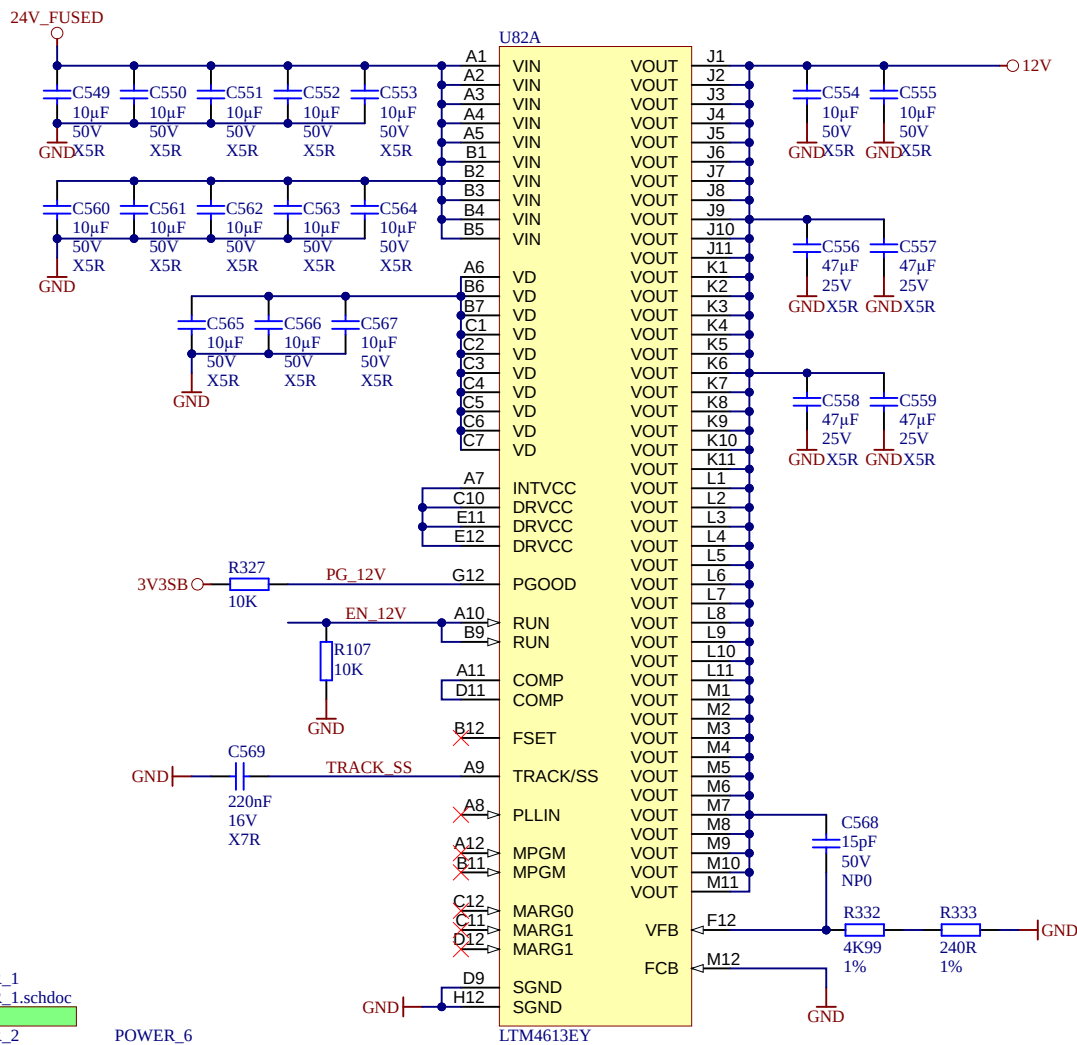
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Date: 18.09.2018	Copyright: Trenz Electronic GmbH / TT	Page 49 of 58
Filename: POWER_MGTPS.schdoc		



		Title: TEB0911 - POWER	
		A4	Rev. 04
Date: 18.09.2018	Copyright: Trenz Electronic GmbH / TT		Page 50 of 58
Filename: POWER_PSFP.schdoc			



	Title: TEB0911 - POWER		
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	Date: 18.09.2018	Copyright: Trenz Electronic GmbH / TT	Page51 of 58
	Filename: POWER_PSLP.schdoc		



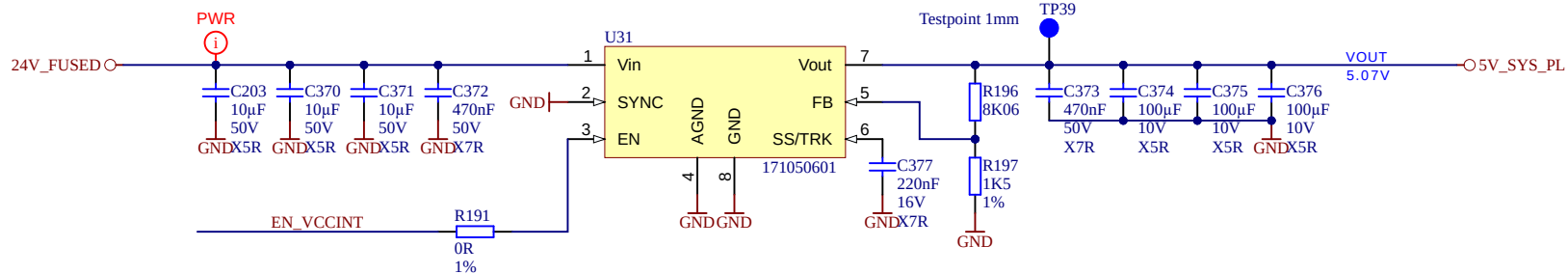
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Date: 18.09.2018	Number: TEB0911 9BEX1MA	Copyright: Trenz Electronic GmbH	Page 53 of 58
Filename: PWR_IN.SchDoc			

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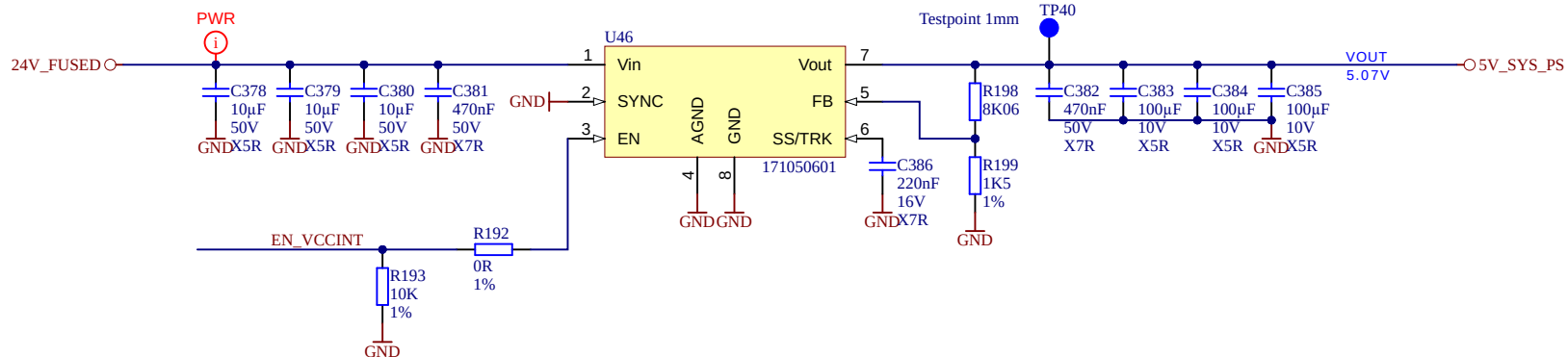
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POWER_MGTPS
POWER_MGTPS.schdoc
POWER_MGTPLR
POWER_MGTPLR.schdoc
POWER_MGTPLL
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POWER_PSF
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POWER_PSLP
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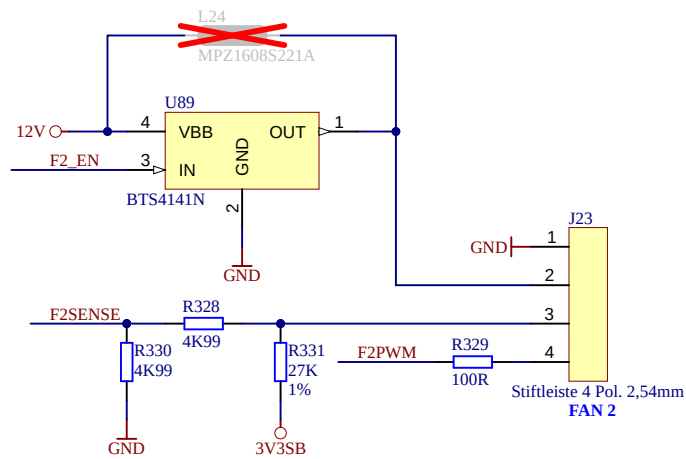
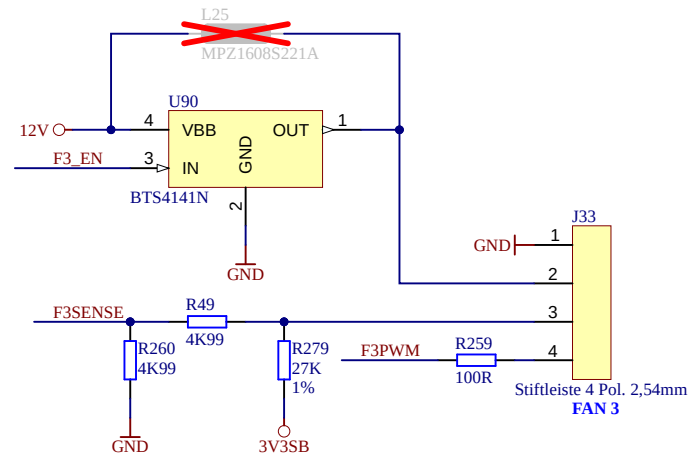
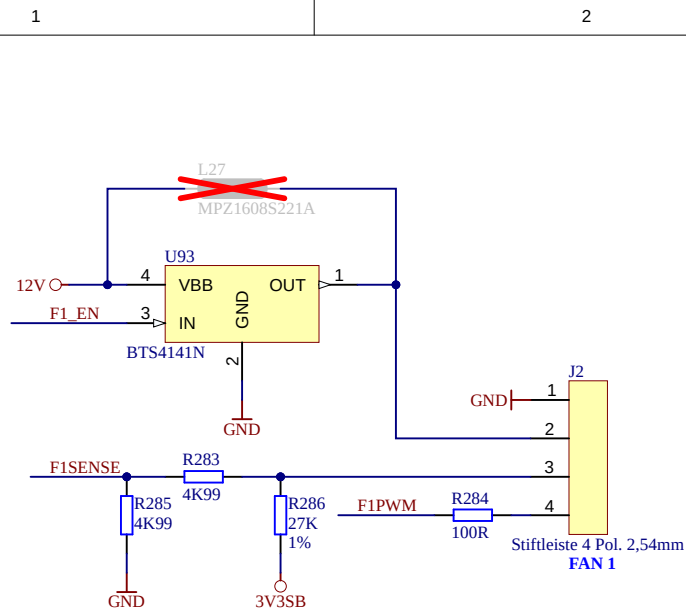
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Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page 54 of 58
Filename: PWR_SYS.SchDoc		

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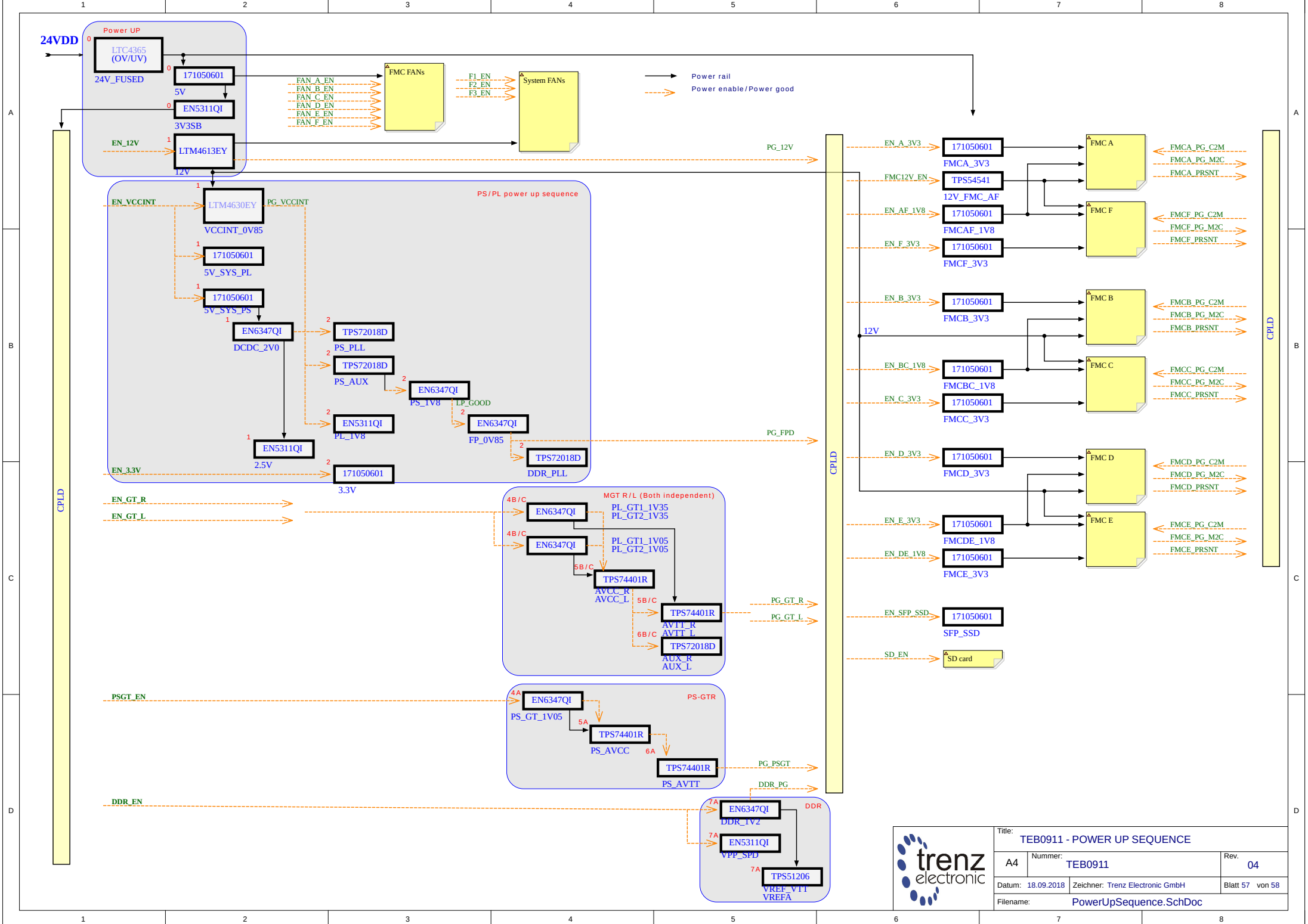
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	A4	Number: TEB0911 9BEX1MA	Rev. 04
	Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page55 of 58
	Filename: FAN_CON.schdoc		



Title: TEB0911 - POWER UP SEQUENCE		
A4	Number: TEB0911	Rev. 04
Datum: 18.09.2018	Zeichner: Trenz Electronic GmbH	Blatt 57 von 58
Filename: PowerUpSequence.SchDoc		

1	2	3	4												
A	REV02:														
	1. 2 CPLDs replaced with LCMXO2-7000HC-4FG484C (U27) 2. Single SFP connector removed. MGT lanes B129_TX/RX_1 connected to FMC F 3. DCDC U45 (power supply of 2xSFP and 1xSFP connectors) removed. 2xSFP powered by U11. 4. HDMI: lane B505_TX2 connected to DP1_TX and lane B505_TX3 to DP0_TX. 5. Supervisor U69 TPS3106E33 replaced with TPS3106E09 6. U17 (SI5345): IN_SEL0/1 disconnected from CPLD and connected to GND 7. Added new clock source U87 (SiT8008AI-73-XXS-25.000000E). Connected to CPLD. Not fitted 8. I2C pull-ups R154/R155: changed from 4.7k to 1.5kOhm 9. FAN: R285, R330, R260 changed to 27K (F*SENSE) 10. Added fuse F2 (4A). Fuse F1 (1.85A) "not fitted" 11. C354 (PWR_IN_24V/GND) deleted 12. Removed clock loop-back OUT9P/N - IN2P/N (U17, SI5345) 13. OUT7 (P) of U17 (clock source SI5345) connected to CPLD U27 Bank 5. 14. Signal SD Card detect SD_CD connected to CPLD, MIO45 connected to CPLD 15. Signals FMC*_PG_C2M(M2C) pulled-up to 3V3SB (was FMC*_3V3) 16. CLK_OUT U15 (USB PHY), serial resistor value R13 changed from 33 to 0 Ohm 17. EN5311QI: Cout value changed from 22uF to 10uF 18. BOM optimization 19. Added connections from CPLD to FMC A and F (x_LA06_SC - x_LA33_SC) 20. Net EN_VCCINT connected also to DCDC U31 (5V_SYS_PL) and U46 (5V_SYS_PS) 21. Added 2 2kb EEPROM (U45/U43) 22. Added power-up sequencing 23. MIO6 connected to CPLD 24. Signals EN_SFP was renamed to EN_SFP_SSD, 12V_FMC was renamed to 12V_FMC_AF, FMC12V_PG to FMCAF_12V_PG 25. Nets names F_LA02/03/04 was swapped.														
	REV03:														
B	1. Signals C_LA27_P/N swapped: C_LA27_P connected to pin R10 (IO_L11P_T1U_N8_GC_67), C_LA27_N connected to R9 (IO_L11N_T1U_N9_GC_67) of U1 (XCZU9EG-1FFVB1156)														
	REV03a (17.11.2017):														
	1. Flash N25Q256A11E1240E changed to N25Q512A11G1240E.														
C	REV04:														
	1. Added SDIO level translator TXS02612RTWR (U95) and 33Ω series resistors on SDIO lines from FPGA side. 2. Added user button S5 and jumper J5 (connected to U27 pin W17). 3. New FAN switches U76-78/U97-99: BTS4141N. 4. New FANs: AFB04512MA. Was added an option to use 5V or 12V (default) power rail. 5. Replaced EOL components: - TPS27082LDDCR -> TPS22918DBVT (U96) - ESD3V3U4ULCE6327XTSA1 -> AOZ8808DI-03 (U28) 6. New SFP+ connector: 2007492-5 instead of 1-2007492-5														
	REV04a (30.08.2019):														
D	1. Resistor R65 (USB VBUS) replaced by 1K.														
	 <table><tr><td colspan="3">Title: TEB0911 - REVISION CHANGES</td></tr><tr><td>A4</td><td>Number: TEB0911 9BEX1MA</td><td>Rev. 04</td></tr><tr><td>Date: 18.09.2018</td><td>Copyright: Trenz Electronic GmbH</td><td>Page58 of 58</td></tr><tr><td colspan="3">Filename: Revision_changes.SchDoc</td></tr></table>			Title: TEB0911 - REVISION CHANGES			A4	Number: TEB0911 9BEX1MA	Rev. 04	Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page58 of 58	Filename: Revision_changes.SchDoc		
	Title: TEB0911 - REVISION CHANGES														
A4	Number: TEB0911 9BEX1MA	Rev. 04													
Date: 18.09.2018	Copyright: Trenz Electronic GmbH	Page58 of 58													
Filename: Revision_changes.SchDoc															
1	2	3	4												